

Ferroelectrics and Negative Capacitance:

What do we know and
What do we need to know?

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Funding Agencies

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STARNET LEAST



NSF E3S Center



FCRP MSD Center



Georgia Tech
Start-up funds



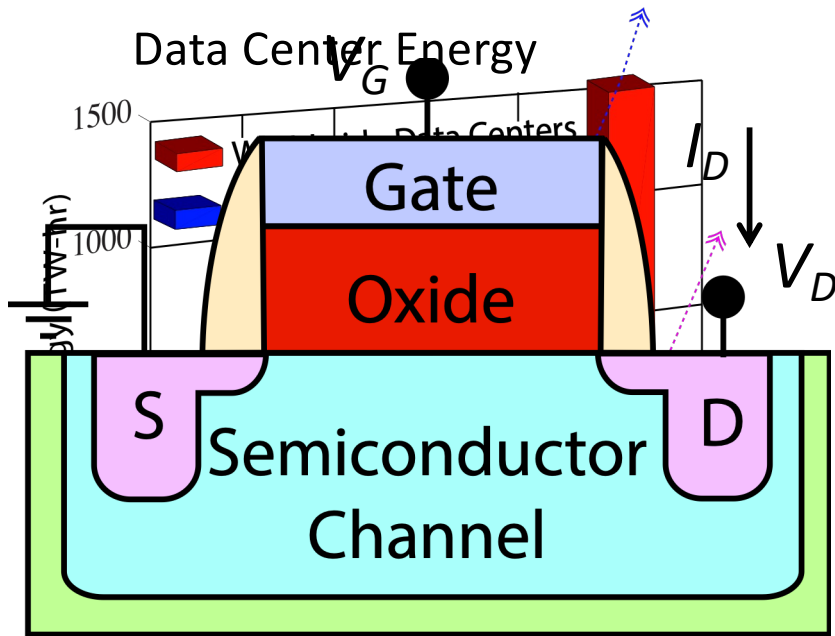
Ferroelectrics and Negative Capacitance:

What do we know and
What do we need to know?

Disclaimer:

1. Experimental facts regarding negative capacitance!
2. The talk is material agnostic!
3. Focuses on "new-yet-easy-and-simple" measurements techniques only!
4. Focuses on what and how we can learn more!

Energy Challenge in Computing

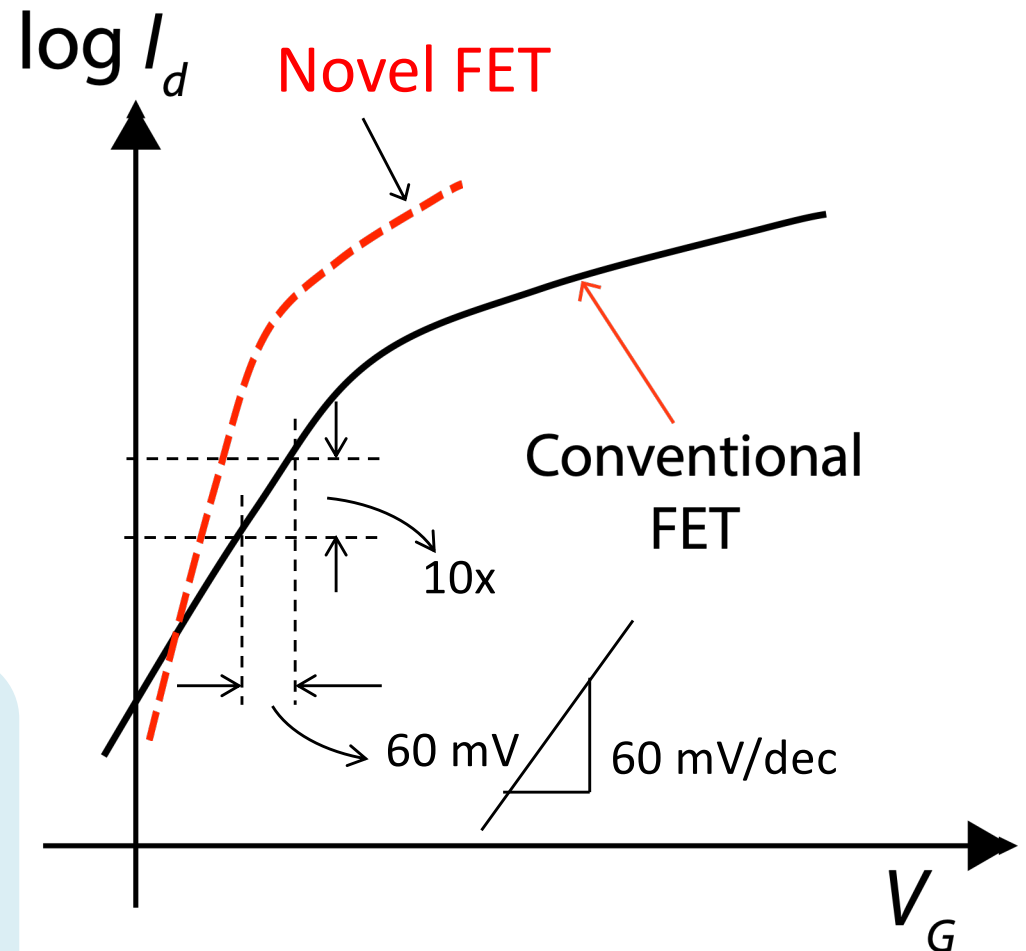


& <http://www.alliancetrustinvestments.com/sri-hub/posts/Energy-efficient-data-centres>

Boltzmann distribution
dictates that

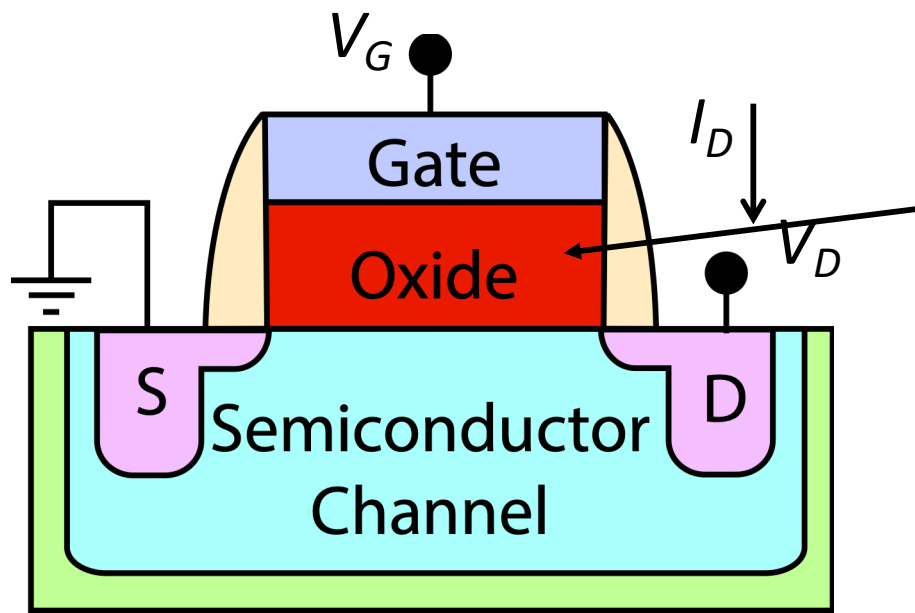
Channel charge, $Q \propto e^{\frac{q\psi_s}{k_B T}}$

Minimum Subthreshold Swing $S_{\min} = \frac{k_B T}{q} \log_e(10) = 60 \text{ mV} / \text{dec}$



We are in search of an Ultra-low Power MOSFET!

The Negative Capacitance Approach



Subthreshold Swing

$$S = \frac{\partial V_G}{\partial (\log_{10} I_D)}$$

$$= \frac{\partial V_G}{\partial \psi_s} \frac{\partial \psi_s}{\partial (\log_{10} I_D)}$$

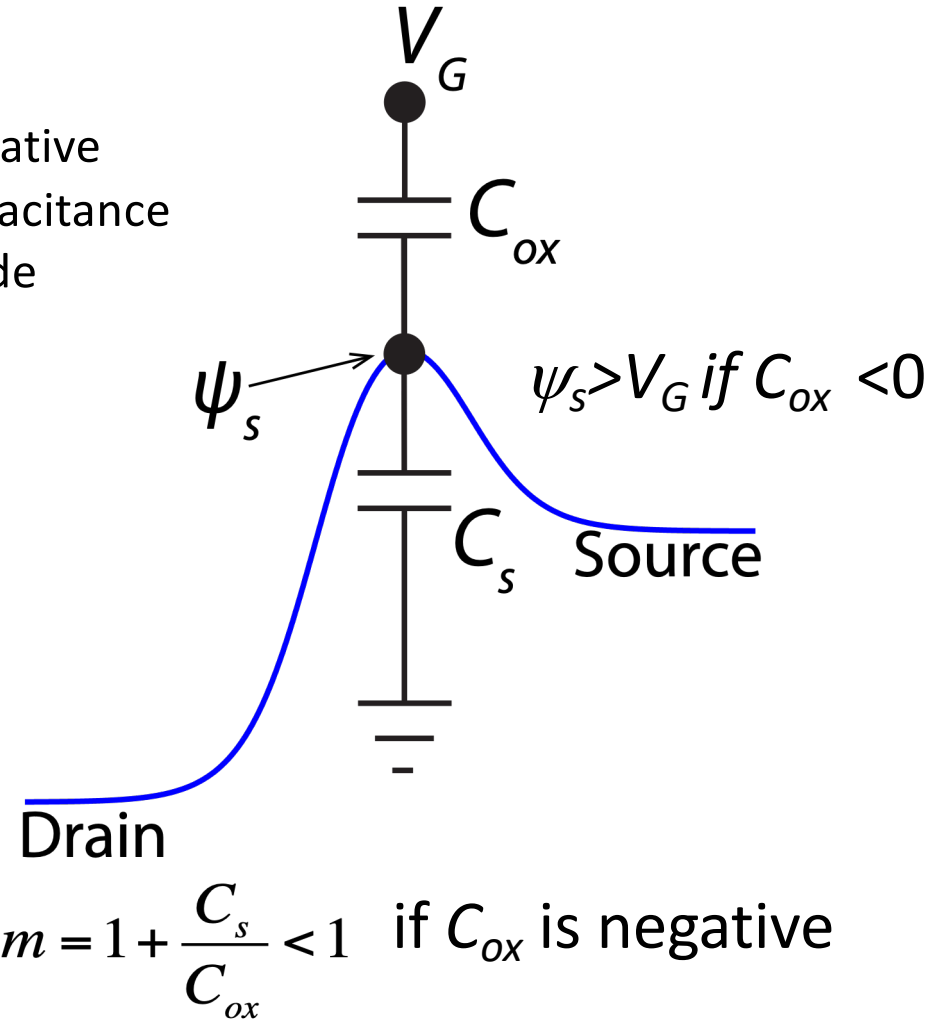
$$\underbrace{\frac{\partial \psi_s}{\partial \log_{10} I_D}}_{\equiv m}$$

$$= m \times 60 \text{ mV/decade}$$

Body Factor

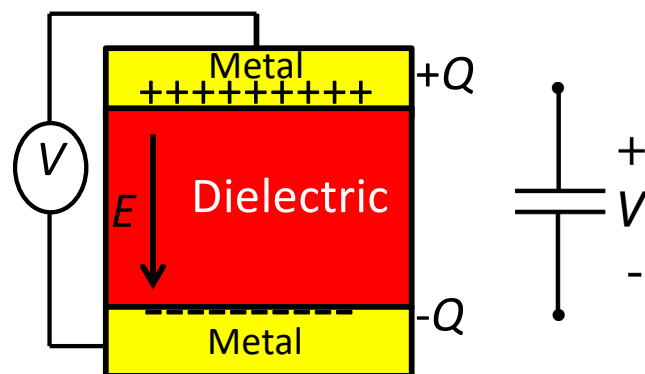
$$m = \frac{\partial V_G}{\partial \psi_s} = 1 + \frac{C_s}{C_{ox}}$$

Negative Capacitance Oxide



Negative capacitance can result in a $S < 60 \text{ mV/decade}$

Capacitance: Positive and Negative

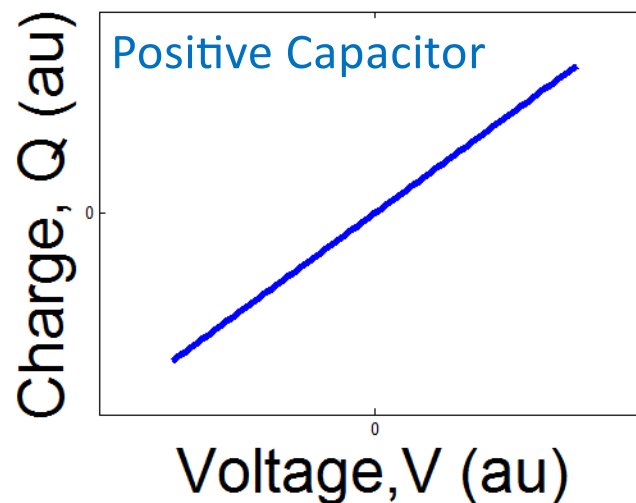


Capacitance $C = \frac{dQ}{dV}$

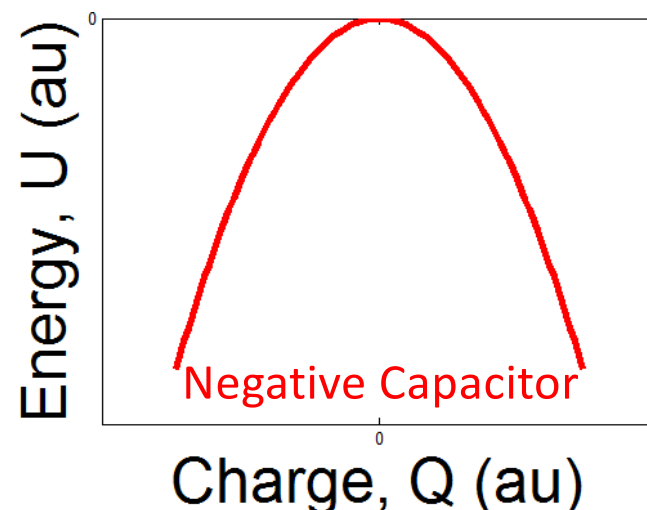
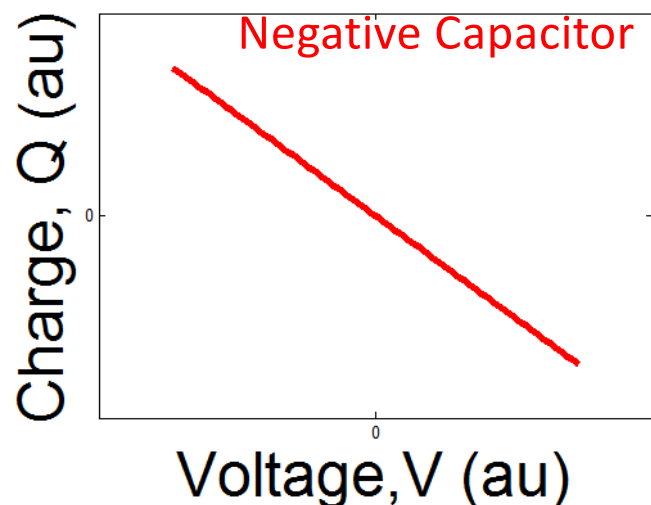
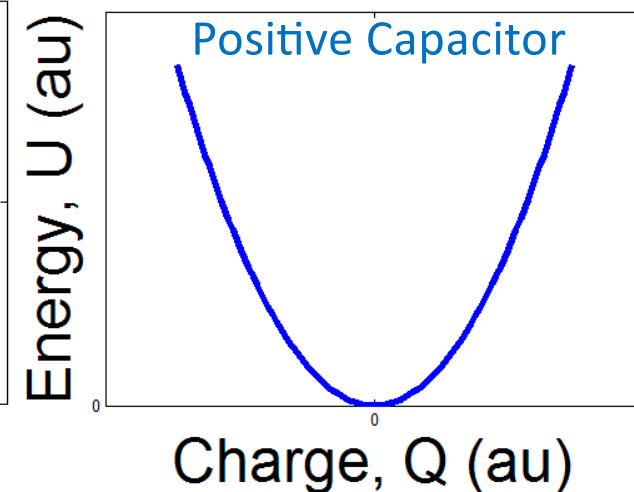
Stored Energy $U = \frac{Q^2}{2C}$

$$C = \left[\frac{d^2 U}{dQ^2} \right]^{-1}$$

Q-V Characteristics

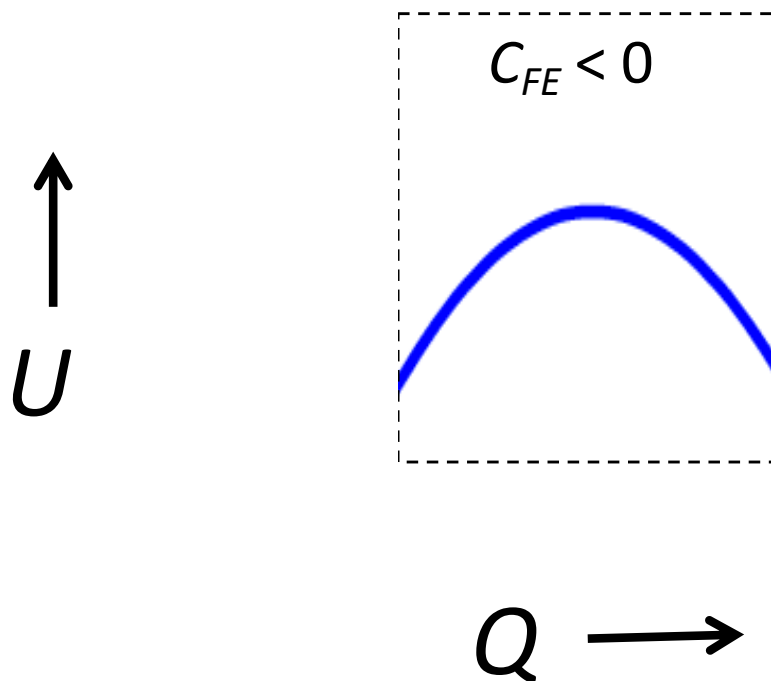


Q-U Characteristics
(Energy Landscape)



C is inverse of radius of curvature of energy landscape

How to get Negative Capacitance Material



Landau Theory of Ferroelectrics

Free energy of a ferroelectric

$$U = \alpha Q^2 + \beta Q^4 + \gamma Q^6; \alpha < 0$$

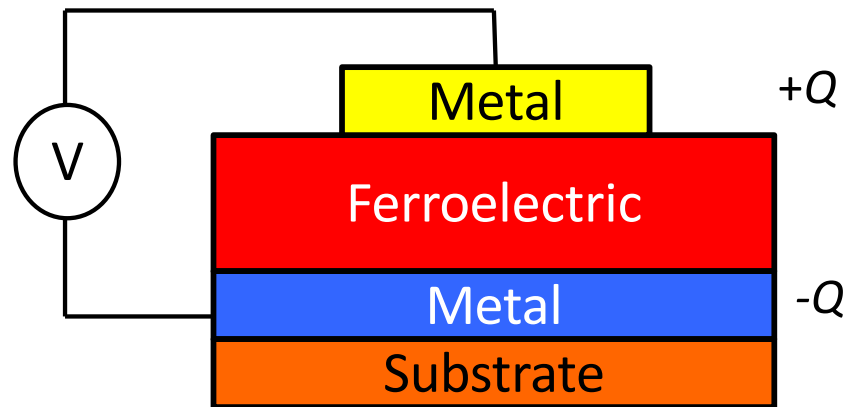
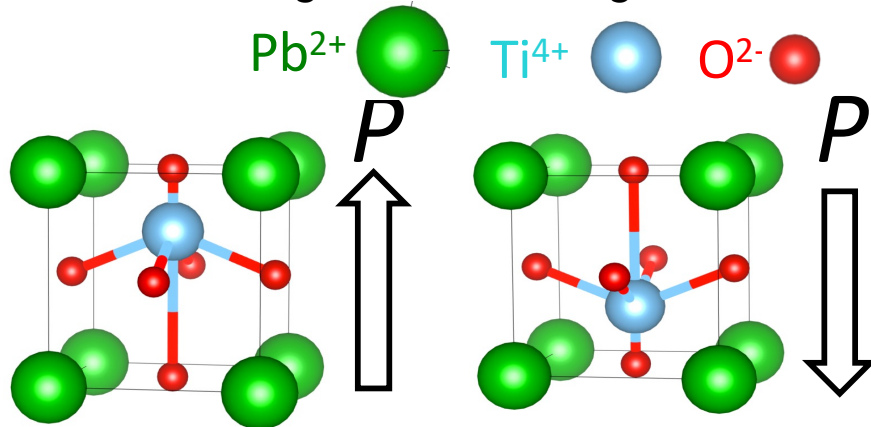
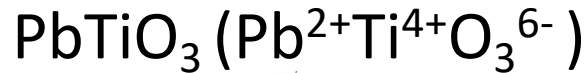
α, β, γ = Anisotropy Constants

Q = Charge

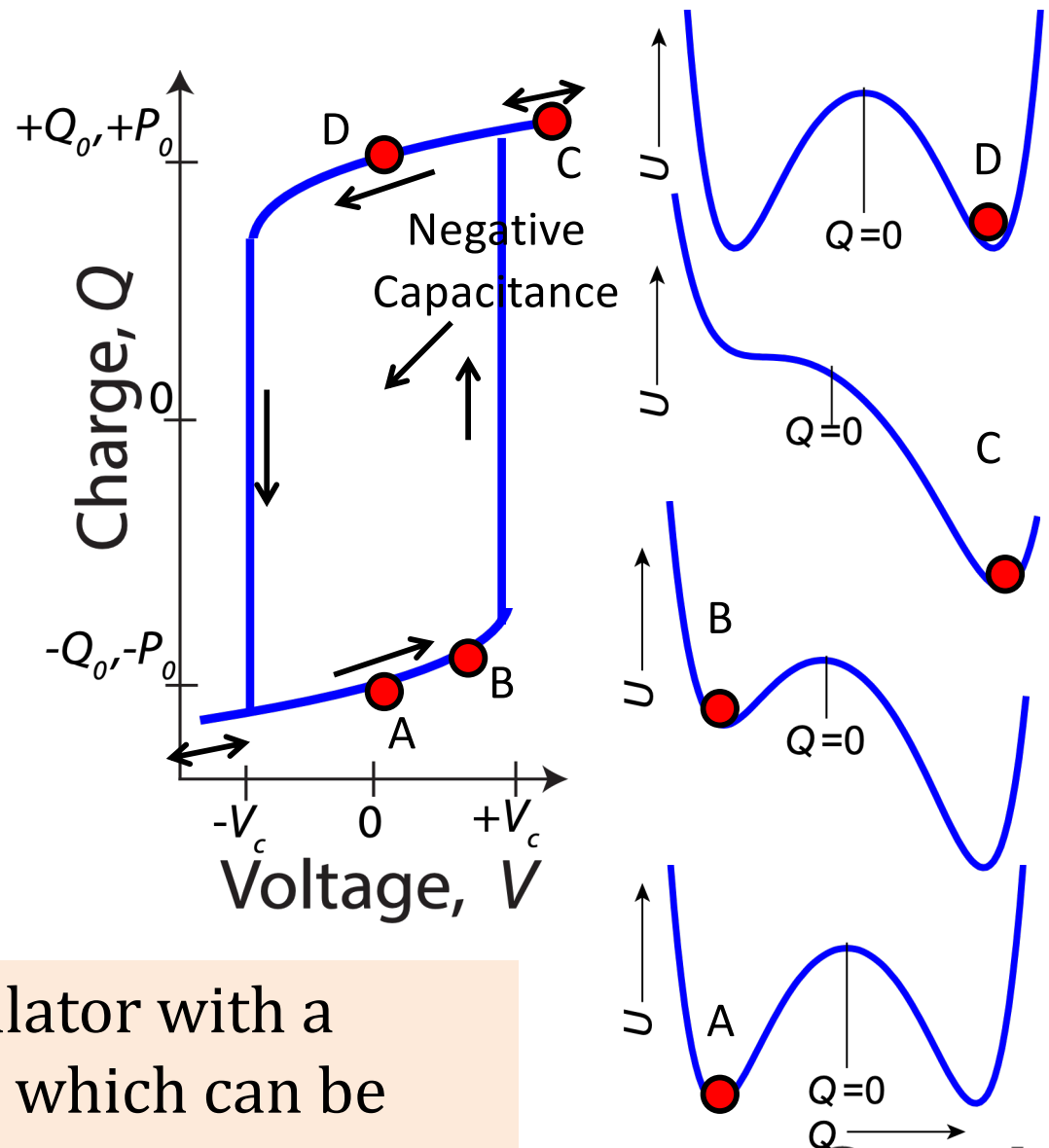
According to the Landau theory of ferroelectric, ferroelectric oxides could give an effective negative capacitance

Ferroelectric Oxides

Classical Ferroelectric



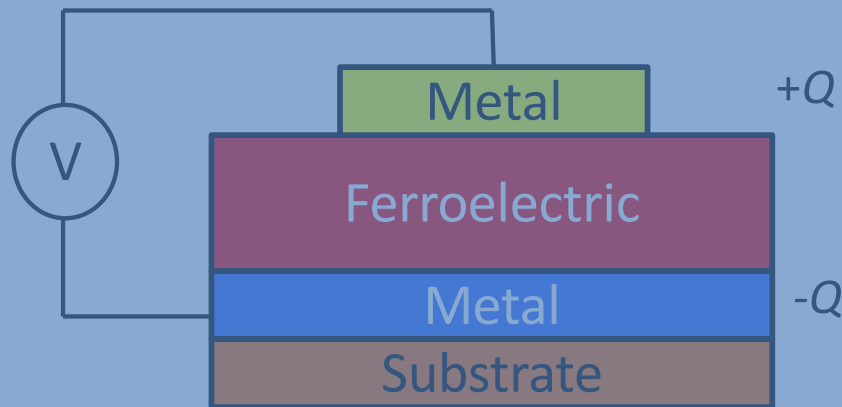
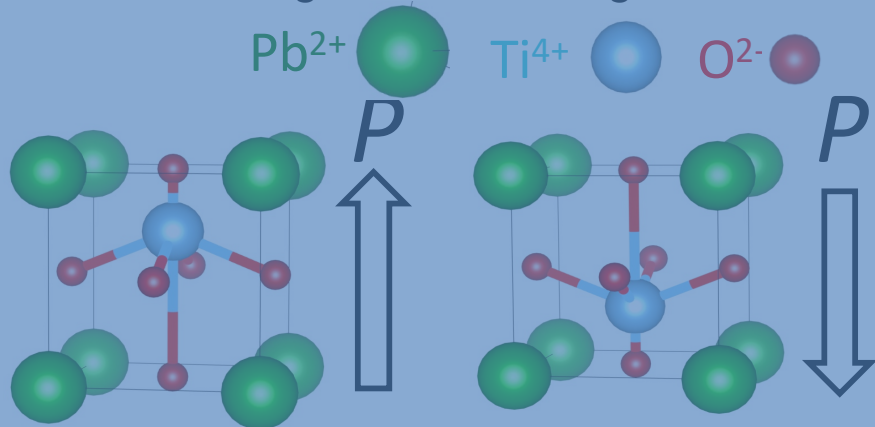
Charge Voltage Characteristics



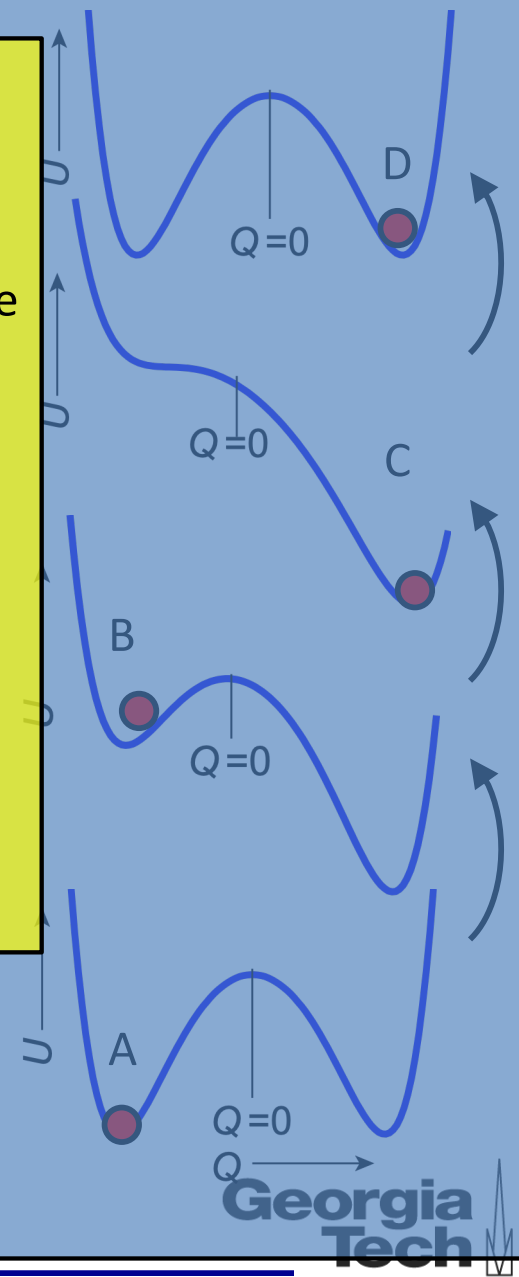
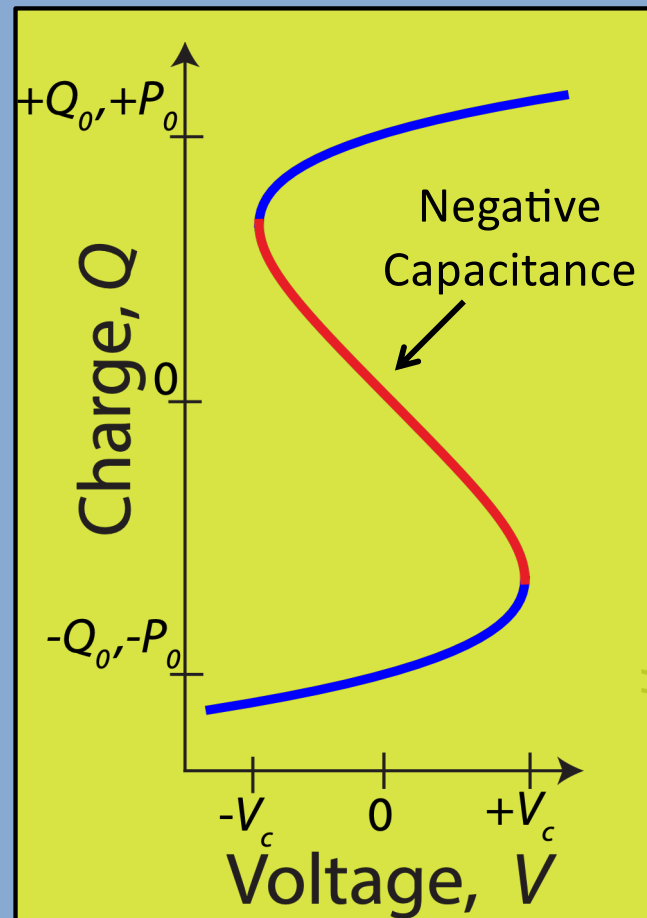
A ferroelectric is an insulator with a spontaneous polarization, which can be switched by an above coercivity voltage.

Ferroelectric Oxides

Classical Ferroelectric

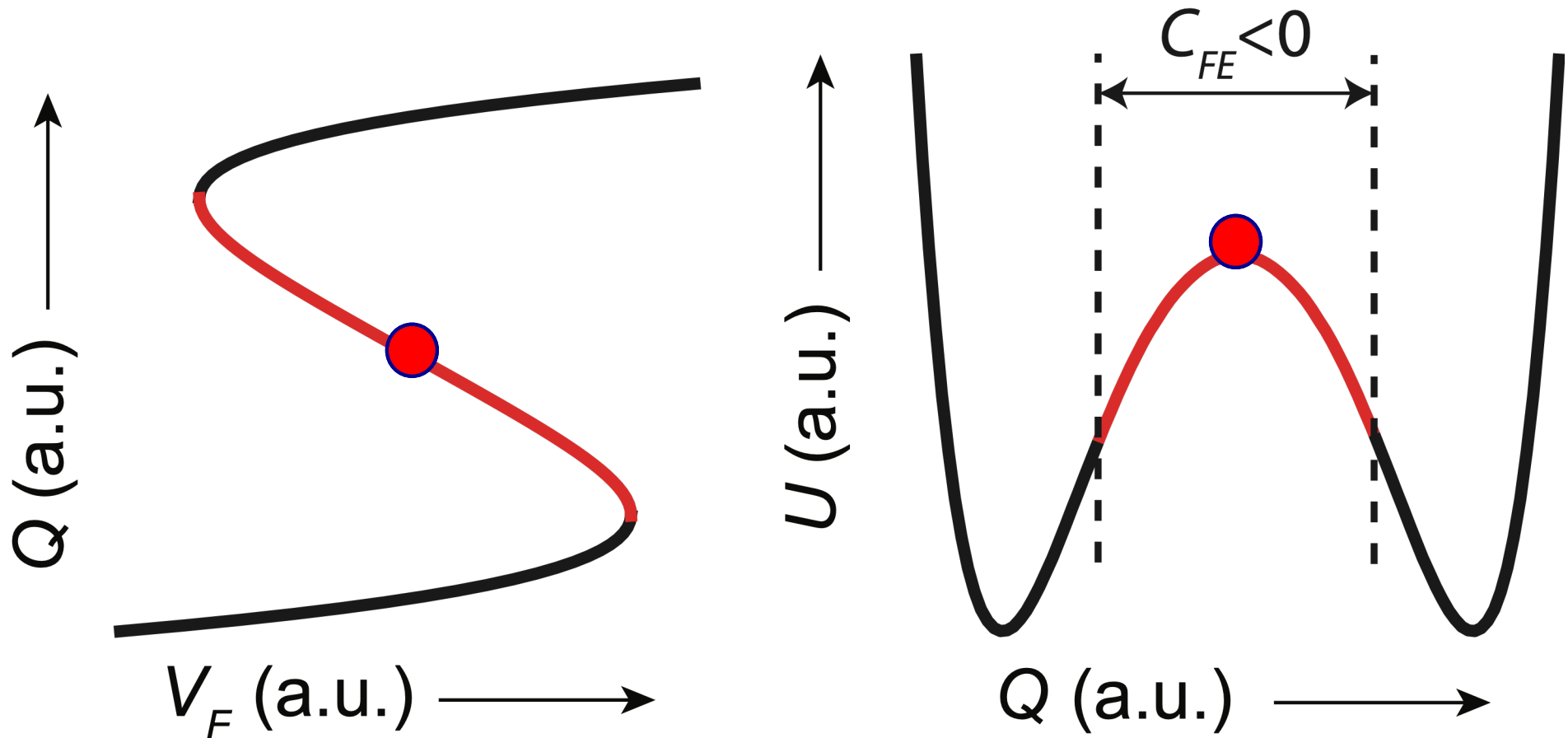


Charge Voltage Characteristics



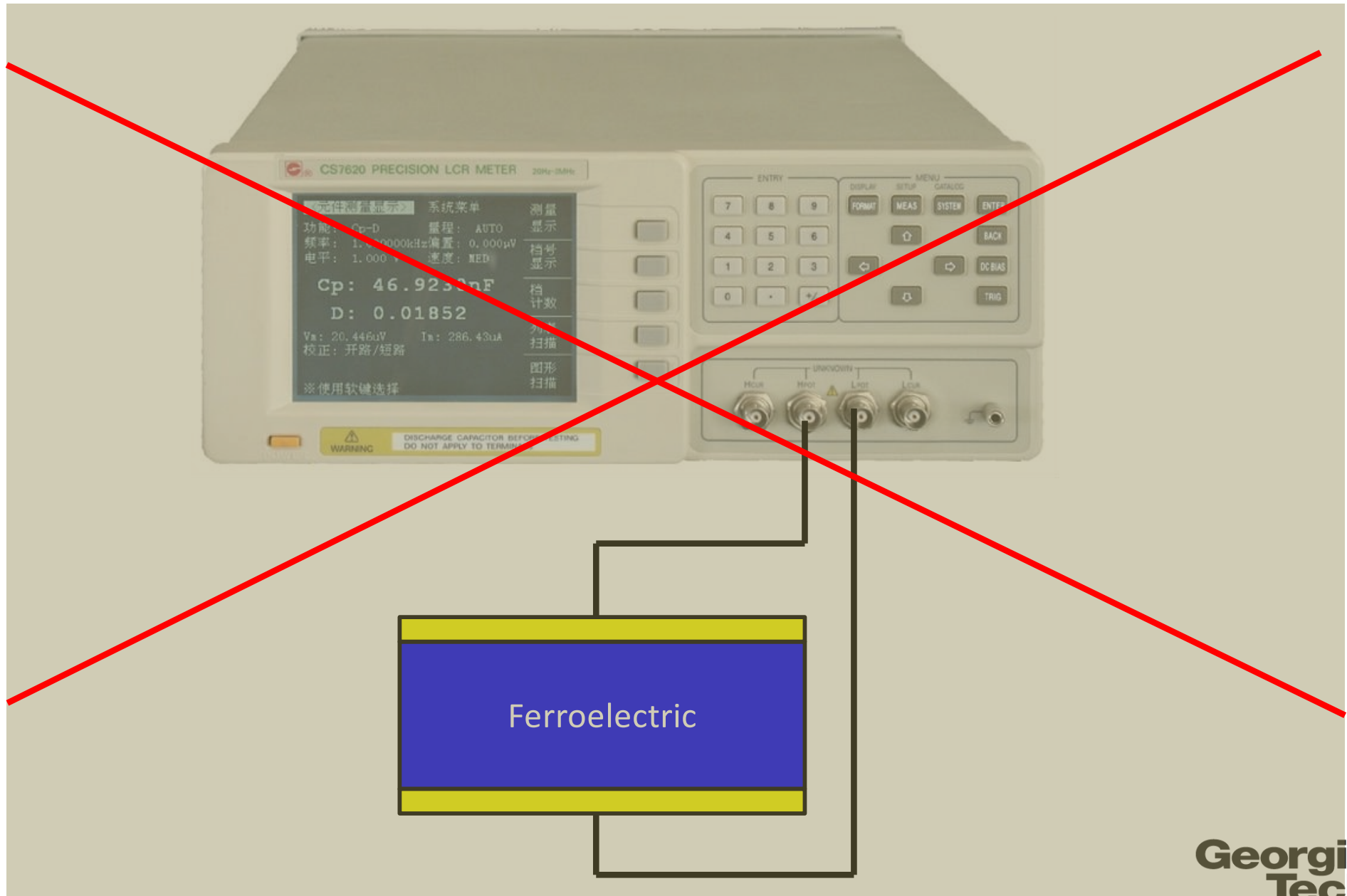
A ferroelectric is an insulator with a spontaneous polarization, which can be switched by an above coercivity voltage.

Why is it difficult to measure?

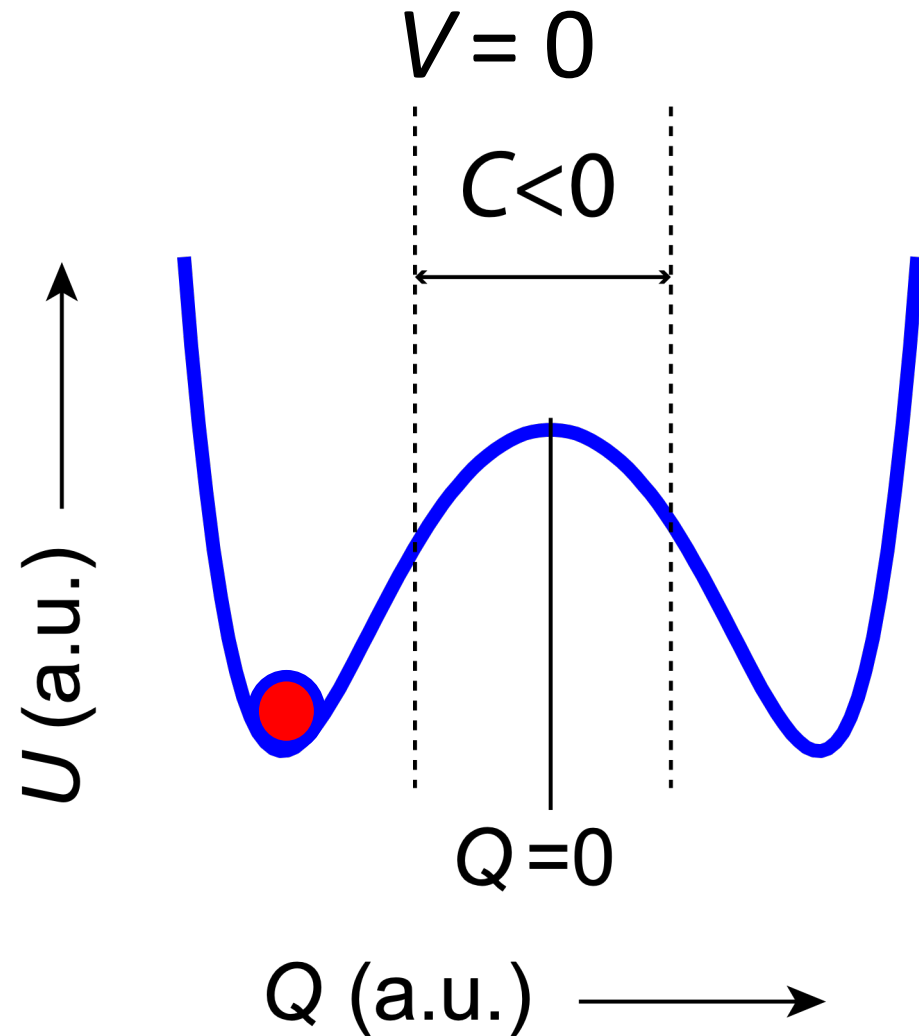
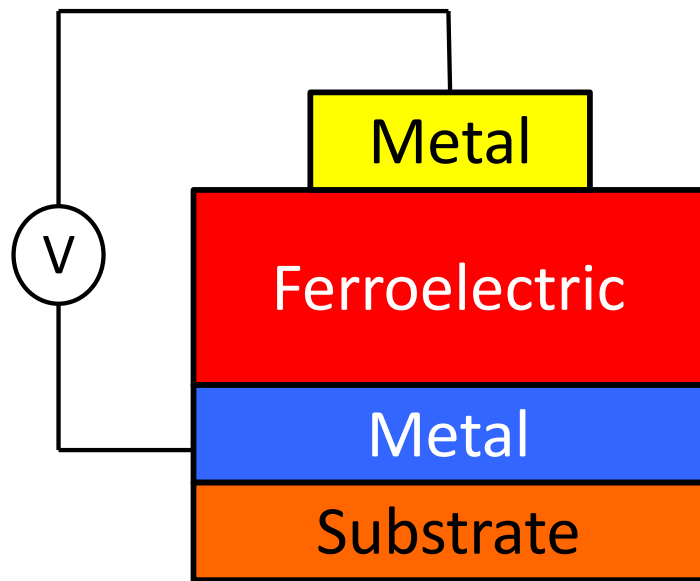


Ferroelectric Negative Capacitance is unstable and cannot be accessed in a time independent equilibrium measurement.

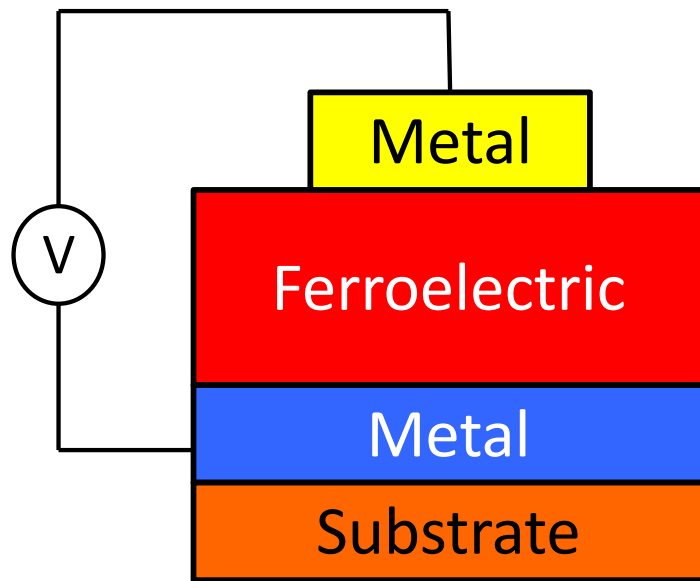
Why is it difficult to measure?



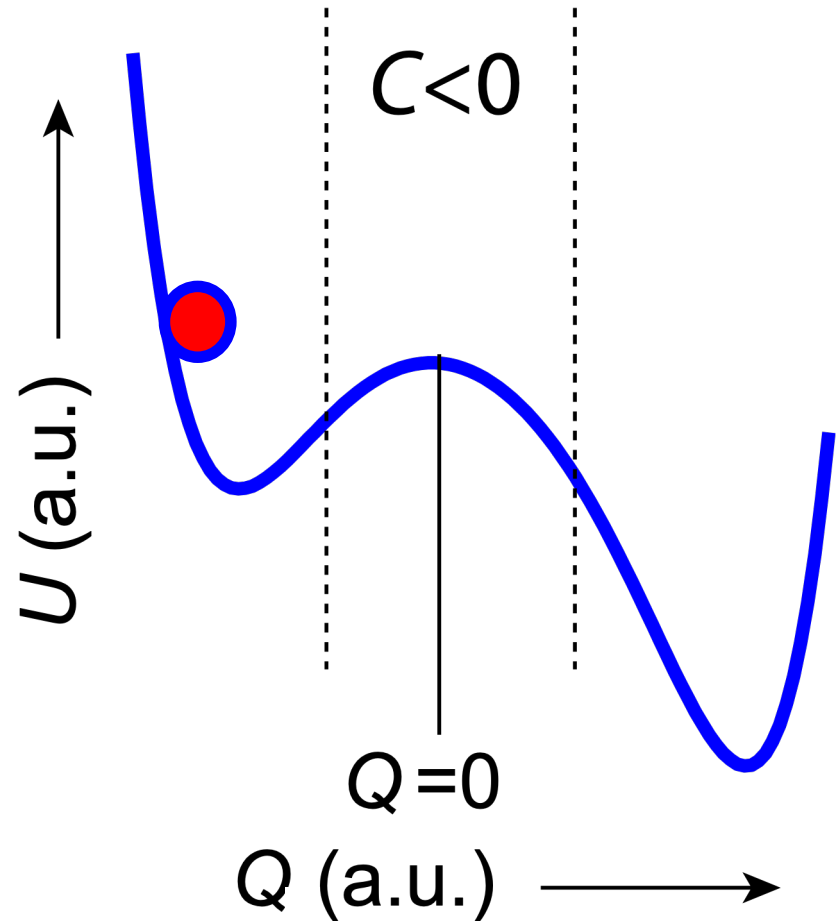
Direct Measurement of Negative Capacitance



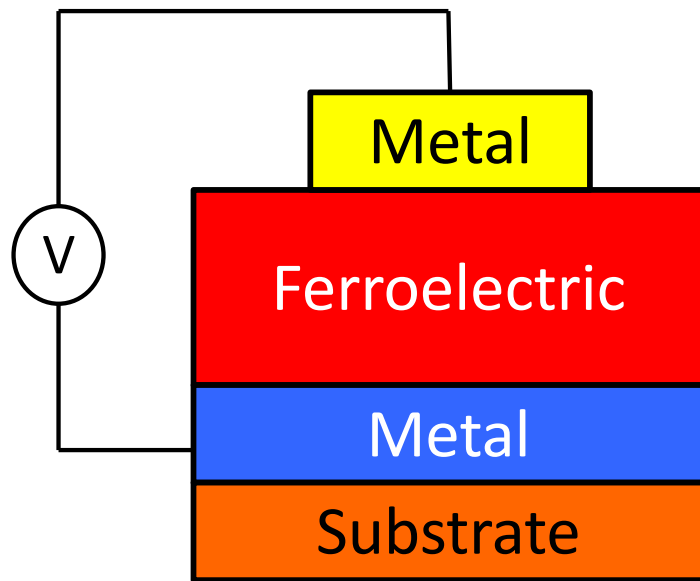
Direct Measurement of Negative Capacitance



$V < \text{Coercive Voltage}$

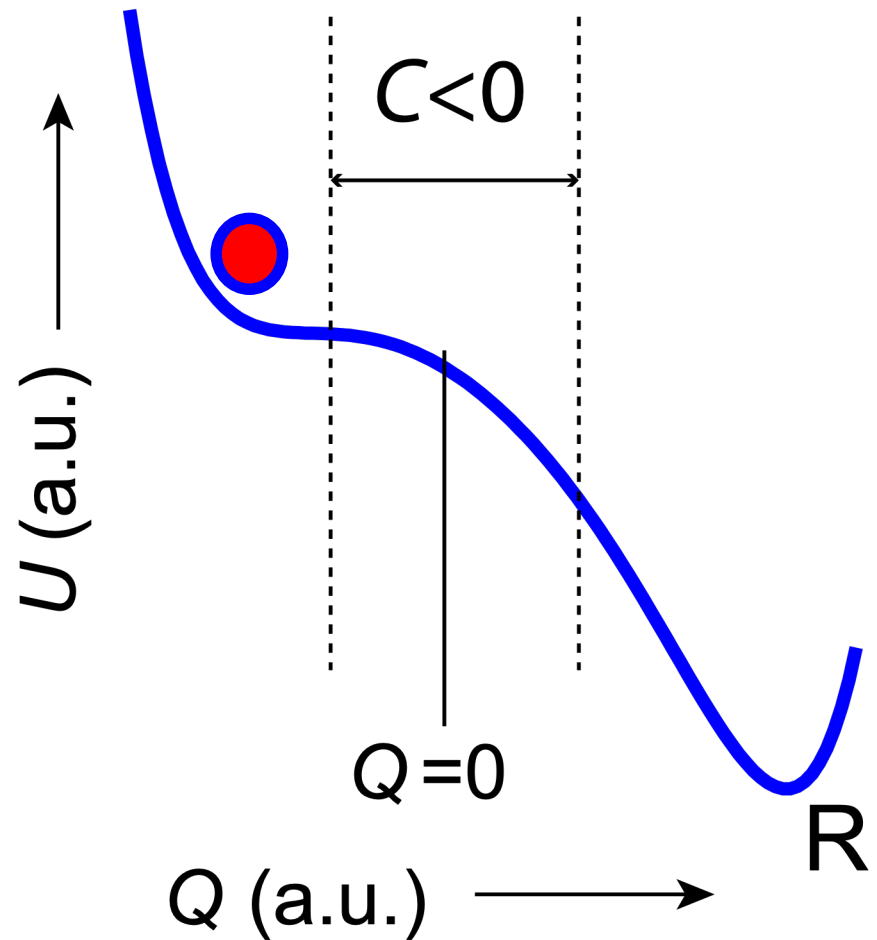


Direct Measurement of Negative Capacitance



A ferroelectric capacitor goes through the unstable negative capacitance states during switching.

$V > \text{Coercive Voltage}$



Growth of Ferroelectric Oxides

Experimental Heterostructure

Ferroelectric $\text{Pb}(\text{Zr}_{0.2}\text{Ti}_{0.8})\text{O}_3$

Metallic SrRuO₃

Substrate SrTiO_3

In-plane lattice
parameter

$$a_{PZT}=0.393 \text{ nm}$$
$$a_{SRO}=0.394 \text{ nm}$$
$$a_{STO}=0.3905 \text{ nm}$$

Pulsed Excimer Laser

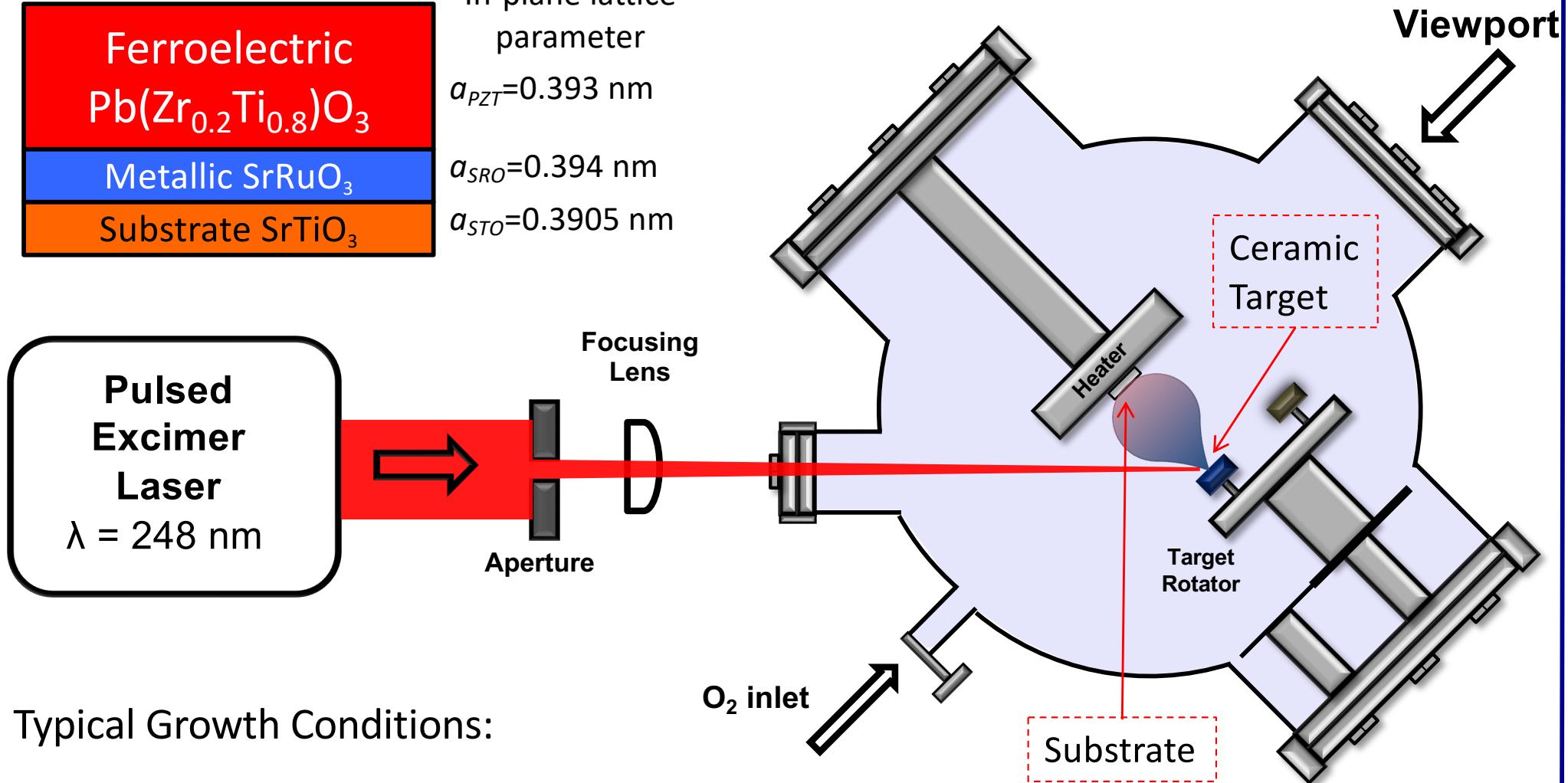
$$\lambda = 248 \text{ nm}$$

Typical Growth Conditions:

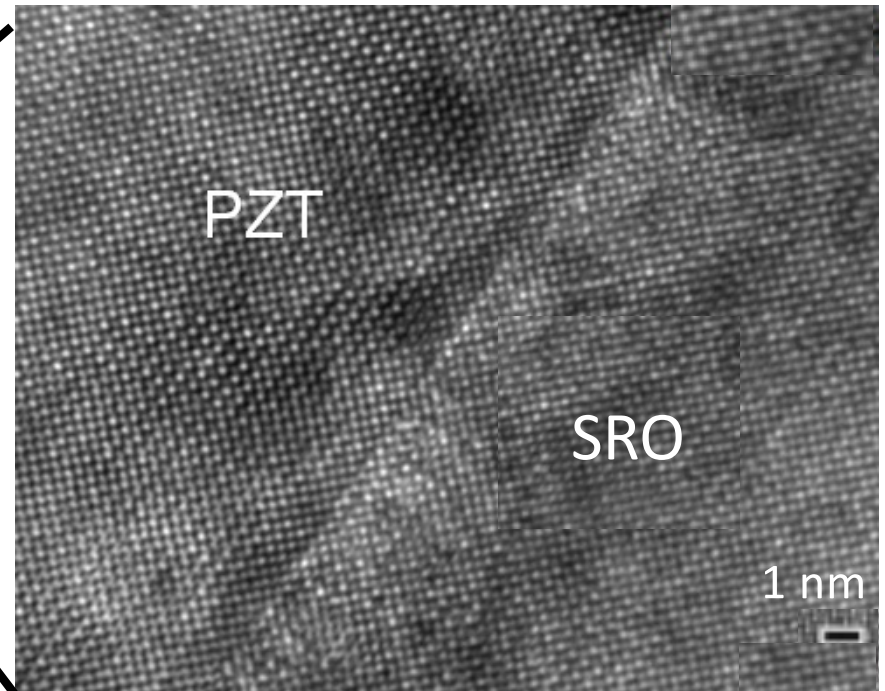
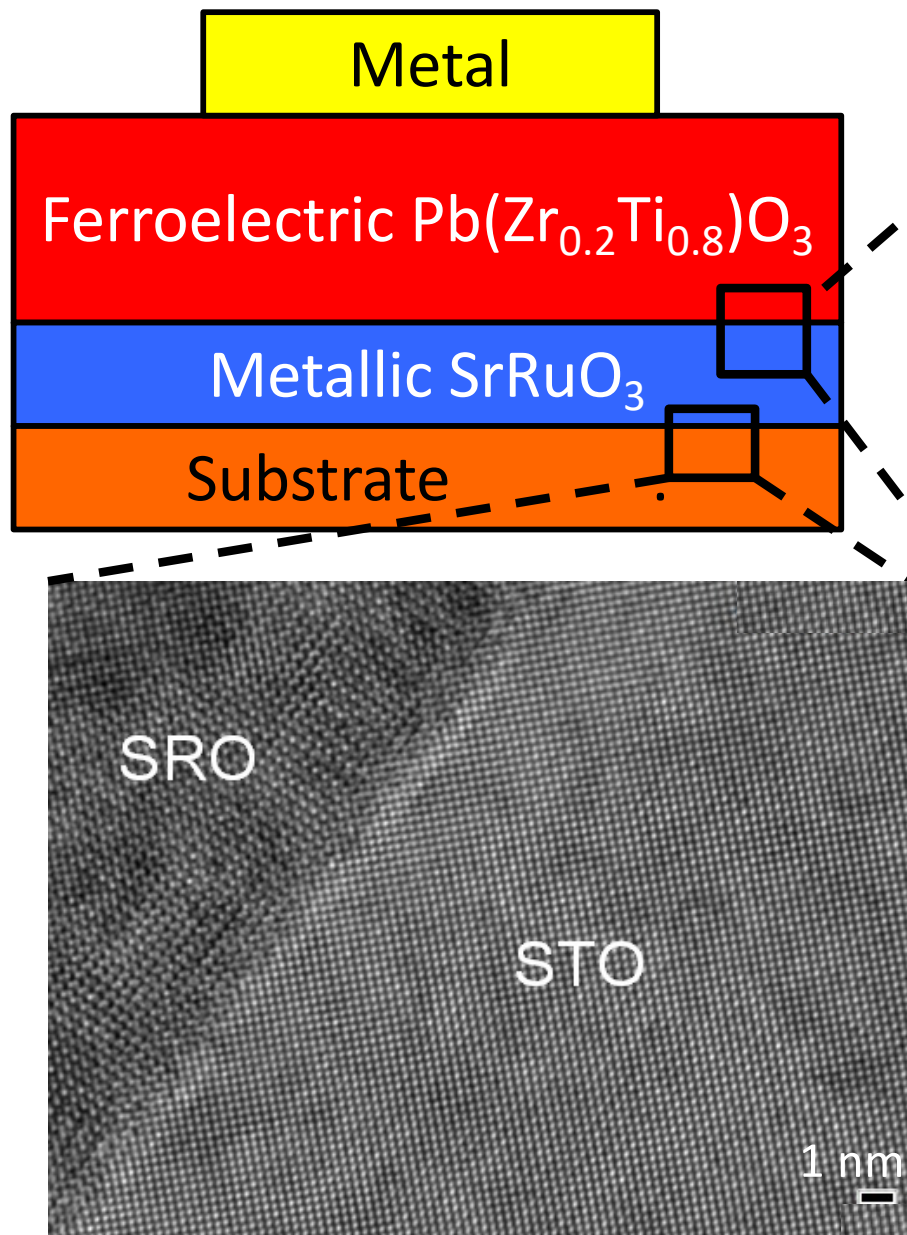
SrRuO₃: 720°C, 100 mTorr, 1.2 J/cm², 5-15 Hz

Pb(Zr_{0.2}Ti_{0.8})O₃: 630 °C, 100 mTorr, 1-1.5 J/cm², 5-15 Hz

Pulsed laser deposition technique



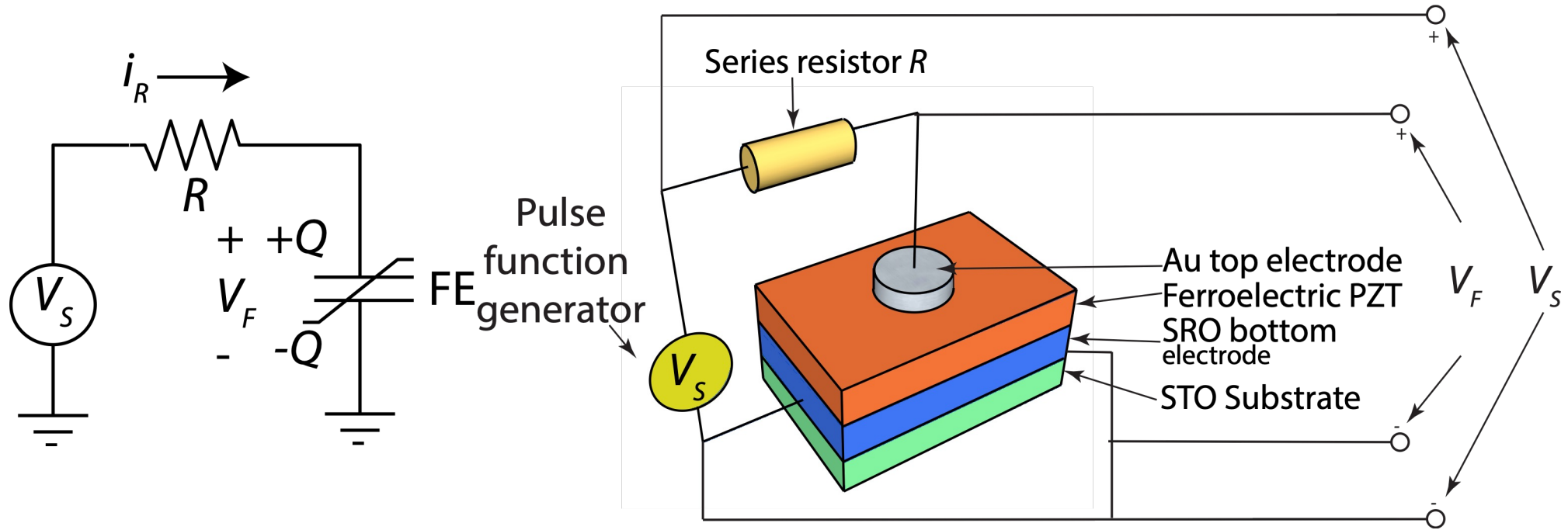
Ferroelectric $\text{Pb}(\text{Zr}_{0.2}\text{Ti}_{0.8})\text{O}_3$



Courtesy: Pan group, U. Mich.

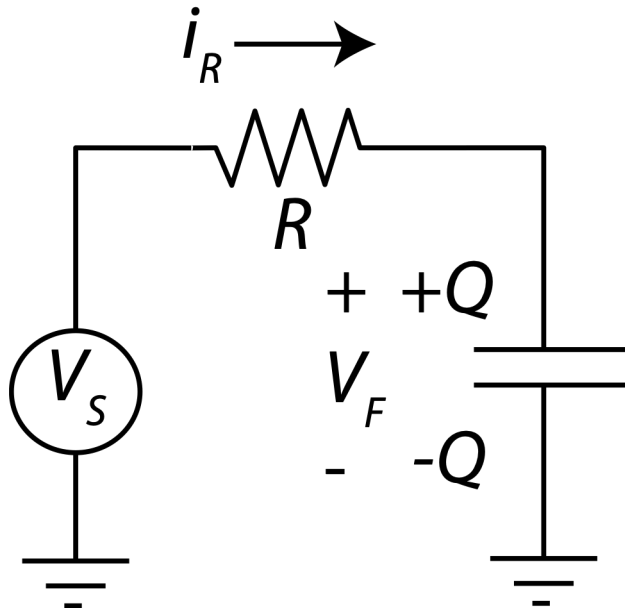
Excellent epitaxial quality!

Direct Measurement of Negative Capacitance



A ferroelectric capacitor connected in series with an external resistor.

What happens in an R - C Series circuit?

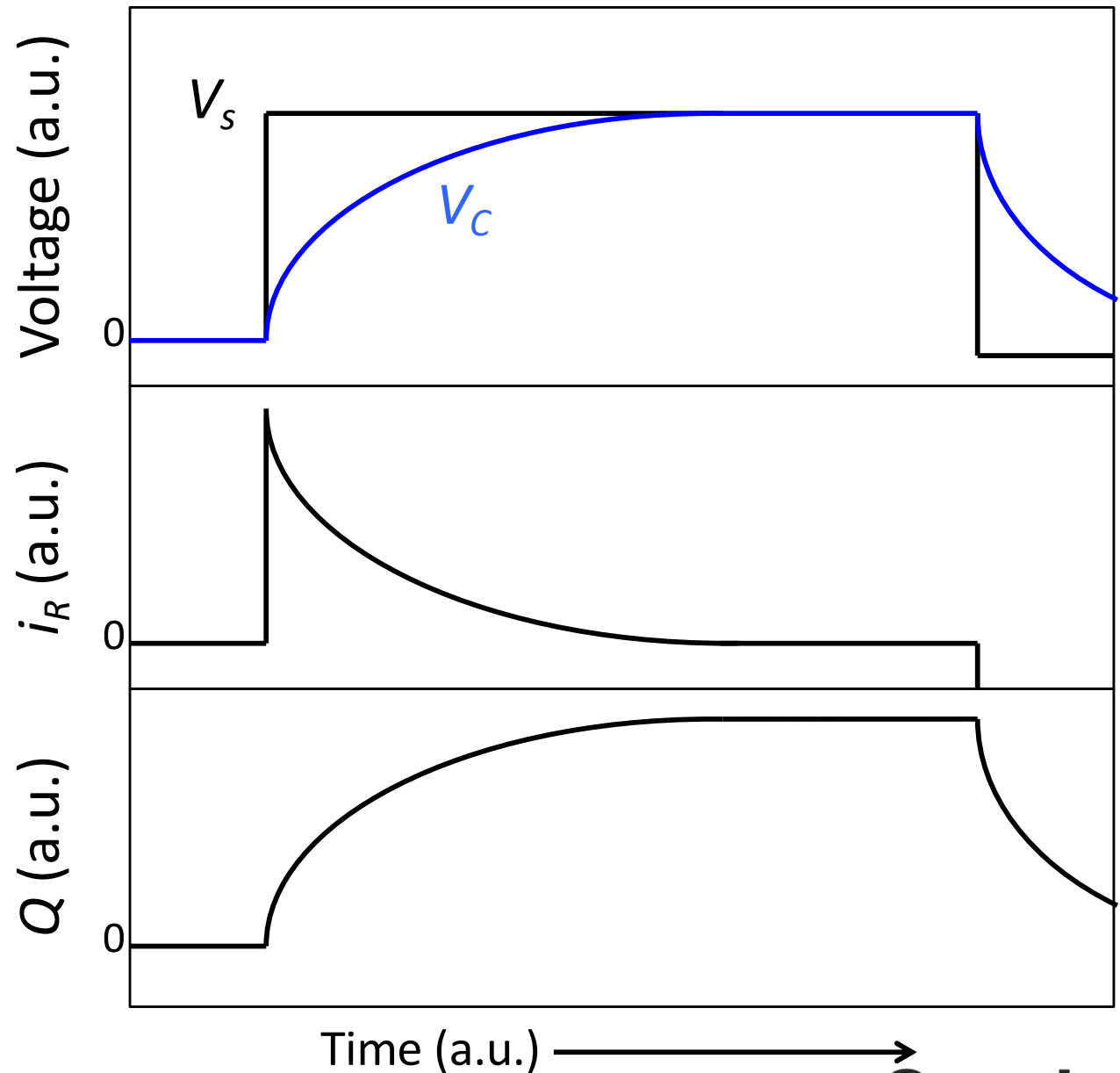


Voltage: $V_C = V_S(1 - e^{-t/RC})$

Current: $i_R = V_S e^{-t/RC} / R$

Charge: $Q = CV_S(1 - e^{-t/RC})$

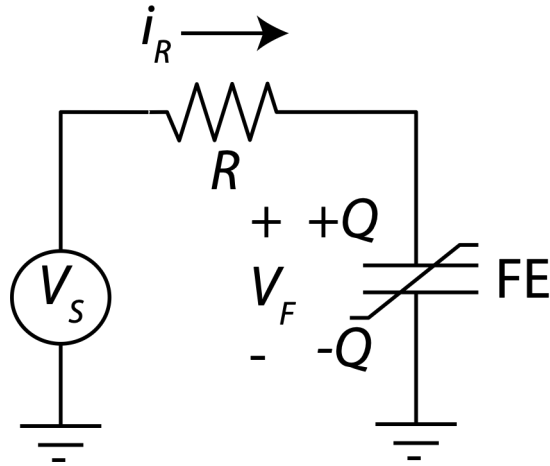
At any time t ,
 $dQ/dV_C = C > 0$.



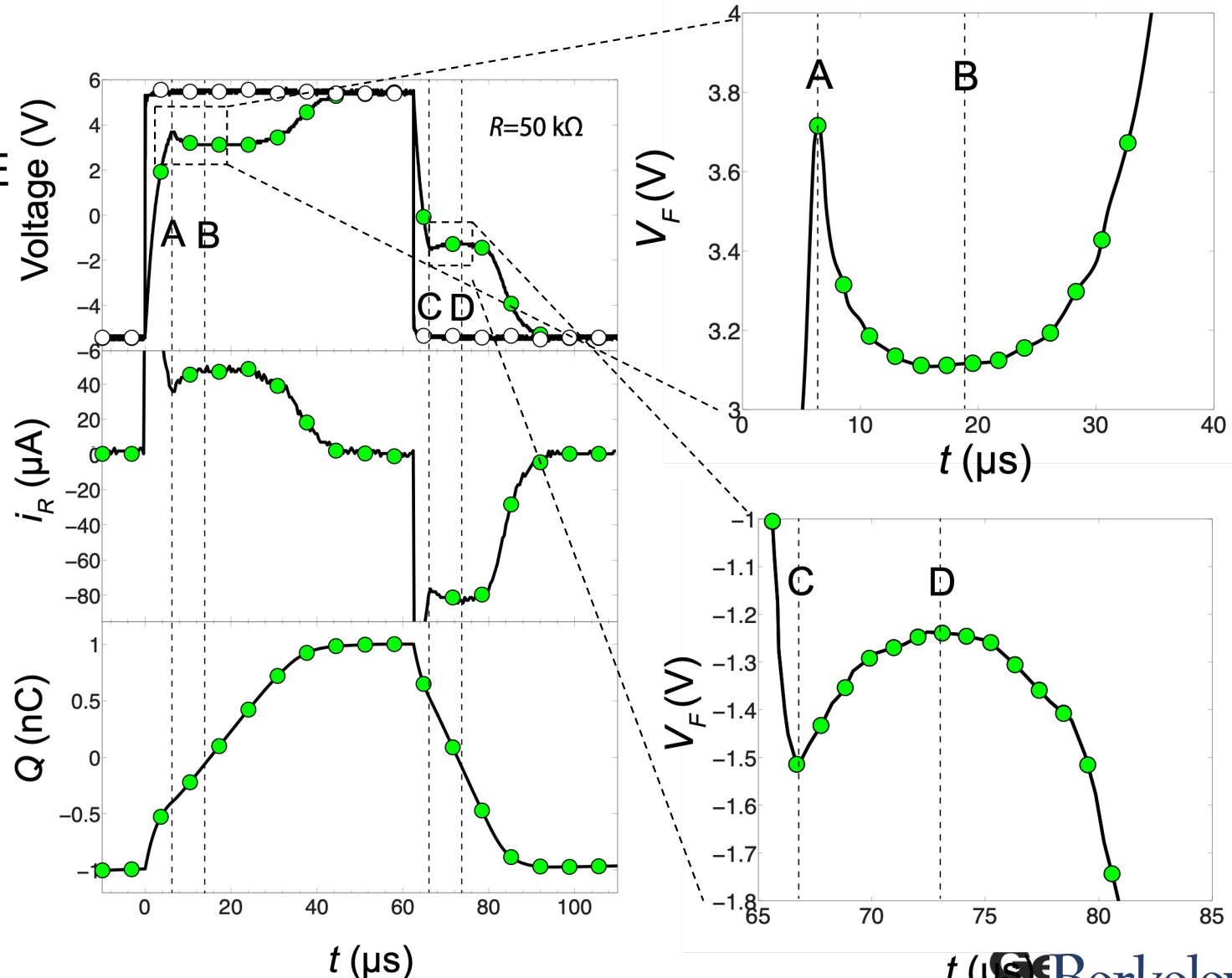
Direct Measurement of Negative Capacitance

60 nm $\text{Pb}(\text{Zr}_{0.2}\text{Ti}_{0.8})\text{O}_3$ (Coercive voltage ~ 2.5 V)

$V_s: -5.4 \text{ V} \rightarrow +5.4 \text{ V} \rightarrow -5.4 \text{ V}$

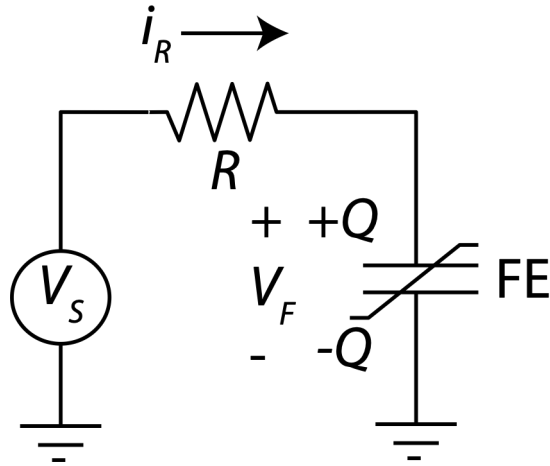


During AB & CD,
 dQ & dV_F has
opposite signs
indicating
 $dQ/dV_F < 0$.

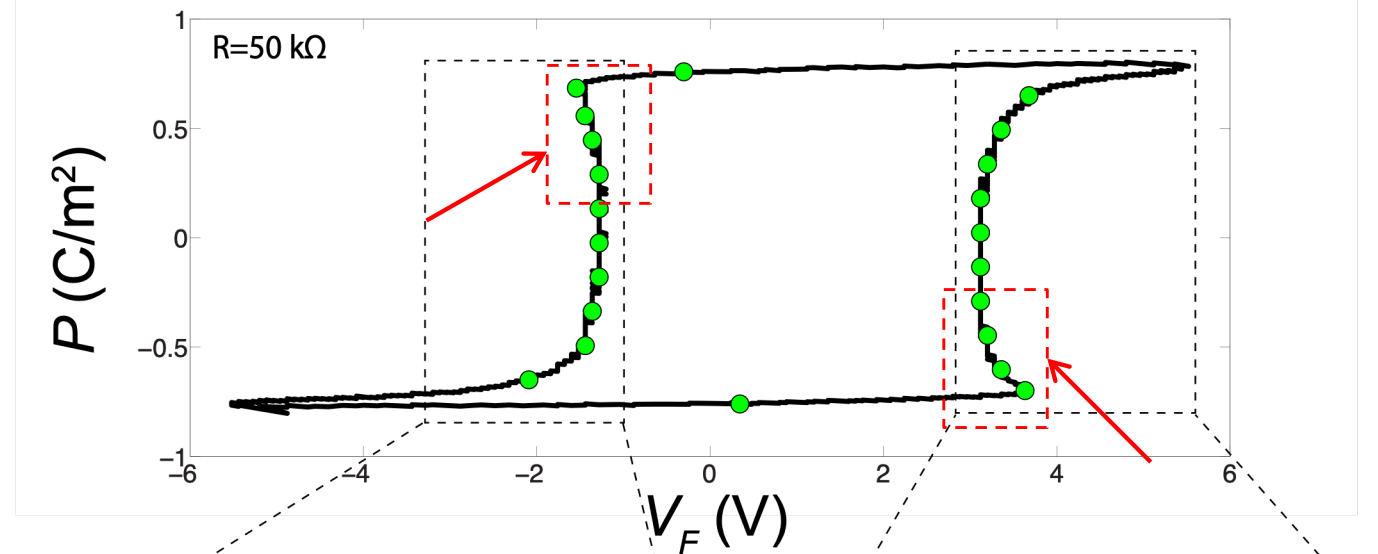


Khan et al. Nature Mater. 14,
182 (2015)

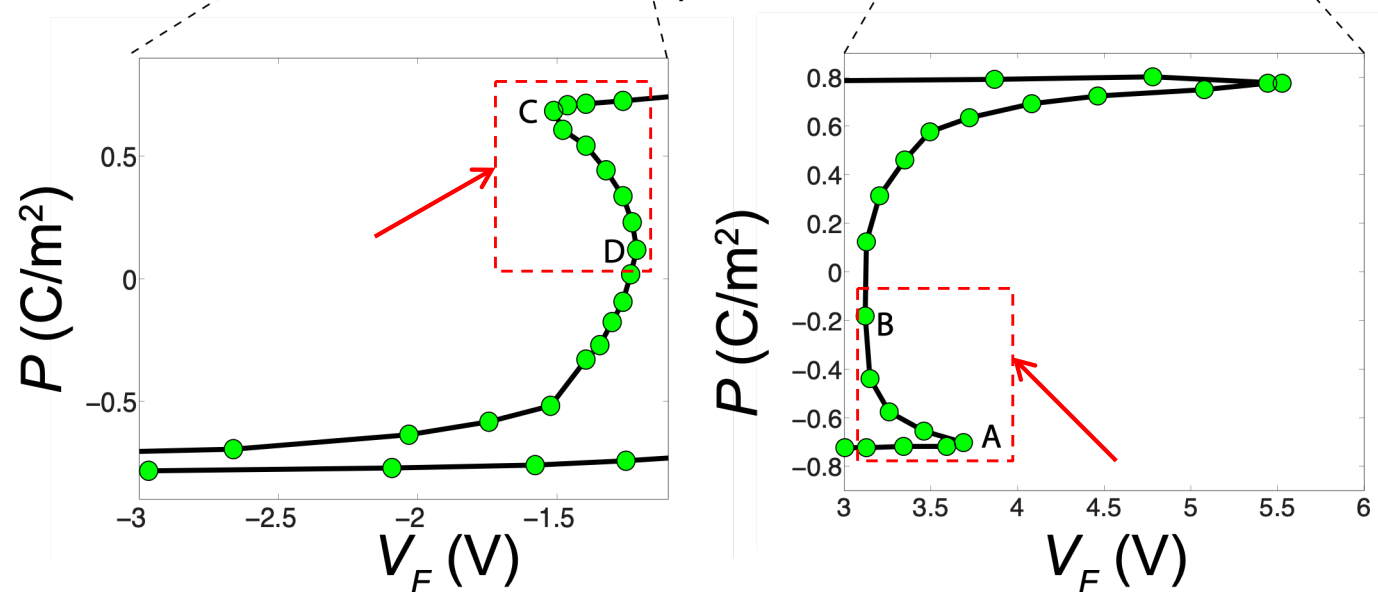
Direct Measurement of Negative Capacitance



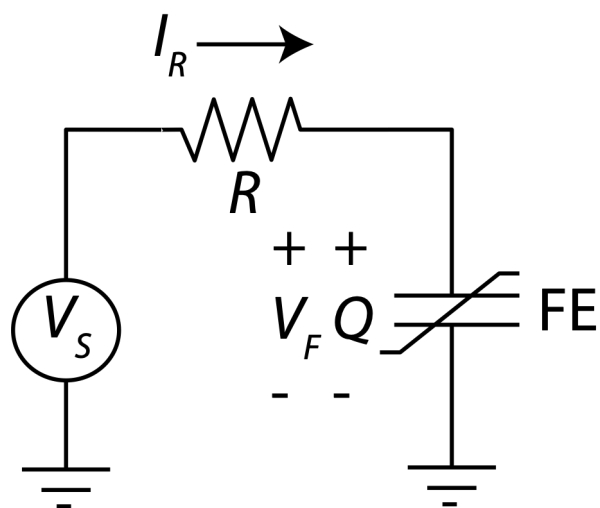
60 nm $\text{Pb}(\text{Zr}_{0.2}\text{Ti}_{0.8})\text{O}_3$ (Coercive voltage ~ 2.5 V)



A negative dQ/dV_F directly observed in an isolated ferroelectric.

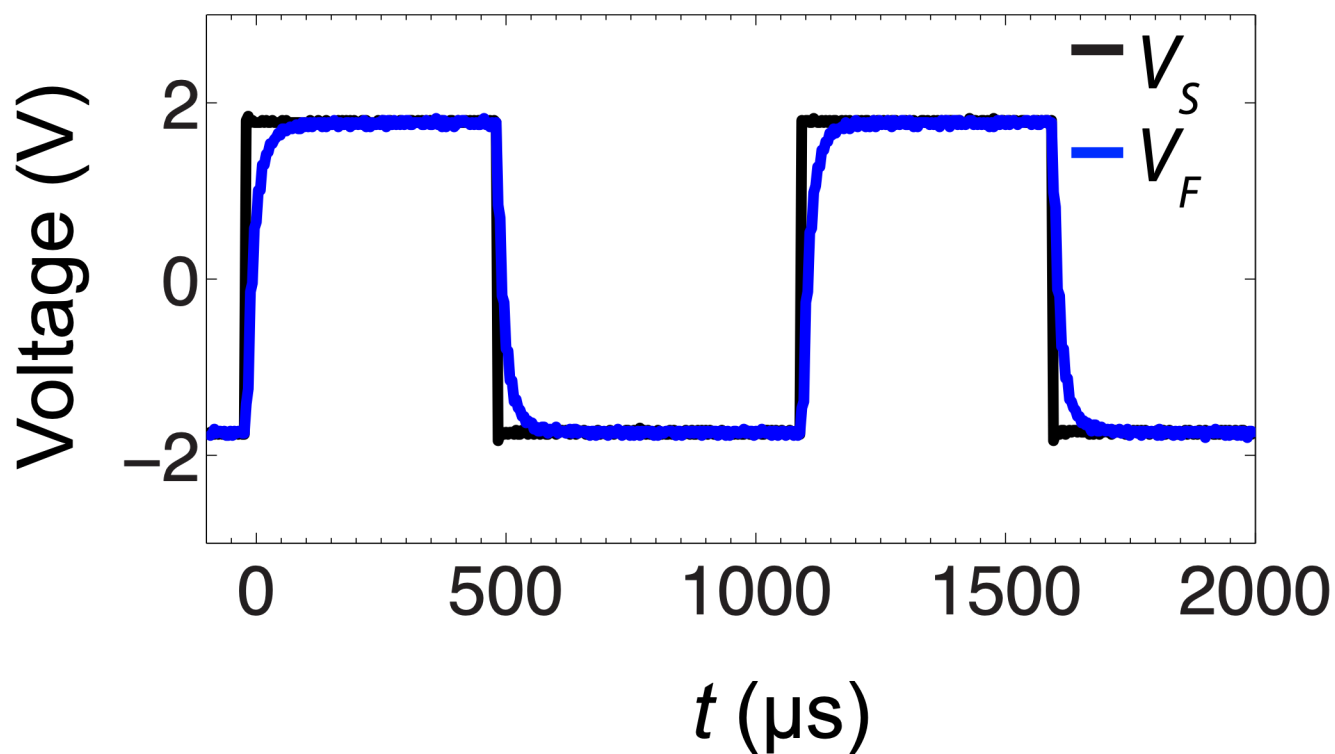


Correlation with ferroelectric switching

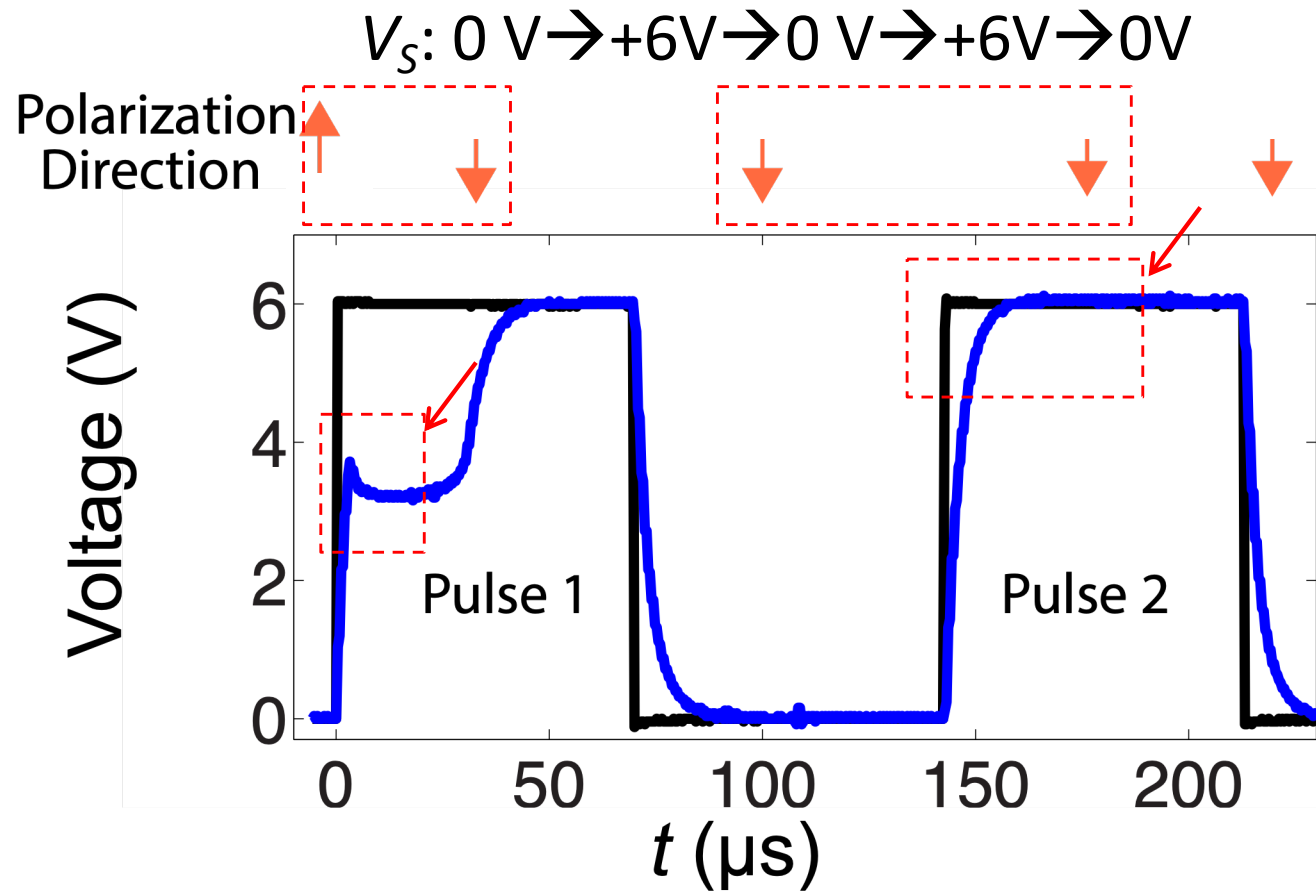
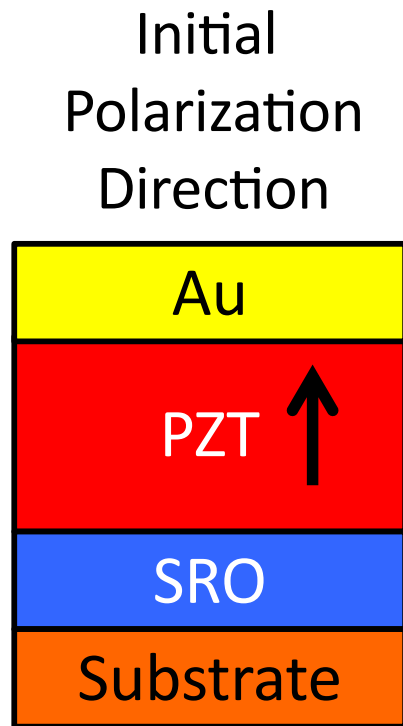


No negative capacitance transients are observed when the voltage is smaller than coercive voltage.

Coercive Voltage (~ 2.5 V)
 $V_S: -1.8$ V $\rightarrow$ $+1.8$ V $\rightarrow$ -1.8 V $\rightarrow$ $+1.8$ V



Correlation with ferroelectric switching

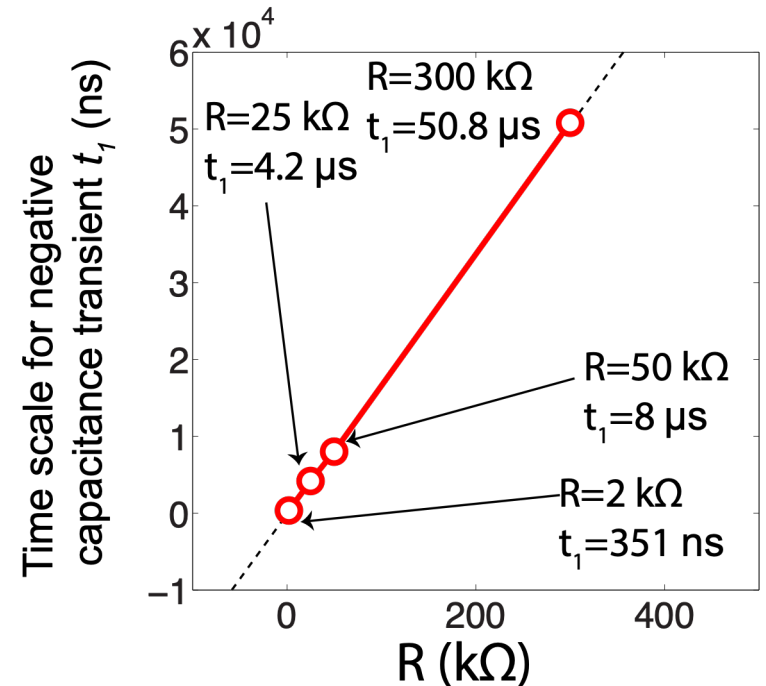
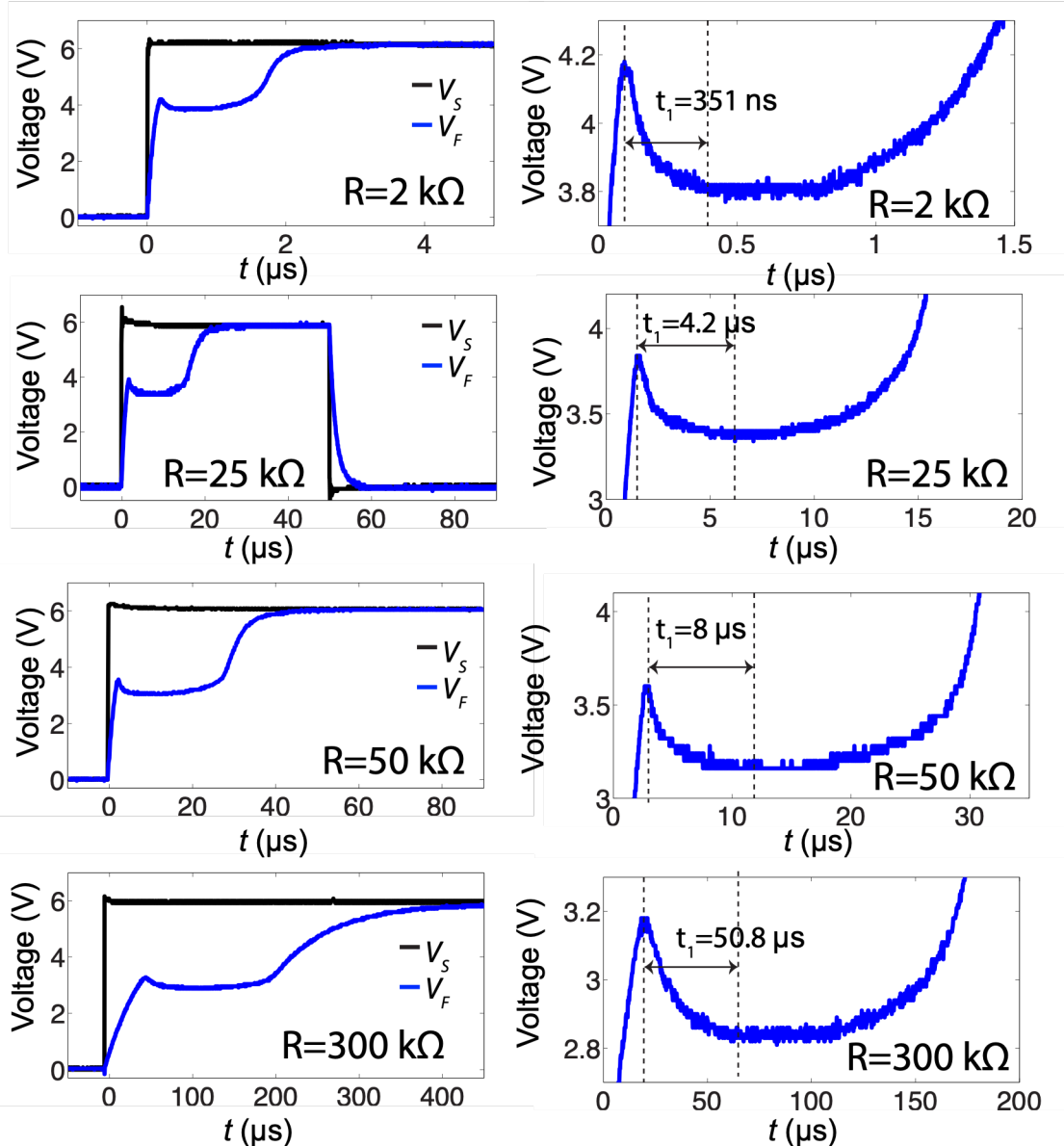


No negative capacitance transients are observed when there is no FE switching event.

Khan et al. Nature Mater. 14, 182 (2015)

Time scale of transients

Close-up of the negative capacitance transients

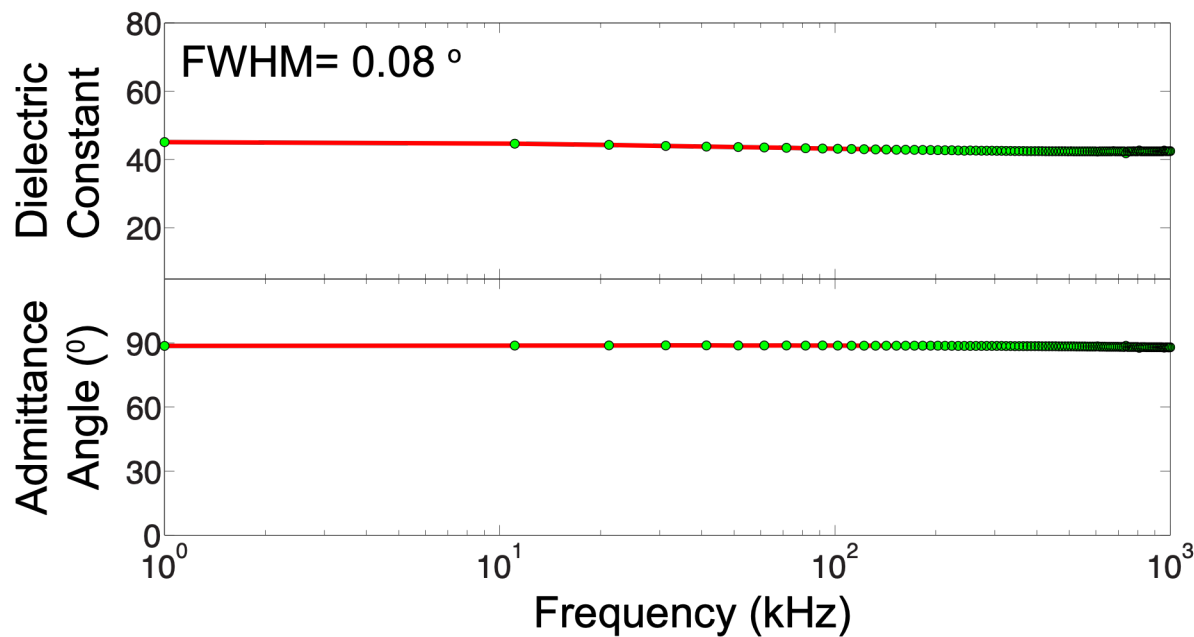


The intrinsic time scale of negative capacitance transient could be very small.

Correlation with Structural Properties

Negative capacitance effect is correlated to the structural quality of the thin film.

Dielectric Frequency Dispersion

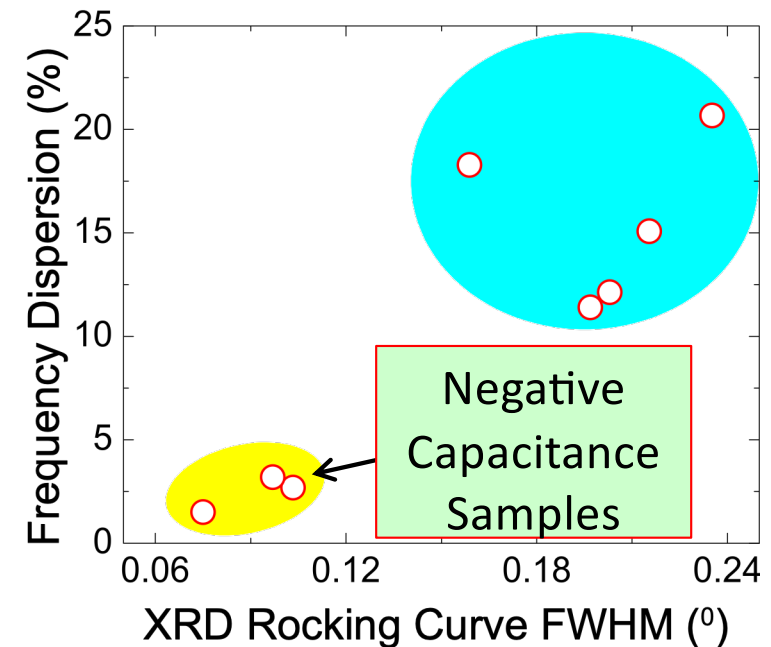


Frequency Dispersion between 1 kHz and 1 MHz < 2 %

Khan et al. Nature Mater. 14, 182 (2015)

Khan et al. Appl. Phys. Lett. 104, 262407 (2014)

Frequency Dispersion vs. FWHM



High quality materials growth is the key!

Landau-Khalatnikov model

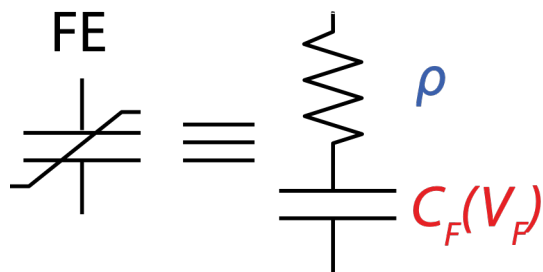
Landau-Khalatnikov Equation: $\rho \frac{dP}{dt} = - \frac{dU}{dP}$

P =Ferroelectric polarization

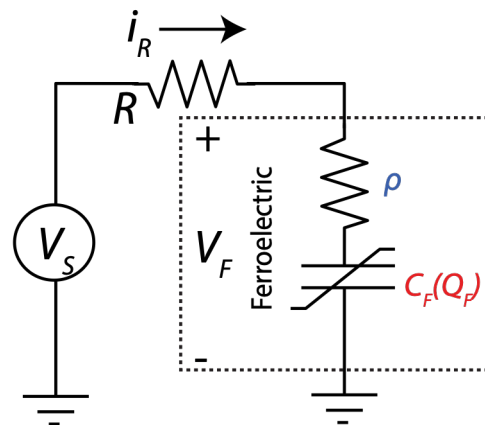
U =Free energy of the system

ρ = Material dependent parameter

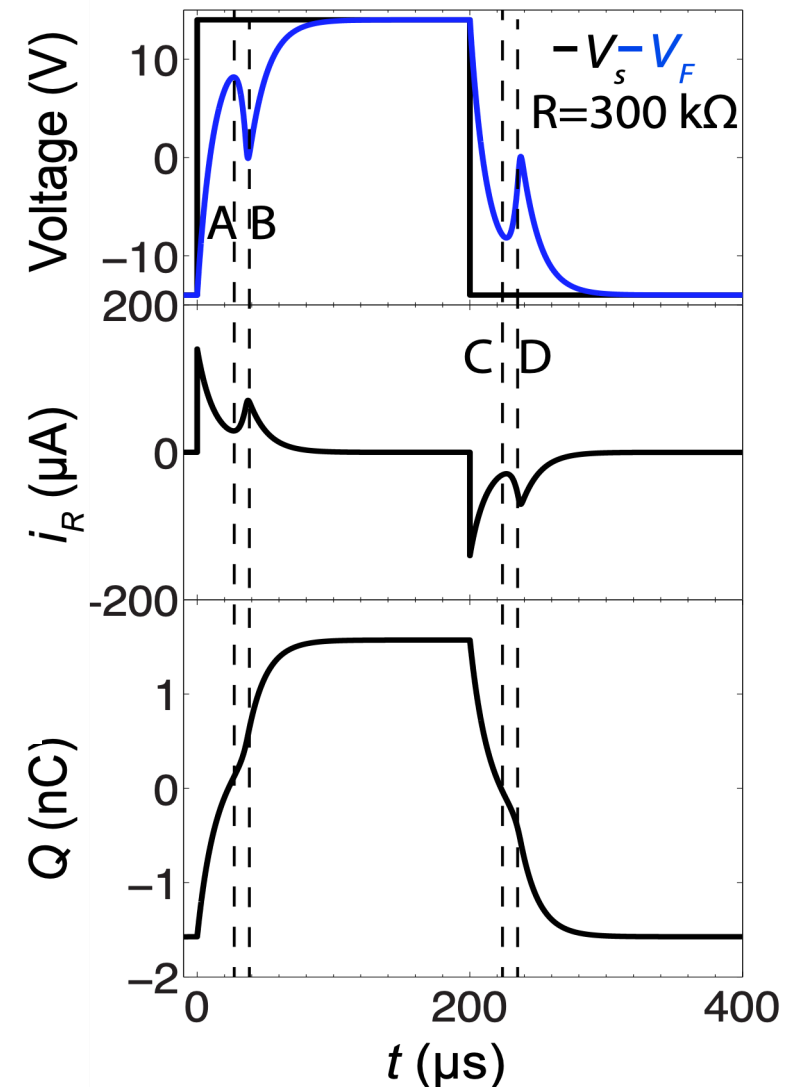
Equivalent Circuit



Simulated Circuit



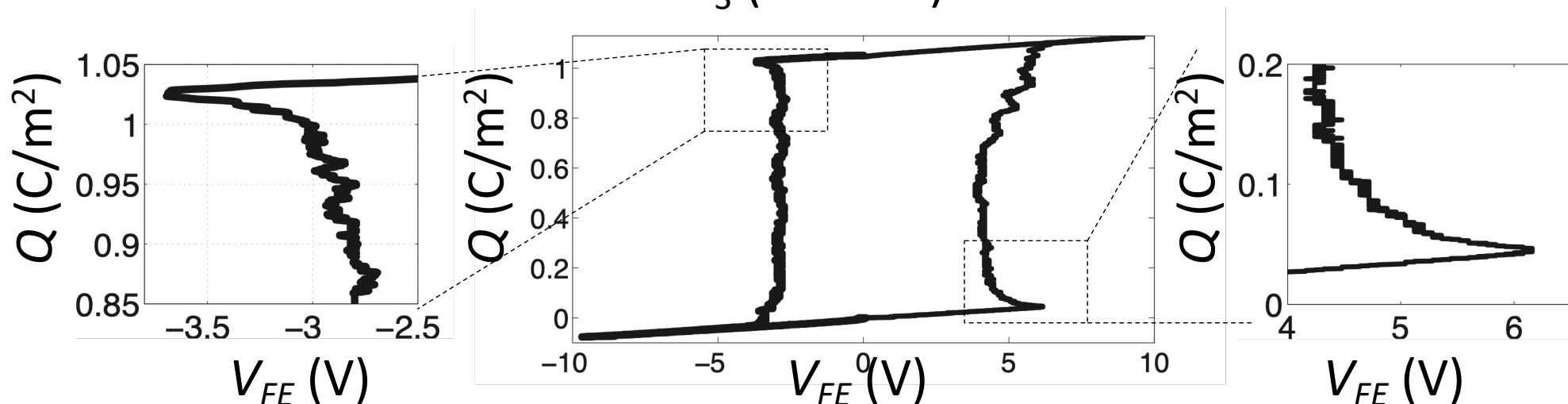
Qualitative match between simulated and experimental transients.



Negative Capacitance in Ferroelectrics

Negative Capacitance is a general phenomenon in ferroelectrics!

BiFeO₃ (300 nm)



Doped HfO₂:

- Hoffmann et al. Direct Observation of Negative Capacitance in Polycrystalline Ferroelectric HfO₂. Advanced Functional Materials 2016.
- Kobayashi et al. Experimental study on polarization-limited operation speed of negative capacitance FET with ferroelectric HfO₂. IEDM 2016

Perspective

Nature Nanotech. 3, 77 (2008)

NEWS & VIEWS

NANOELECTRONICS

Negative capacitance to the rescue?

Victor V. Zhirnov* and Ralph K. Cavin
are at the Semiconductor Research Corporation,
Research Triangle Park, North Carolina 27709, USA.
*e-mail: Victor.Zhirnov@src.org

At this point the proposed negative capacitance mechanism has not been observed in experiments, and it remains

Nature Materials 14, 137 (2015)

news & views

FERROELECTRICS

Negative capacitance detected

The experimental detection of negative capacitance in ferroelectrics rekindles hopes that the phenomenon could be used to further push the miniaturization of conventional transistors.

Gustau Catalan, David Jiménez and Alexei Gruverman

Negative Capacitance in 1956!

Landauer et al. Polarization reversal in the Barium Titanate hysteresis loops.

J. Appl. Phys. 27, 752 (1956)

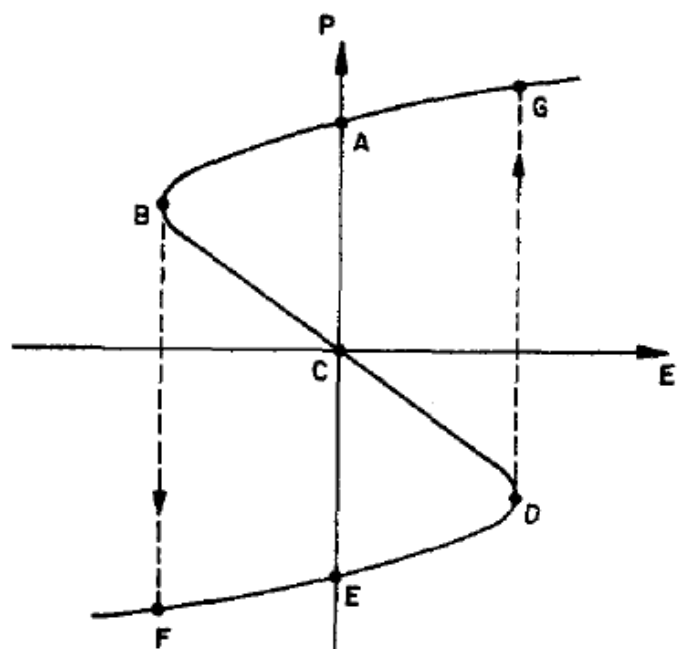


FIG. 1. Plot of polarization against field, at room temperature, as given by Devonshire's theory. P =polarization, E =field. Direction of field and polarization are taken to be the same, and parallel to the "c" axis of a single domain crystal. The solid line $GAB CDEF$ gives the theoretical relationship.

Merz. Switching time in BaTiO₃ and its dependence on its crystal thickness. **J. Appl. Phys.** 27, 938 (1956)

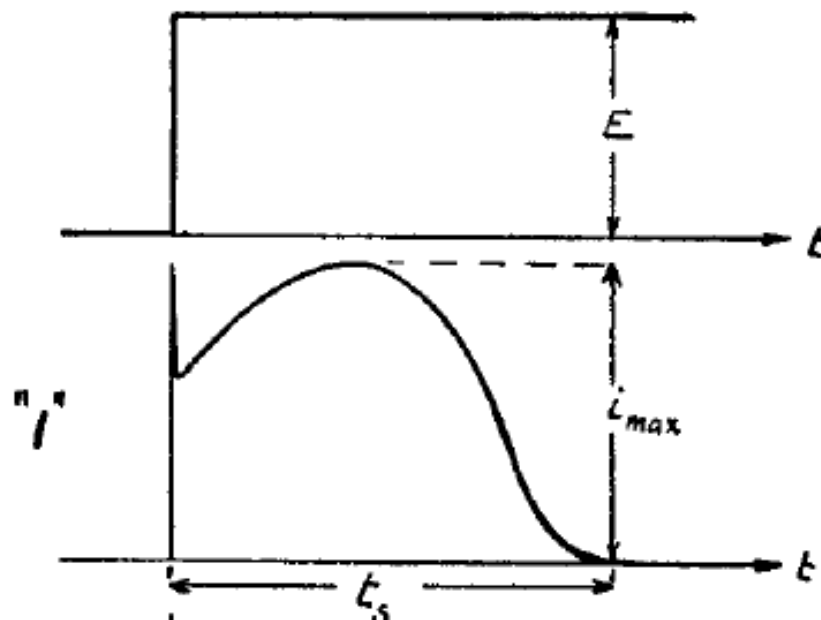
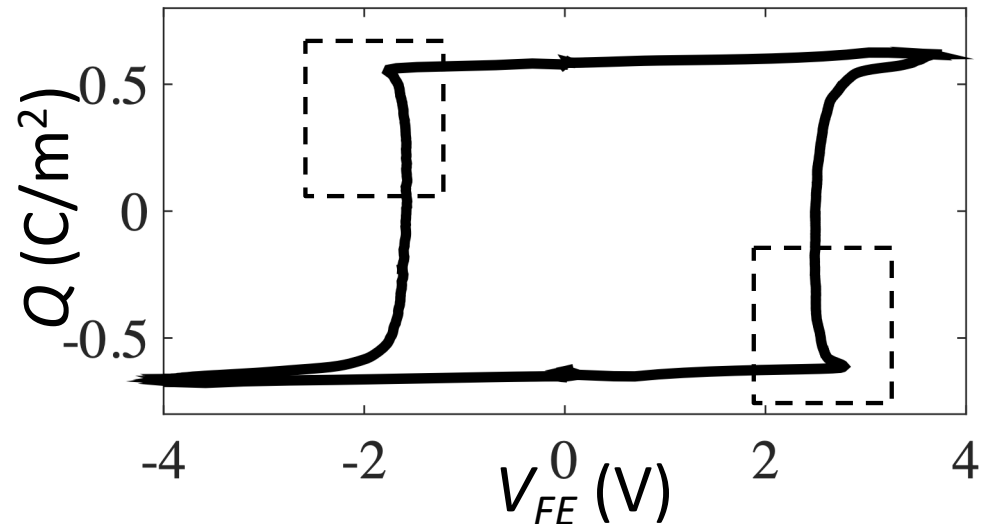


FIG. 1. Pulsing field and pulsing current *versus* time.

What's next?

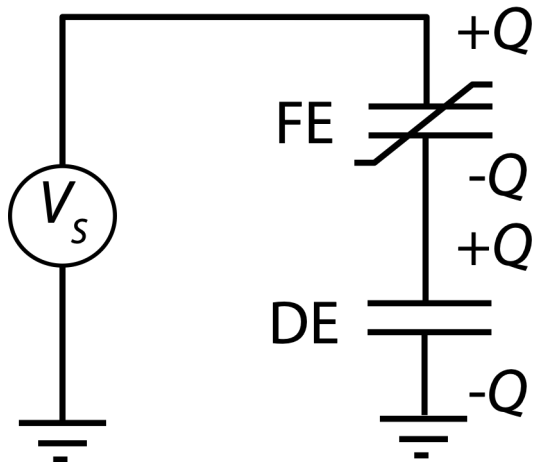
Now that negative capacitance has been detected...



1. Can we stabilize negative capacitance in a ferroelectric?
2. Can we see capacitance enhancement and voltage amplification?
3. Can we see sub-60 mV/decade of subthreshold swing?

Stabilization of Negative Capacitance

Ferroelectric-Dielectric
Series Combination



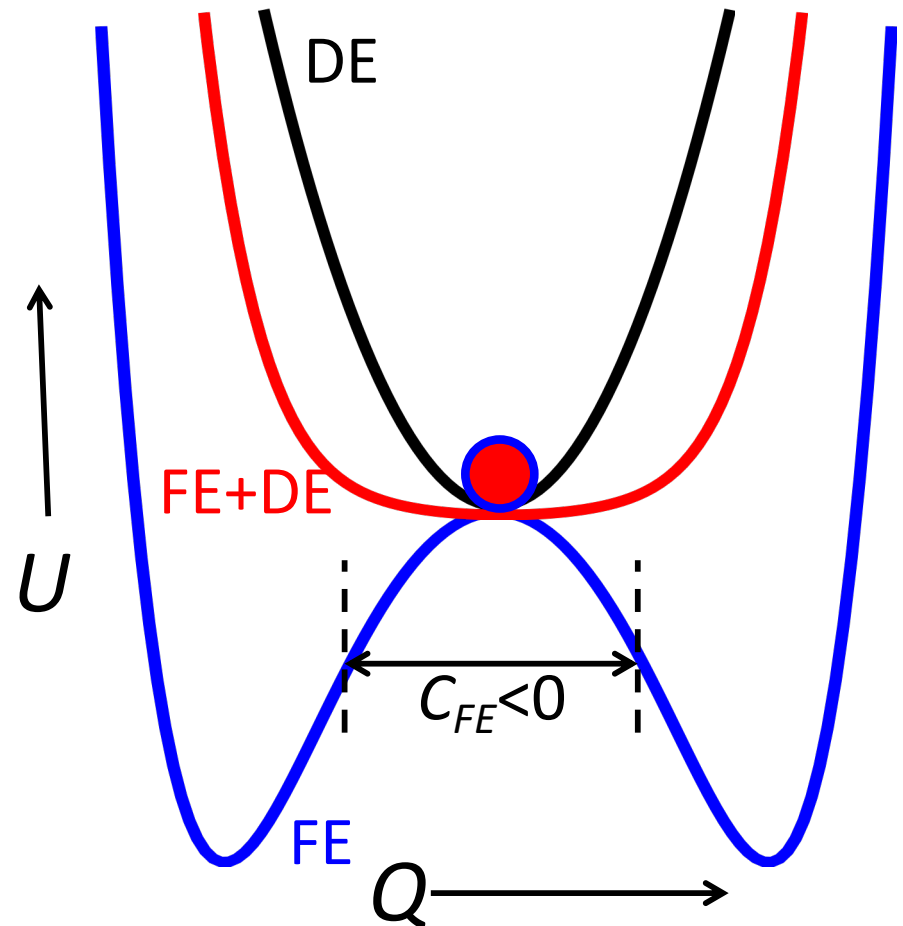
$$C = \left[\frac{d^2 U}{dQ^2} \right]^{-1}$$

Equivalent Capacitance

$$C_{FE+DE} = \left[\frac{1}{C_{FE}} + \frac{1}{C_{DE}} \right]^{-1}$$

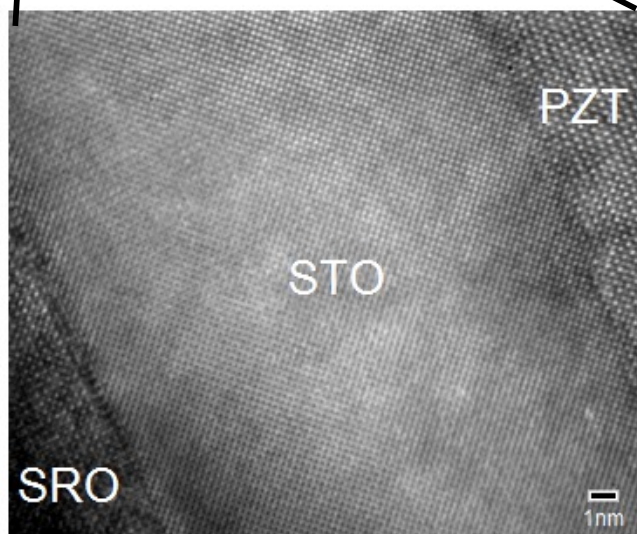
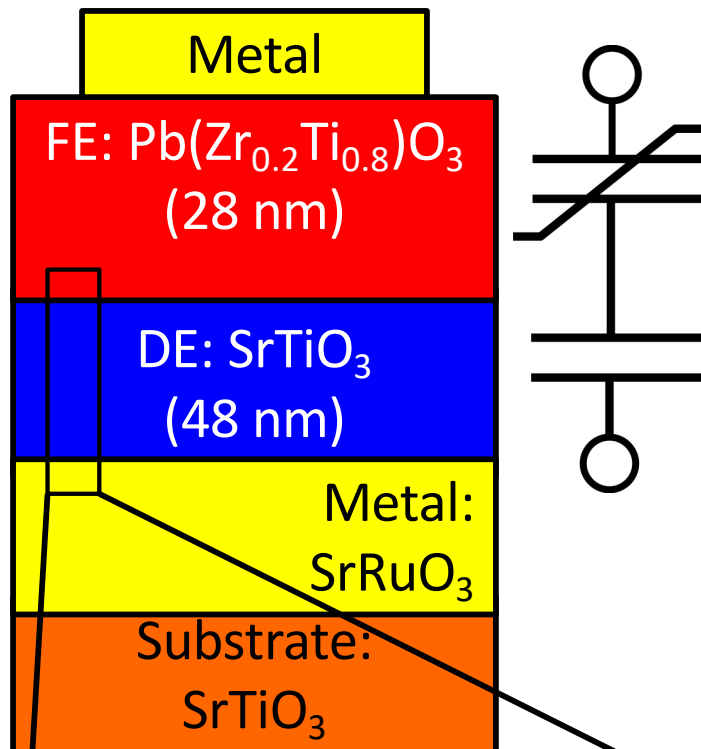
$$C_{FE+DE} > C_{DE} \quad \text{if} \quad C_{FE} < 0$$

Energy Landscapes

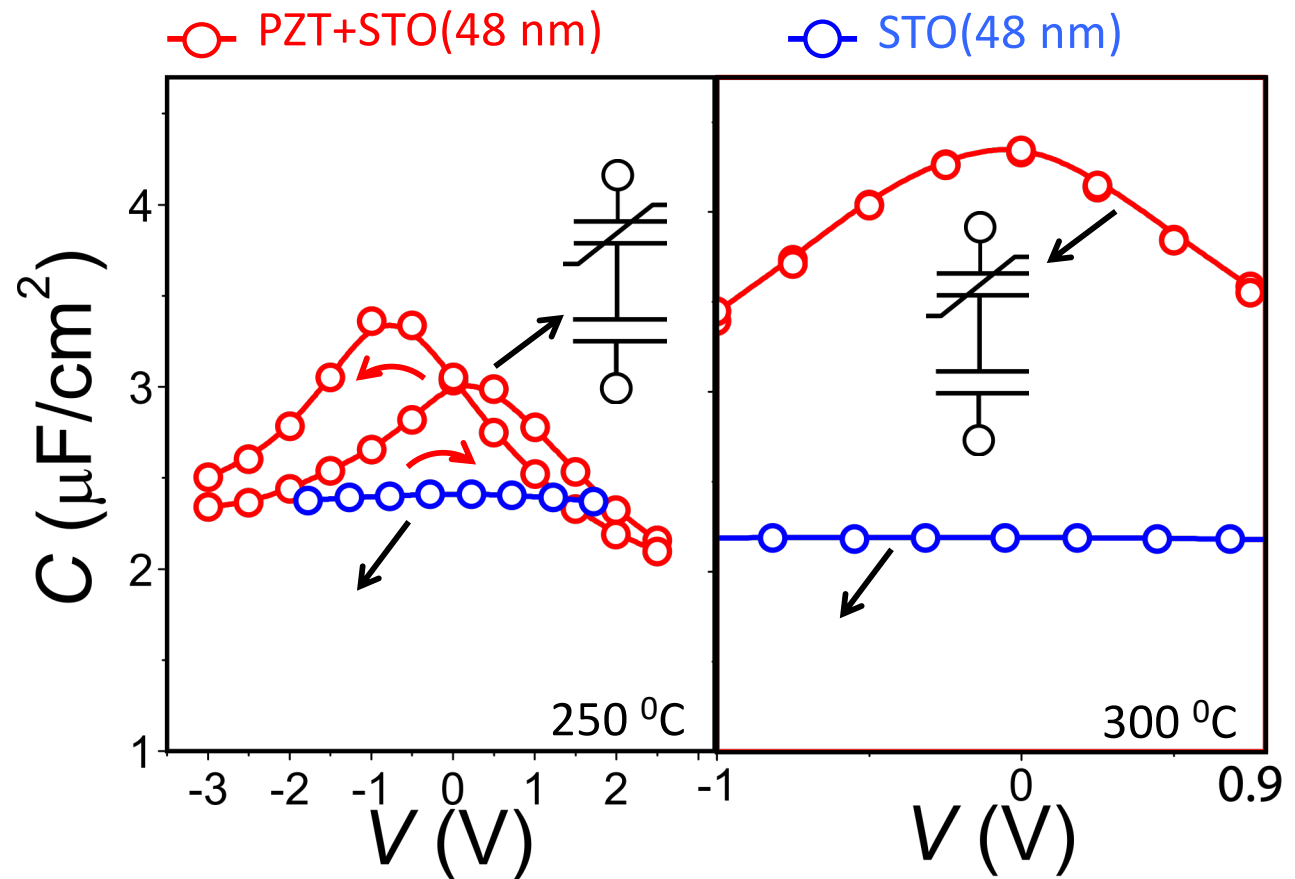


Enhancement of capacitance in a FE-DE Series combination!

Stabilization of Negative Capacitance



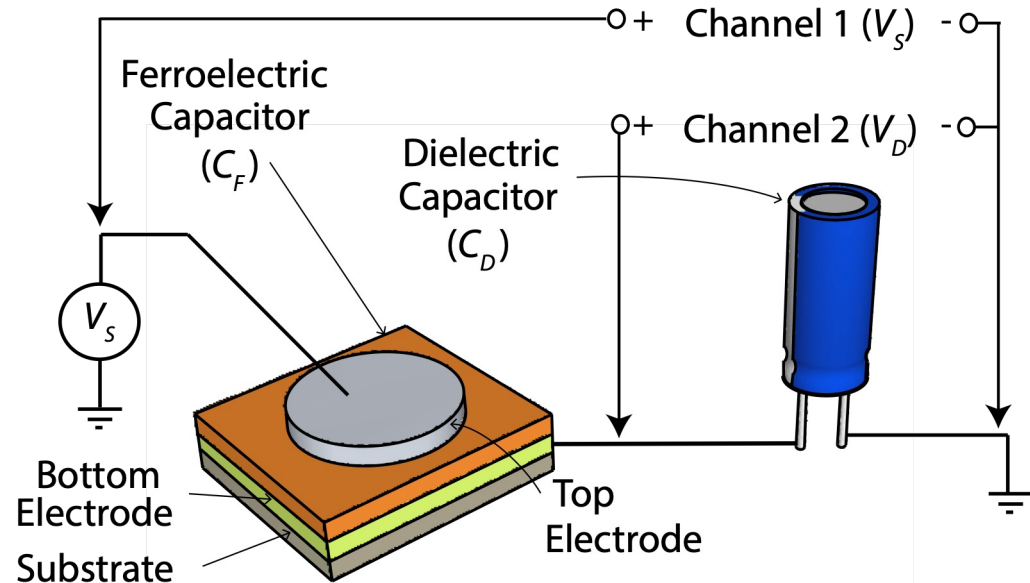
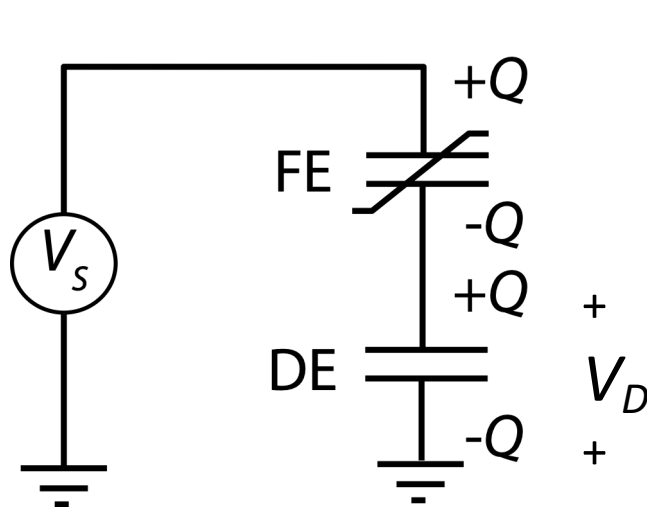
C-V Characteristics



Capacitance Enhancement in a FE-DE heterostructure experimentally observed!

Khan et al. Appl. Phys. Lett. 100, 113501 (2011)
(cover article, most downloaded Aug/Sep 2011, Editor's choice)
Gao, Khan et al. Nano Lett. 14, 5814 (2014)

Voltage Amplification in a FE+DE System

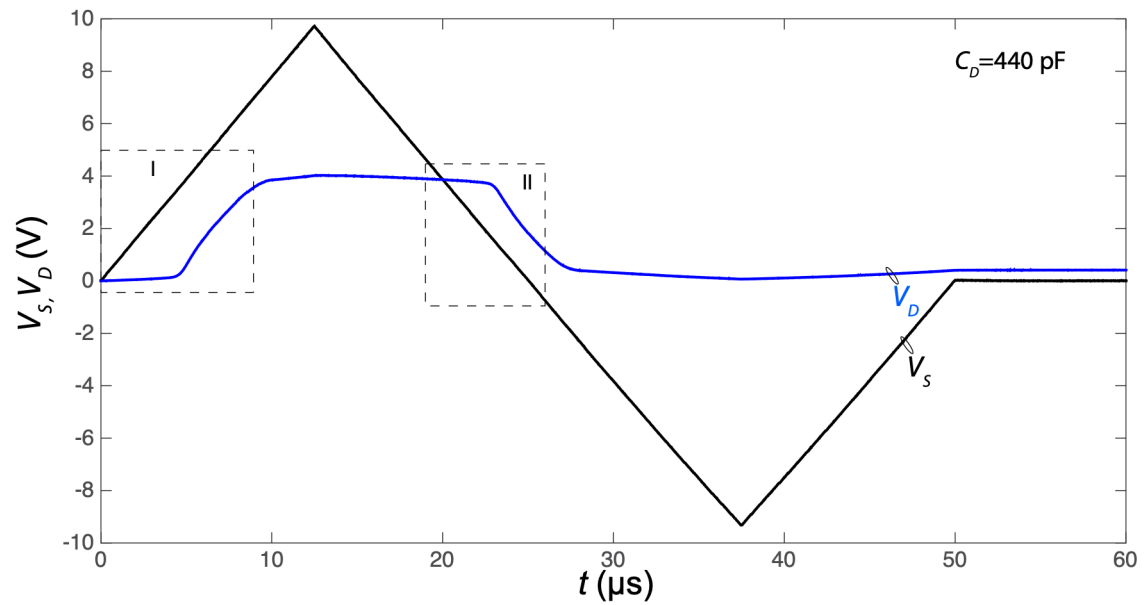
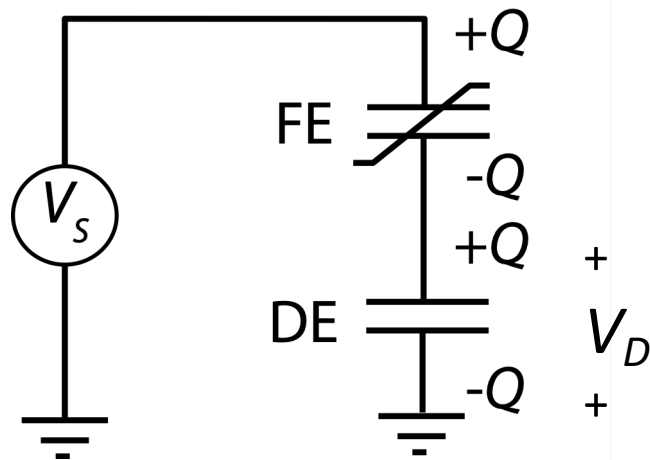


Voltage amplification:
$$\frac{dV_D}{dV_S} = \frac{C_F}{C_F + C_D} = \frac{|C_F|}{|C_F| - C_D}$$

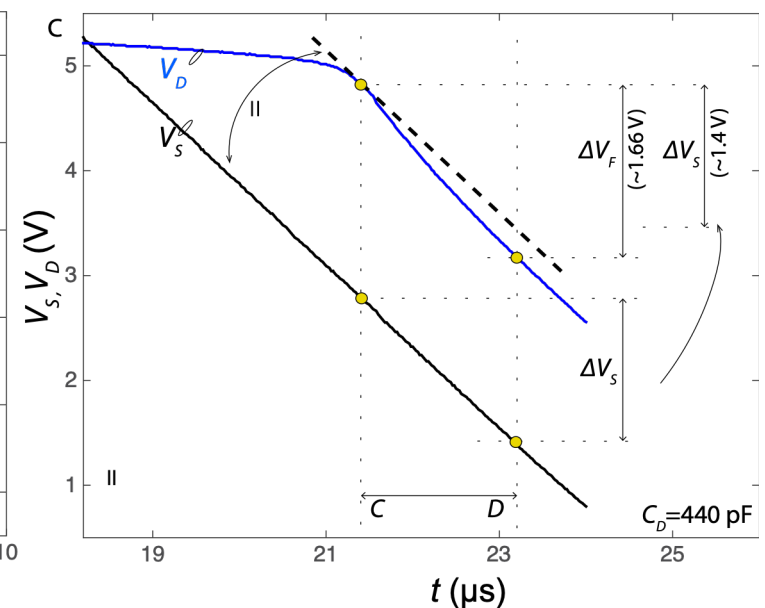
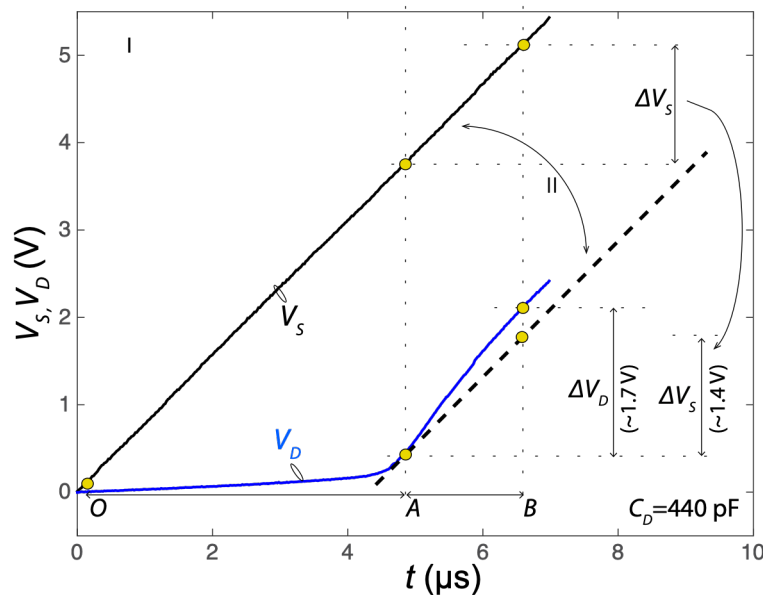
If $|C_F| > C_D$,
$$\frac{dV_D}{dV_S} > 1$$

Voltage amplification can be possible in passive ferroelectric networks!

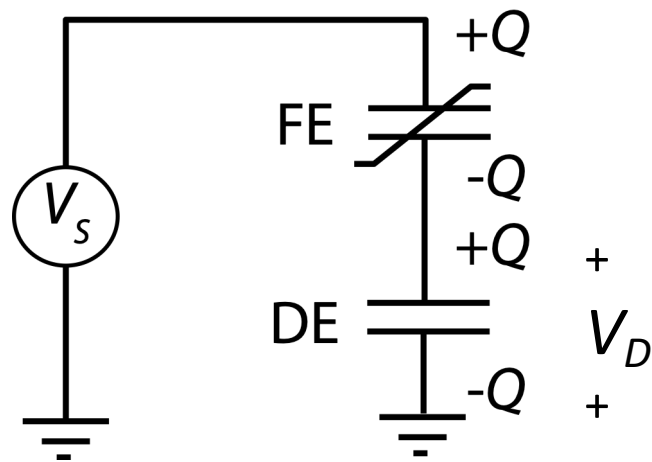
Ferroelectric+Dielectric Capacitor



Khan, Hoffmann
et al. (under
review)



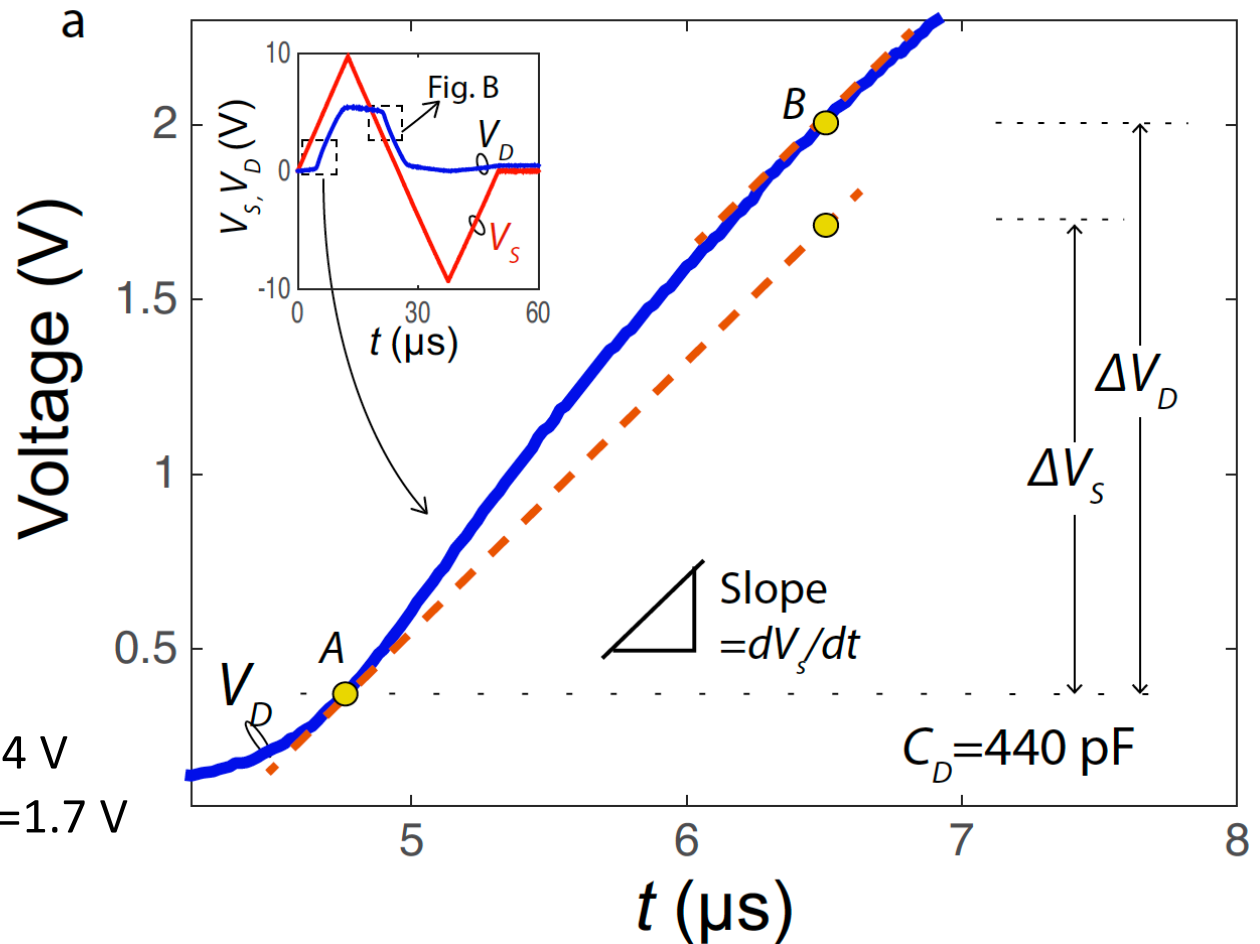
Ferroelectric+Dielectric Capacitor



Khan, Hoffmann et al. (under review)

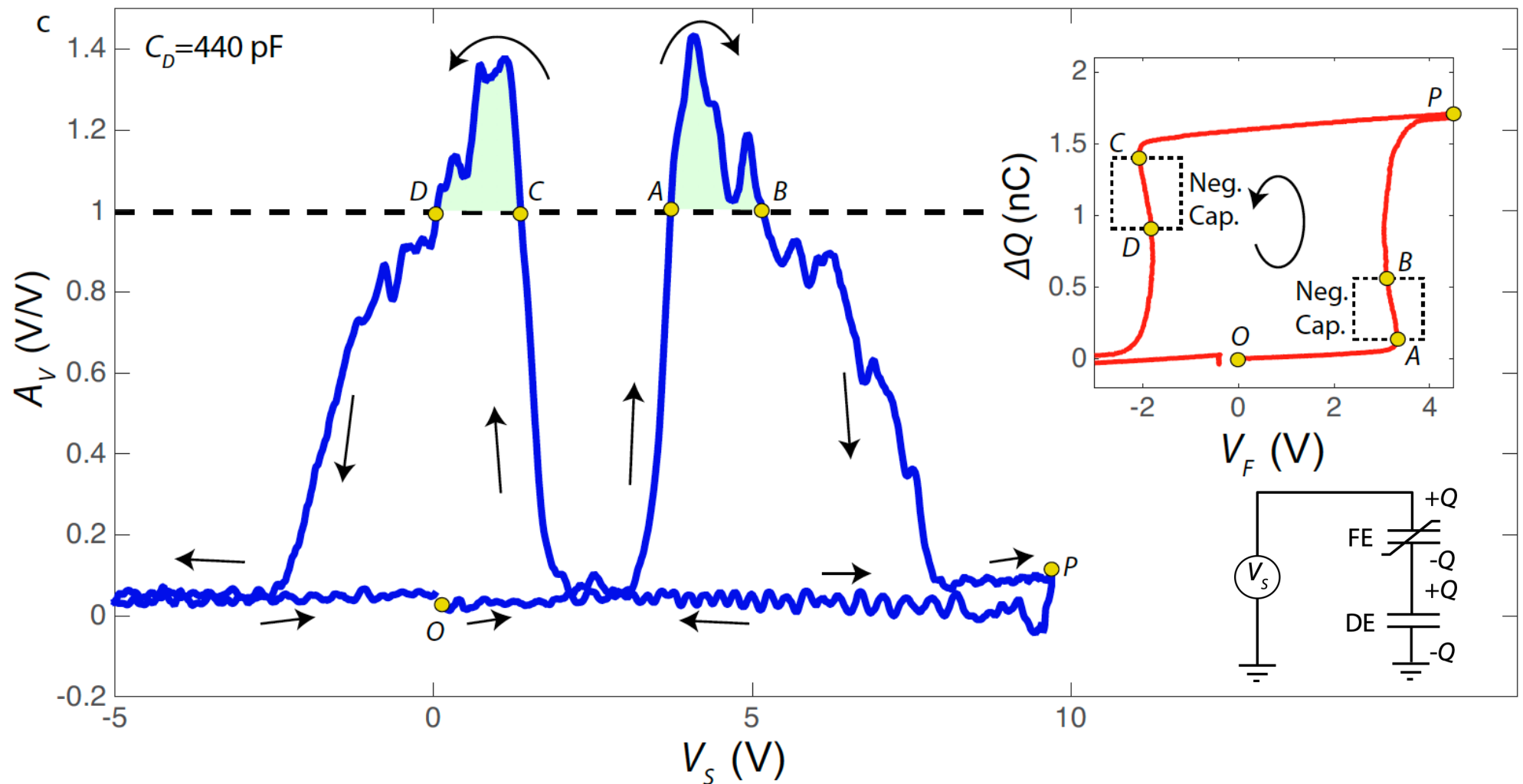
Change in source voltage $\Delta V_S = 1.4 \text{ V}$
 Change in dielectric voltage $\Delta V_D = 1.7 \text{ V}$

$$\frac{\Delta V_D}{\Delta V_S} = \frac{1.7 \text{ V}}{1.4 \text{ V}} \approx 1.21 = \frac{|C_F|}{|C_F| - C_D}$$



Voltage amplification in a FE-DE series network experimentally observed!

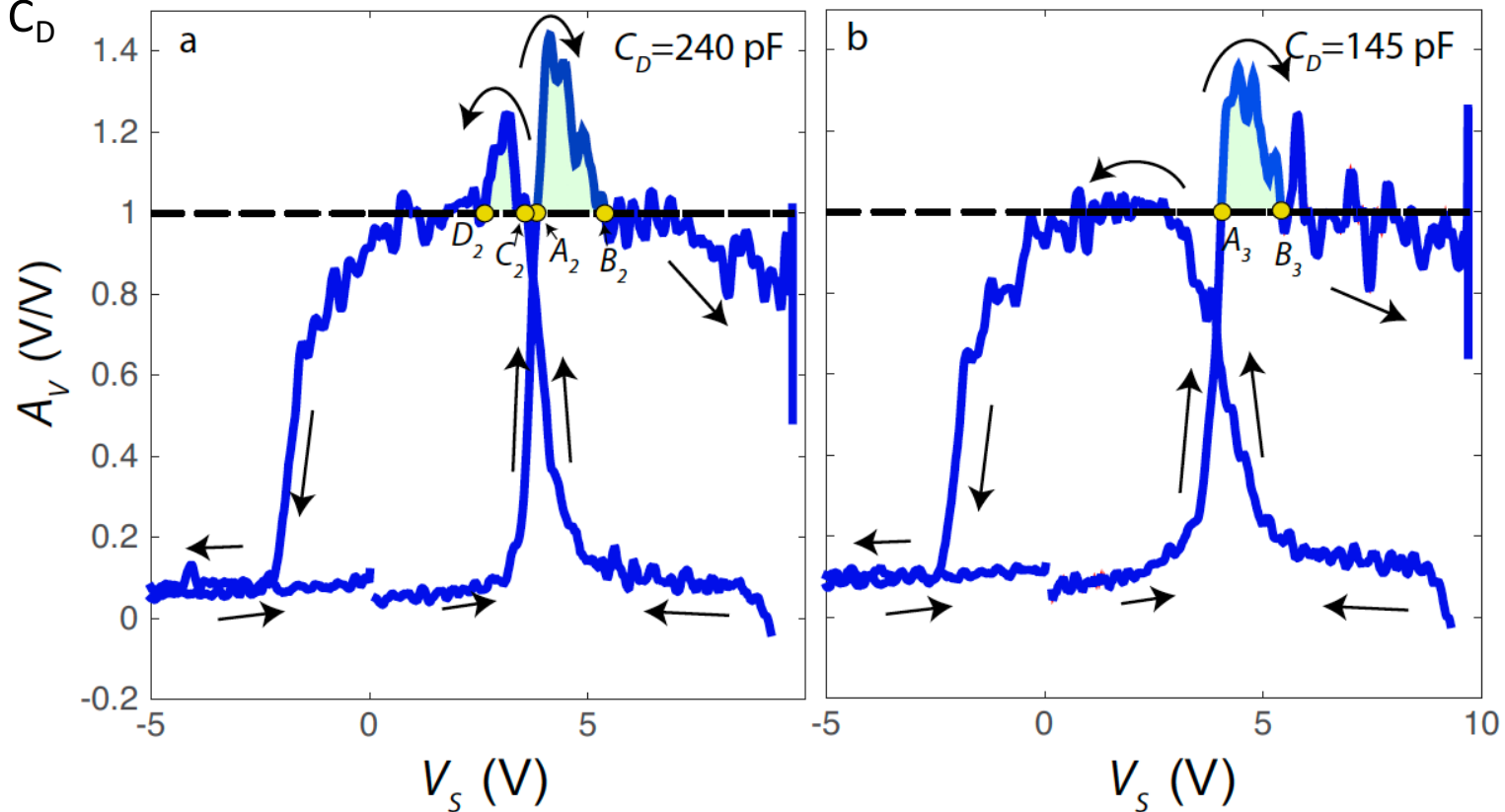
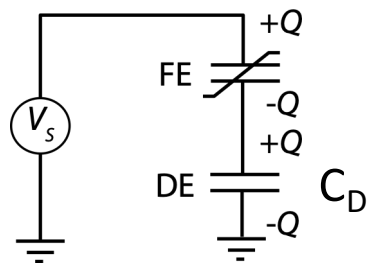
Ferroelectric+Dielectric Capacitor



Khan, Hoffmann
et al. (under
review)

Voltage amplification in a FE-DE series
network experimentally observed!

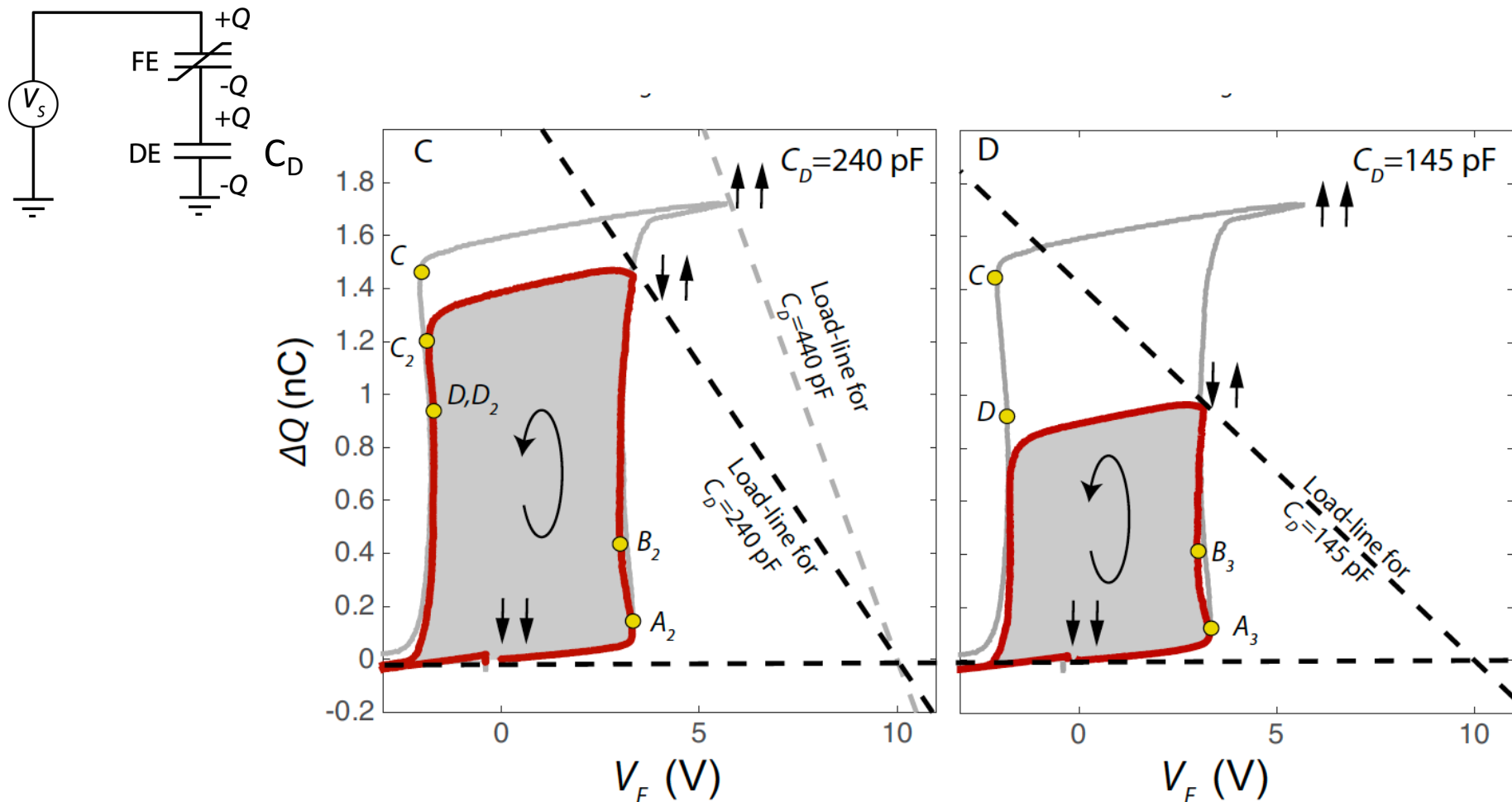
Ferroelectric+Dielectric Capacitor



Khan, Hoffmann
et al. (under
review)

Voltage amplification reduces in negative
voltage sweeps with the reduction of C_D !

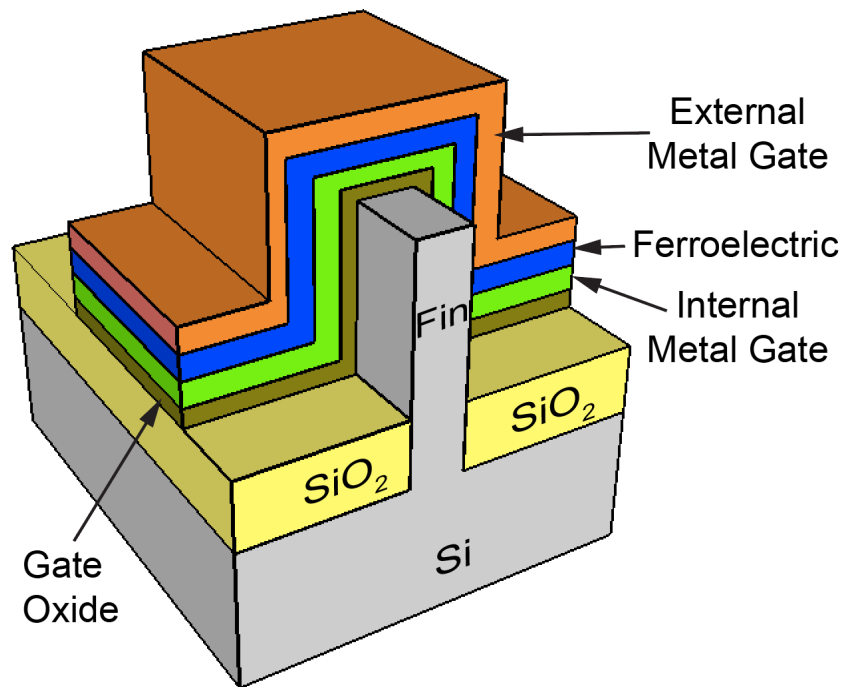
Ferroelectric+Dielectric Capacitor



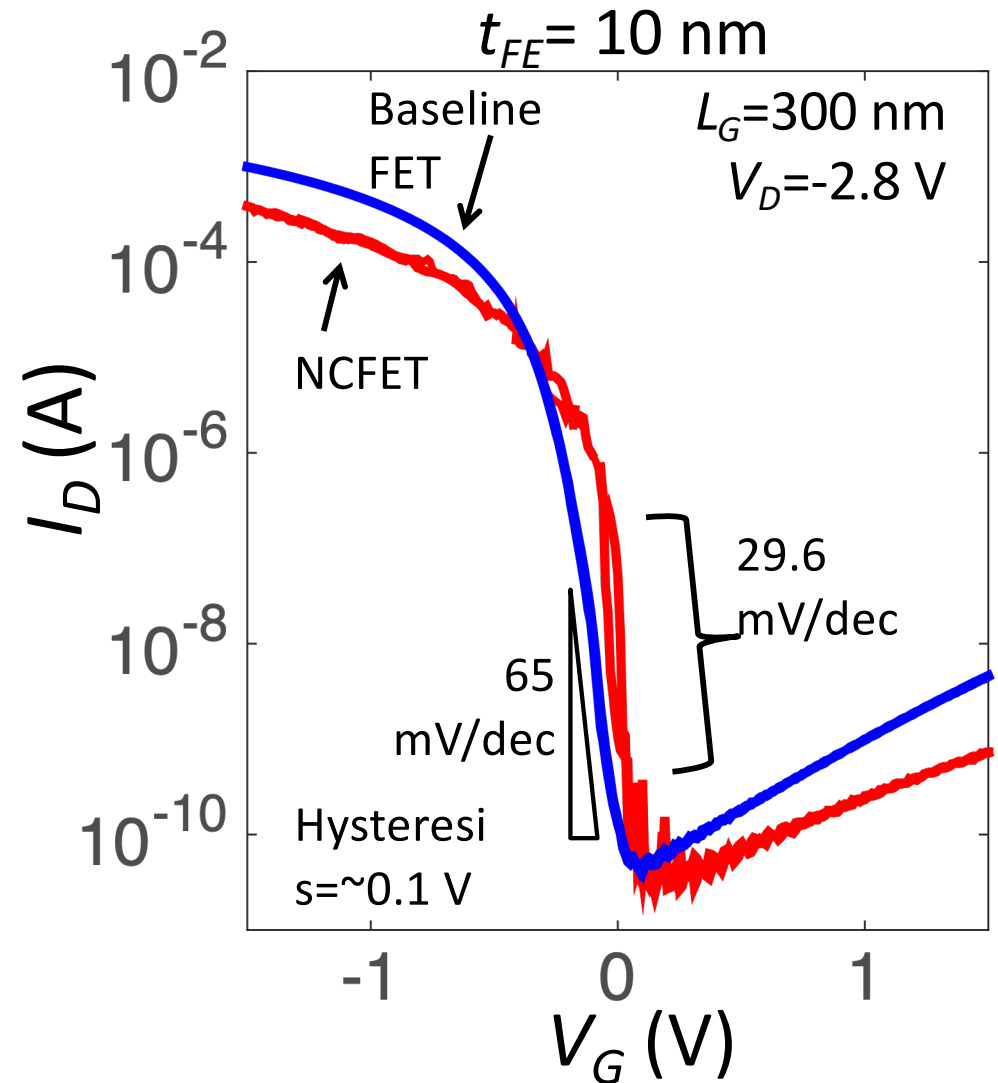
Khan, Hoffmann
et al. (under
review)

Domain mediated mechanisms are at play!
Initial epochs of switching show negative
capacitance!

Negative Capacitance Transistor



Khan et al. (unpublished)



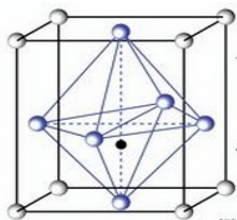
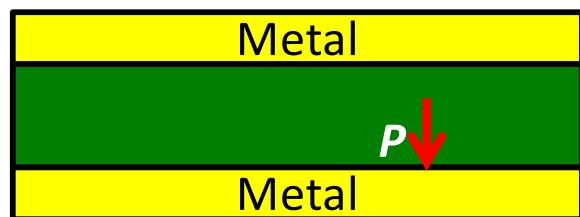
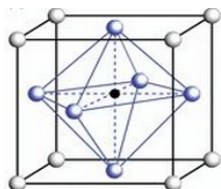
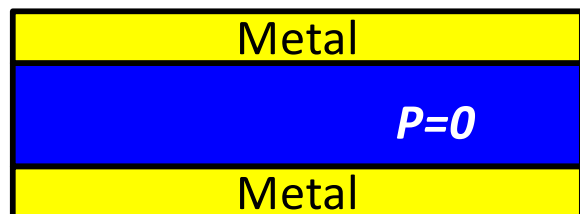
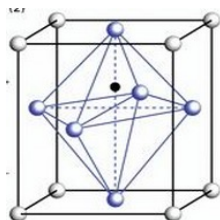
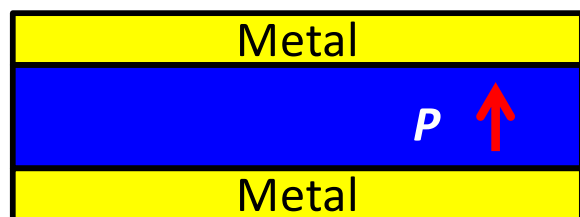
Negative capacitance can reduce subthreshold swing below 60 mV/dec!

What do we need to know?

- 1. Domains:** What role do domain mechanisms play for negative capacitance?
 - We do not have a good model for negative capacitance in the presence of domain mechanisms.
- 2. Speed:** What the fundamental time scale for negative capacitance mechanisms?
 - Sub-ps?
- 3. NCFET Designs:** How to design an NCFET? ~ 0 mV/decade swing?
 - Internal metal gate: no!
 - Make $|C_{FE}| = C_{MOS}$, Parasitic capacitances help!

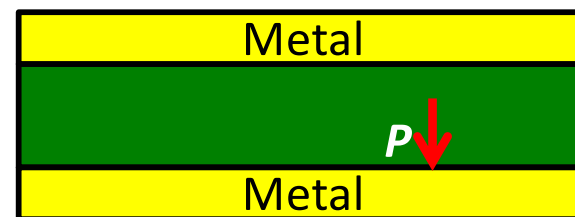
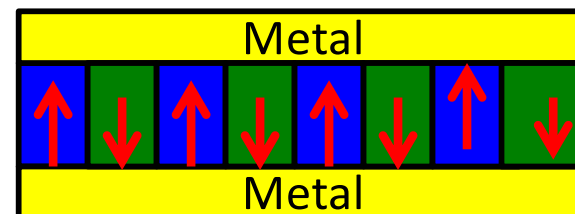
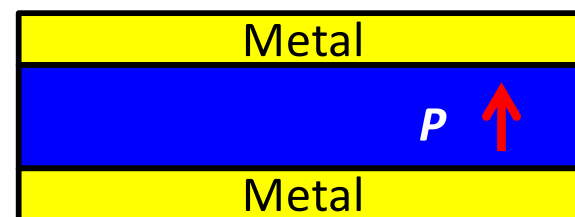
Domain mechanisms

Ideal



Landau type switching
Single domain switching
Intrinsic switching

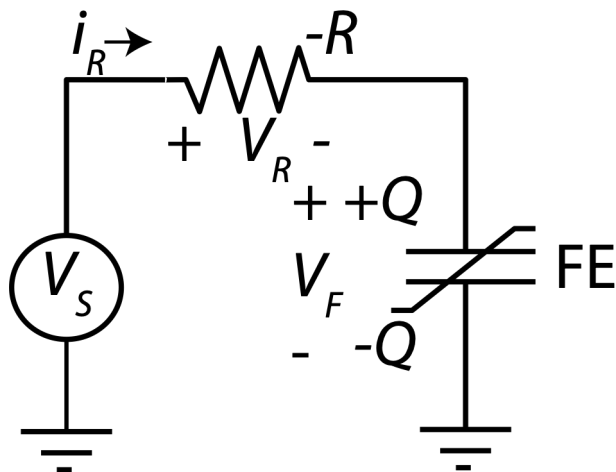
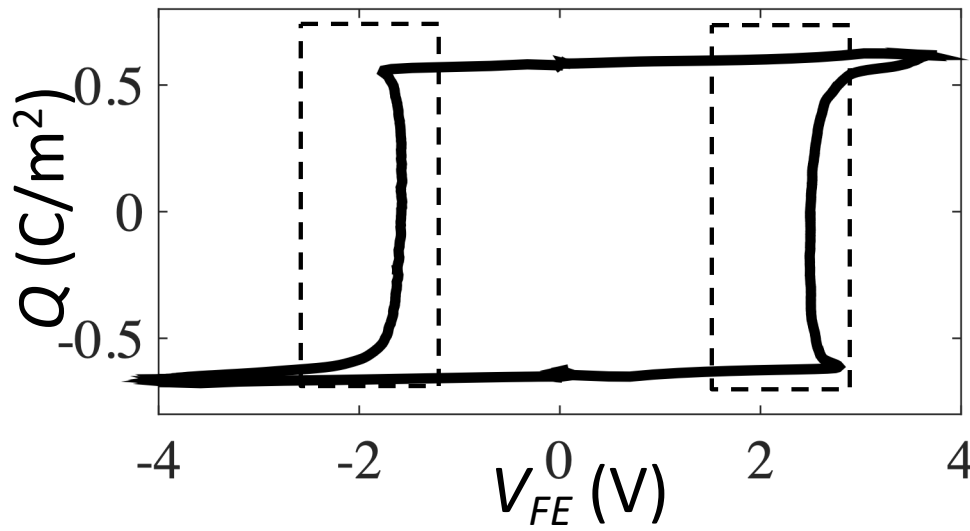
In reality



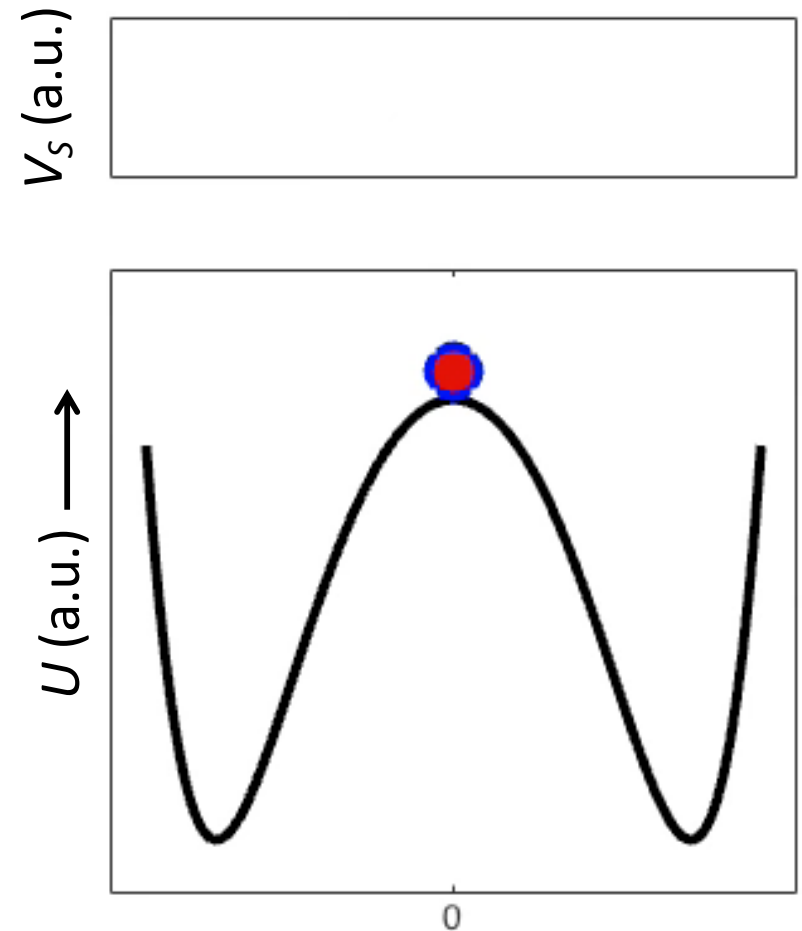
We do not have a good model for negative capacitance in the presence of domain mediated mechanism of switching

Domain mechanisms

There are lots of physics in the transition edges!



Energy Landscape



THANKS

Ferroelectrics and Negative Capacitance:

What do we know and
What do we need to know?

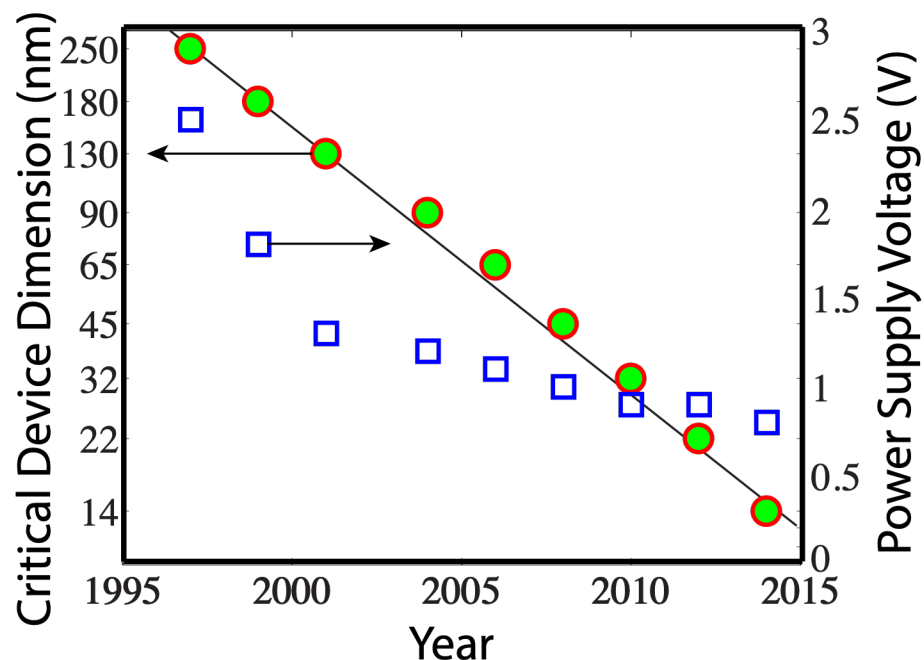
QUESTIONS
&
COMMENTS



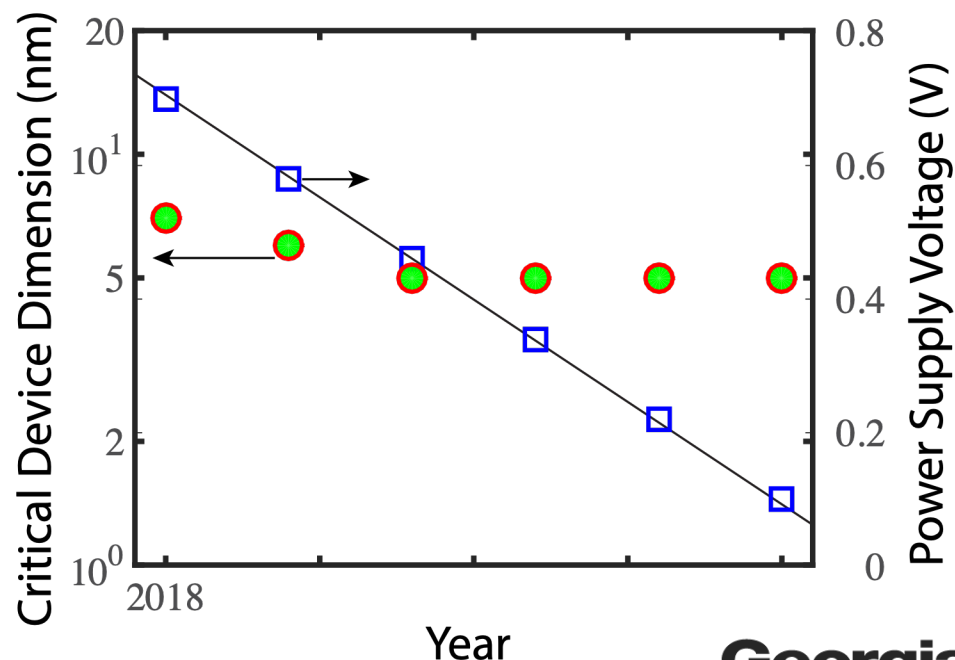
Summary

- **Negative capacitance detected!**
- **Amplification due to negative capacitance demonstrated!**
- **Sub-60 mV/decade subthreshold switching is possible!**

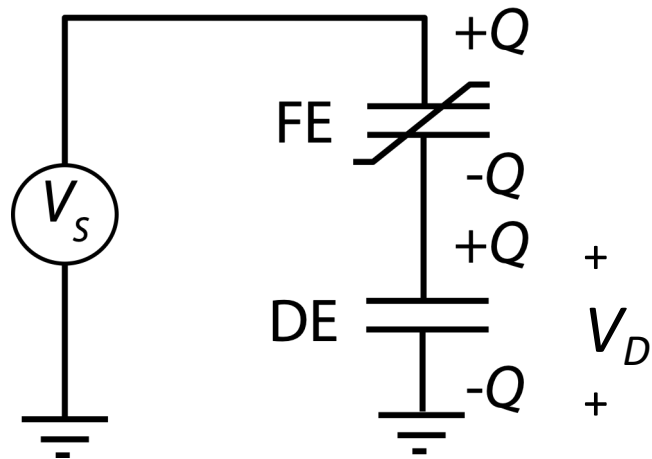
Current CMOS Technology



Beyond CMOS Technology

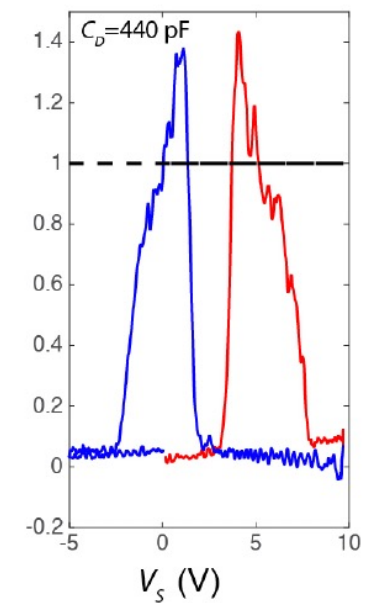
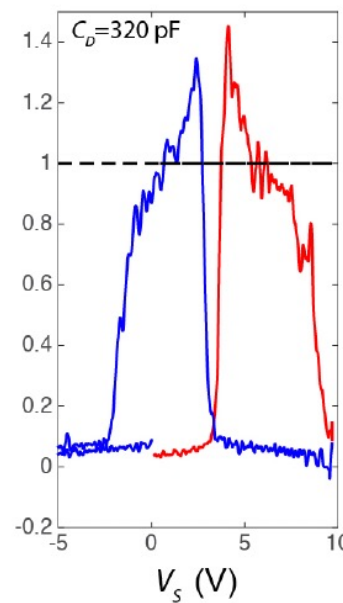
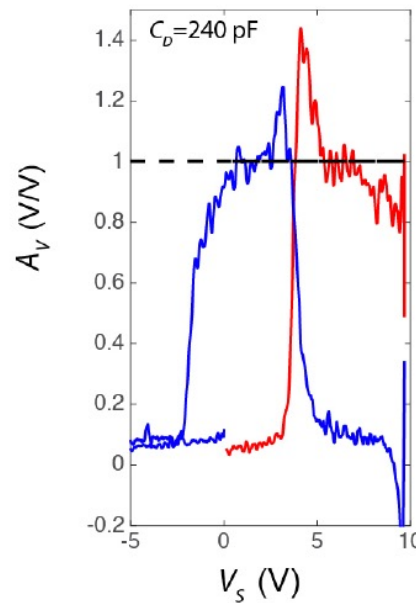
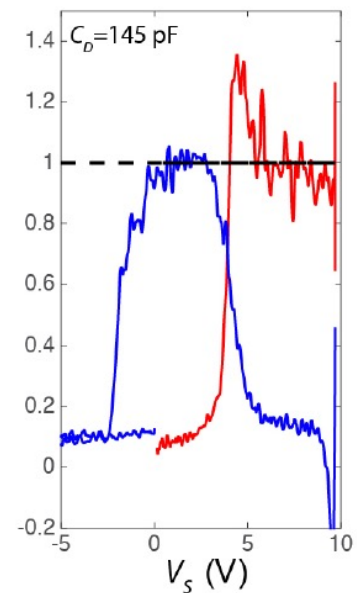
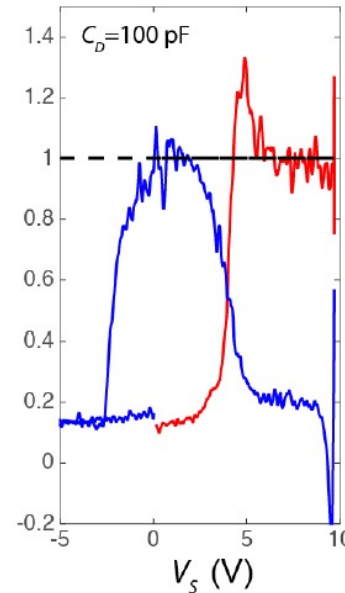
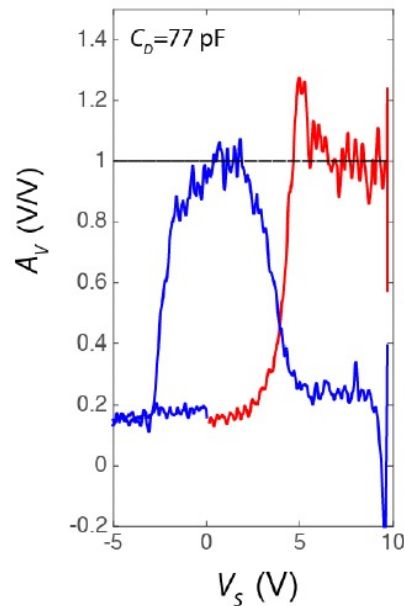


Ferroelectric+Dielectric Capacitor



Khan, Hoffmann et al.
(under review)

Voltage amplification
reduces in negative
voltage sweeps with
the reduction of C_D !



Speed

What is the speed of negative capacitance mechanisms?

1. 10-100 ns in HFO2 FETs

Müller et al. Nanosecond Polarization Switching and Long Retention in a Novel MFIS-FET Based on Ferroelectric HfO₂. IEEE Electron Devices Letters 33, 185 (2011).

2. ~220 ps in micron size PZT capacitors:

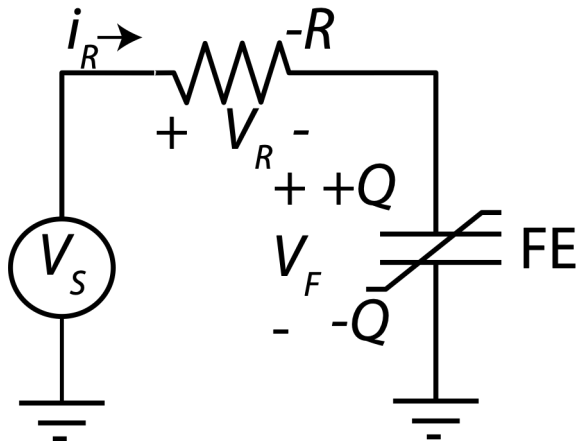
Li et al. Ultrafast polarization switching in thin-film ferroelectrics. Applied Physics Letters 84, 1175 (2004).

3. ~ps scale atomic displacement in BaTiO₃:

Chen et al. Ultrafast terahertz-field-driven ionic response in ferroelectric BaTiO₃. Physical Review B 94, 180104(R) (2016).

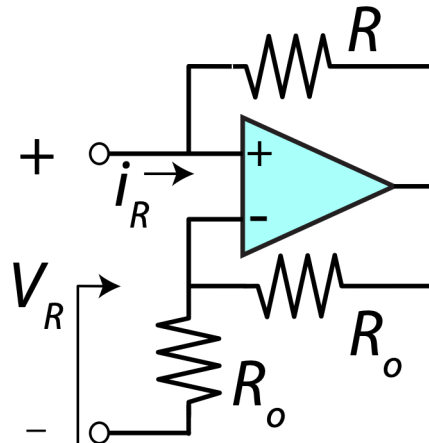
Dynamic Stabilization of an Unstable State

Circuit Diagram



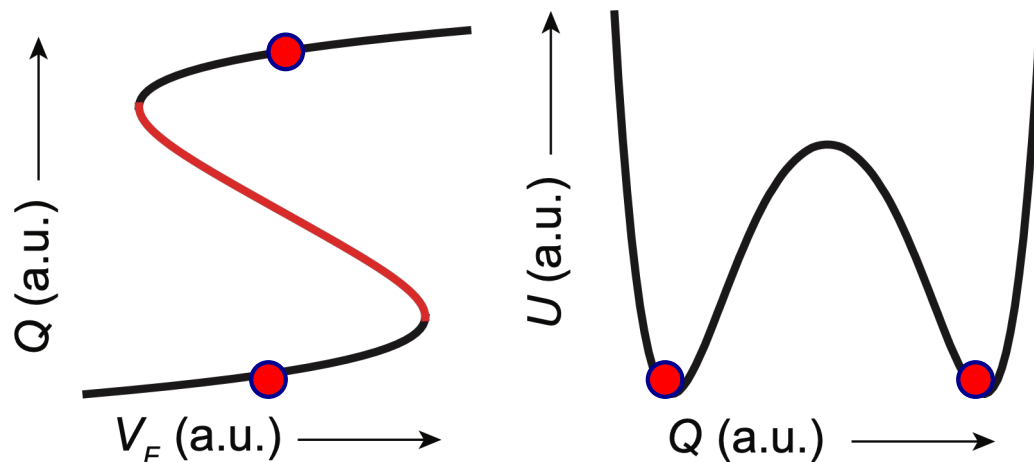
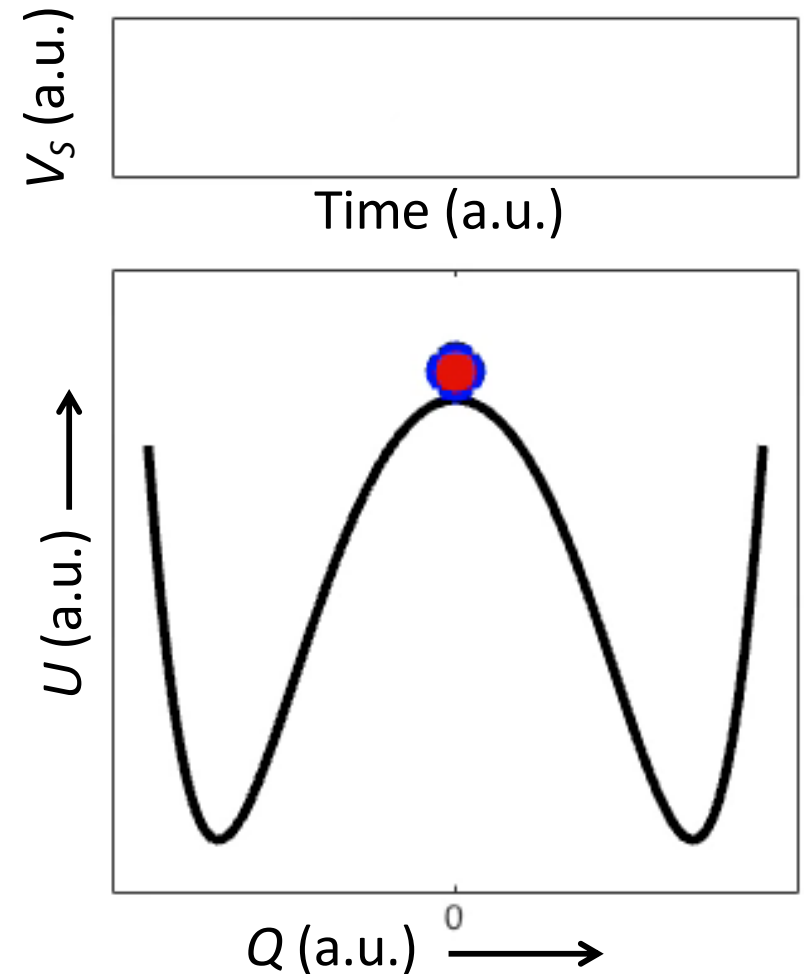
$$\text{KVL: } V_S = -R \frac{dQ}{dt} + V_F(Q)$$

Negative Resistor



$$R_{in} = \frac{V_R}{i_R} = -R$$

Energy Landscape



New technique for stabilization

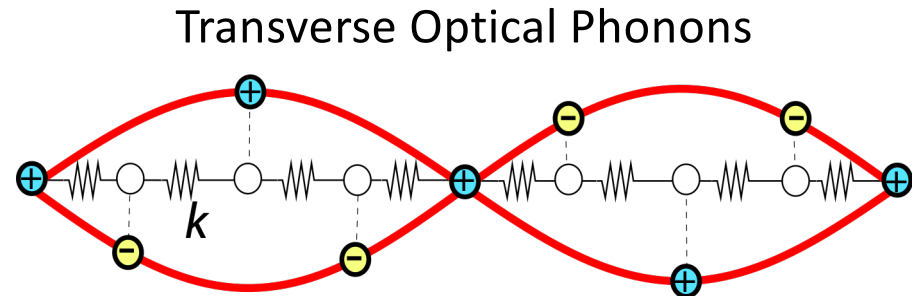
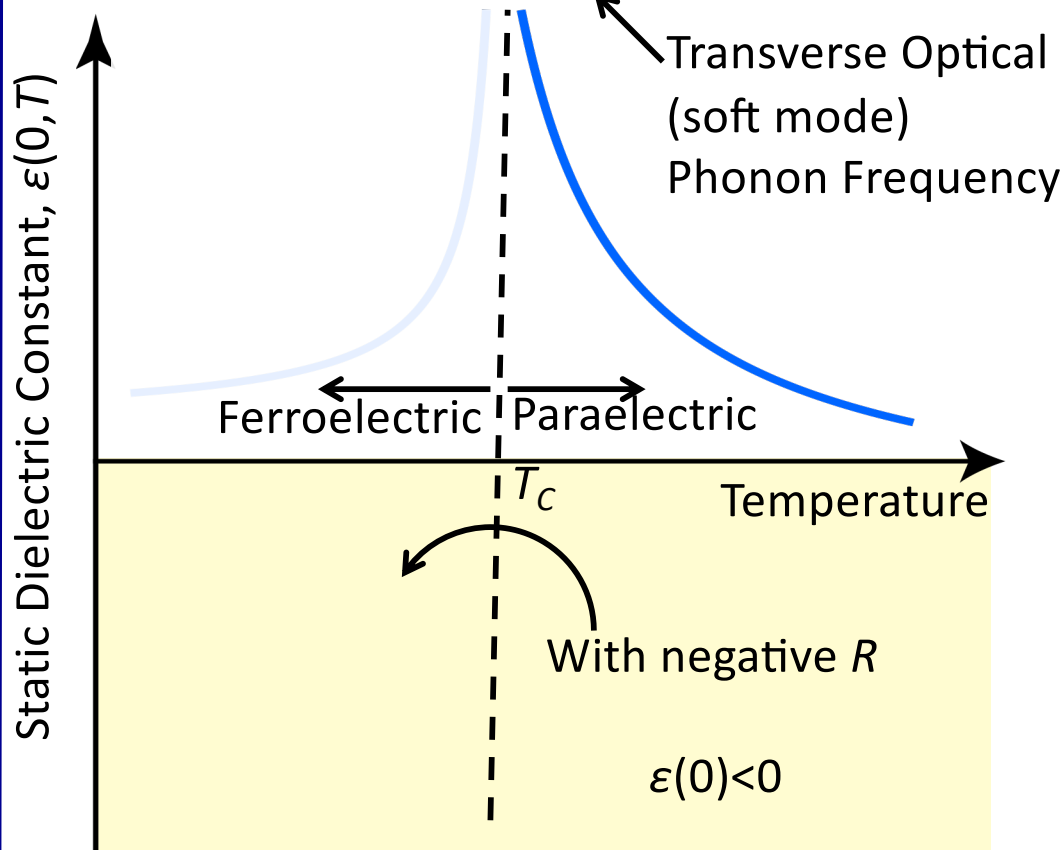
How does the physics change?

Ferroelectricity occurs due to certain vibration modes called soft mode phonons.

$\epsilon(0, T)$ = Static Dielectric Constant at T

Lydanne-Sachs -Teller Relation $\frac{\epsilon(0, T)}{\epsilon(\infty)} = \frac{\omega_L^2}{\omega_T^2(T)}$

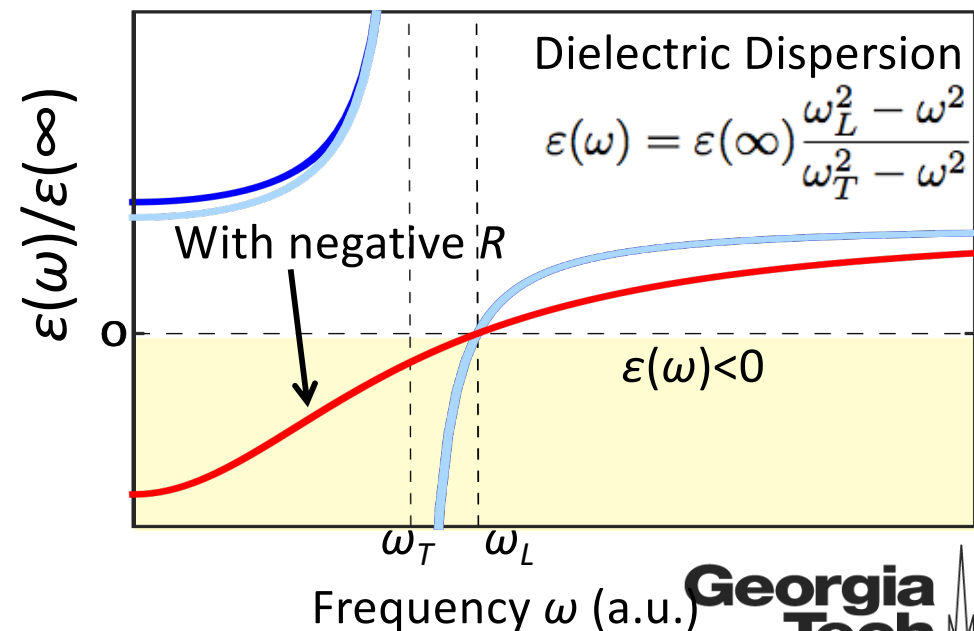
Longitudinal Phonon Frequency



Soft mode theory:
Cochran. Adv. Phys
9, 387 (396)

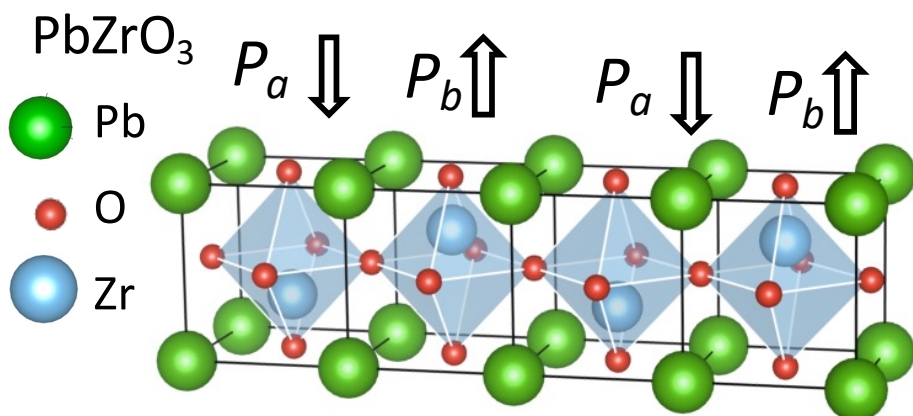
If $\epsilon(0) < 0$, $\omega_T^2 \approx \frac{k}{m_{eq}} < 0$
and, hence, $k < 0$

Revisit the soft-mode theory of ferroelectrics!

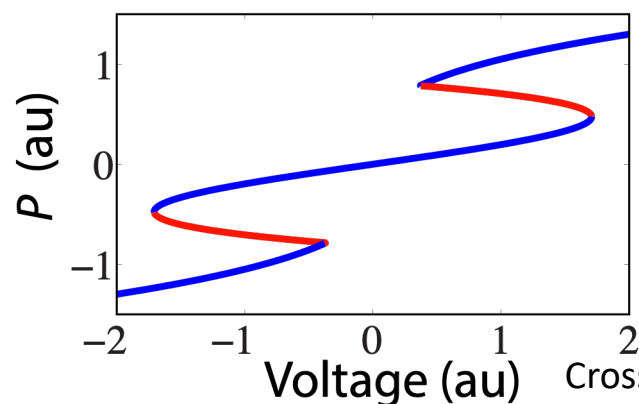


Experiments & Beyond Ferroelectrics

Antiferroelectric Oxides



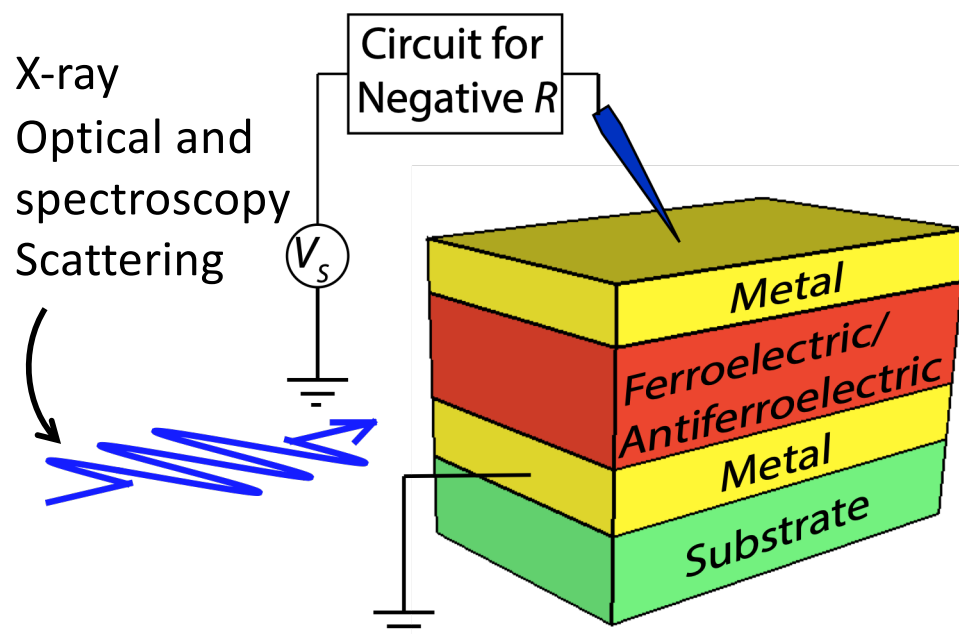
Charge Voltage Characteristics



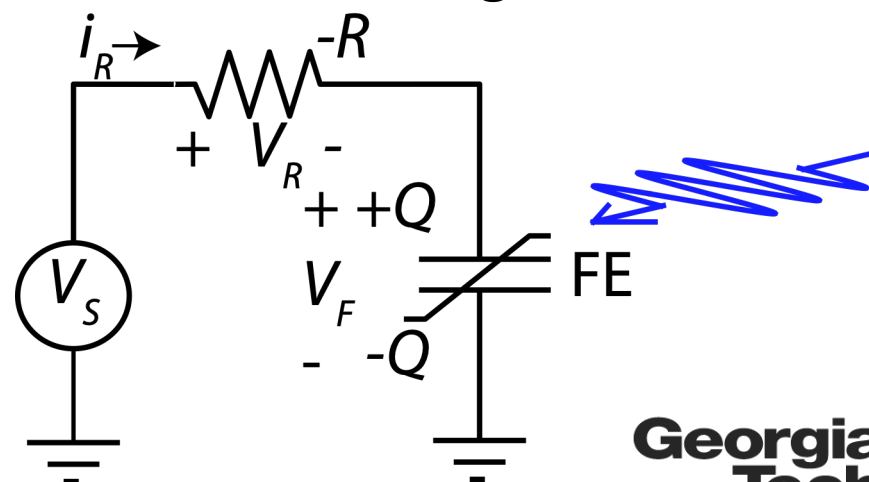
Cross . J. Jap. Soc. Phys.
23, 77 (1967)

- Complex Oxide Antiferroelectrics
 - $\text{Pb}(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$ and doped variants
 - BeFeO_3 and doped variants
- CMOS Compatible $\text{Hf}_x\text{Zr}_{1-x}\text{O}_2$

Possible Experimental Setup



Circuit Diagram



Roadmap to Future

Electronics with Ferroelectrics and
Unconventional Oxides

Technology
Development

Fundamental
Science

Materials Growth

Complex Oxides
(Pulsed Laser Deposition)

Transition Metal Binary Oxide
(Atomic Layer Deposition)

Nano-Fabrication

Heterostructures

Nanostructures

Devices

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Ian Young, Intel
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Fellowship 2012-13



Office of
Naval Research



STARNET LEAST



NSF E3S Center

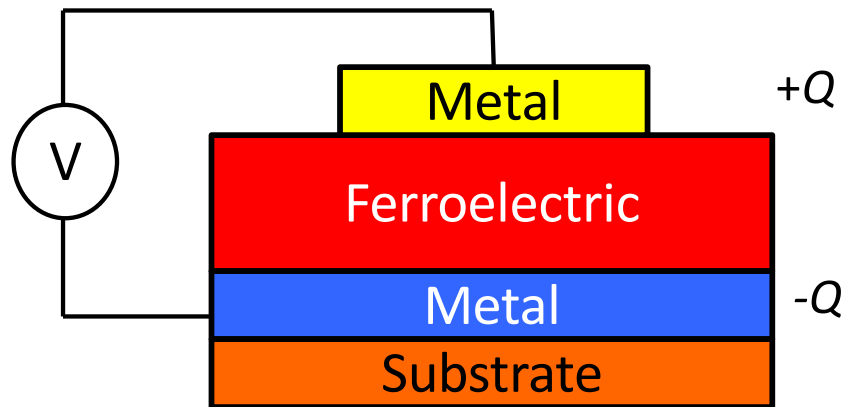
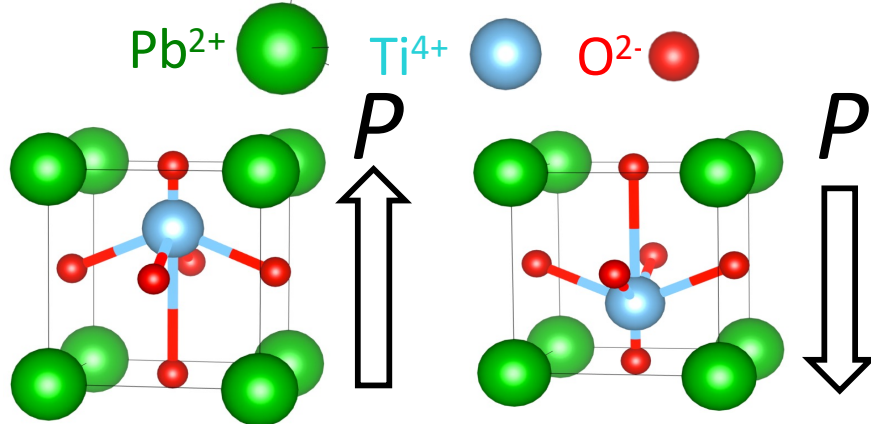
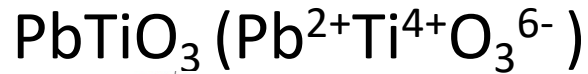


FCRP MSD Center

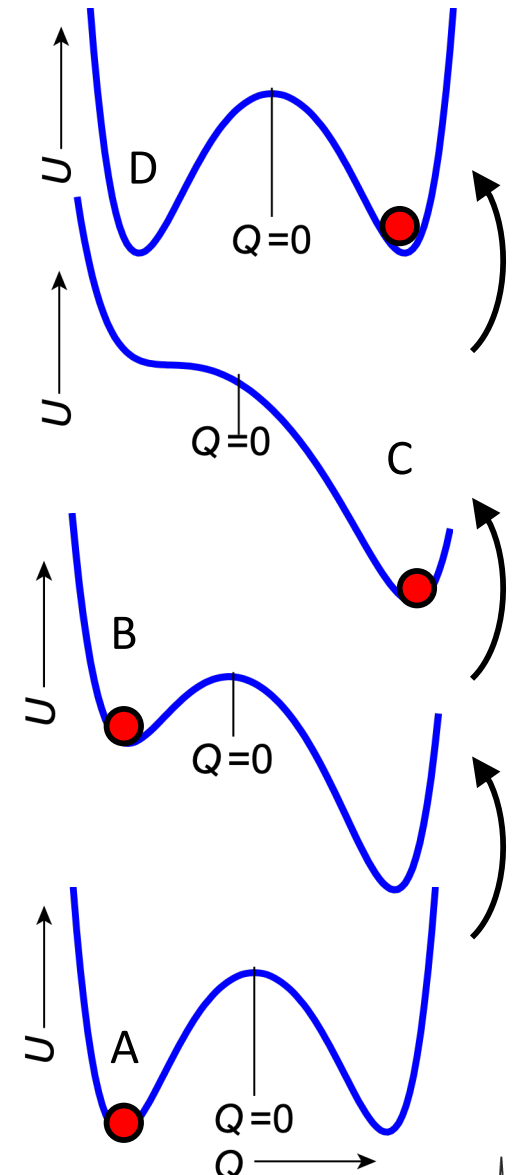
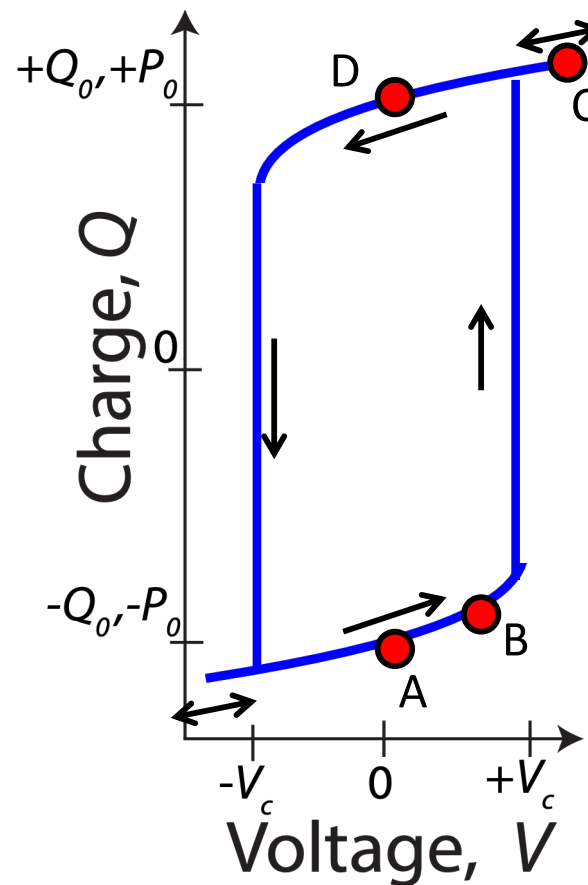


Ferroelectric Oxides

Classical Ferroelectric



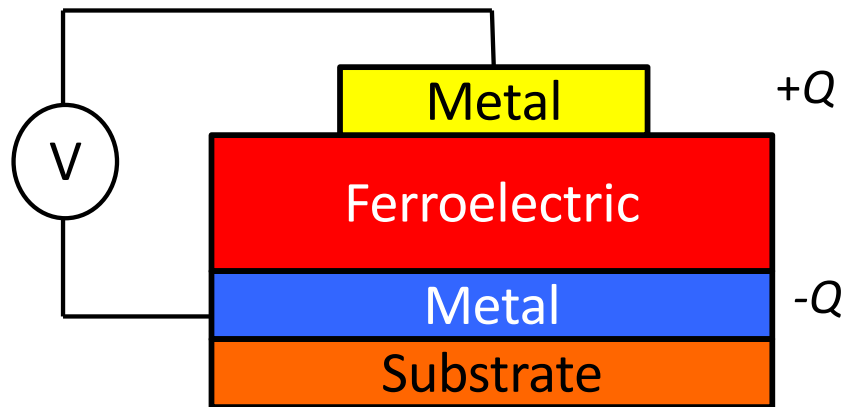
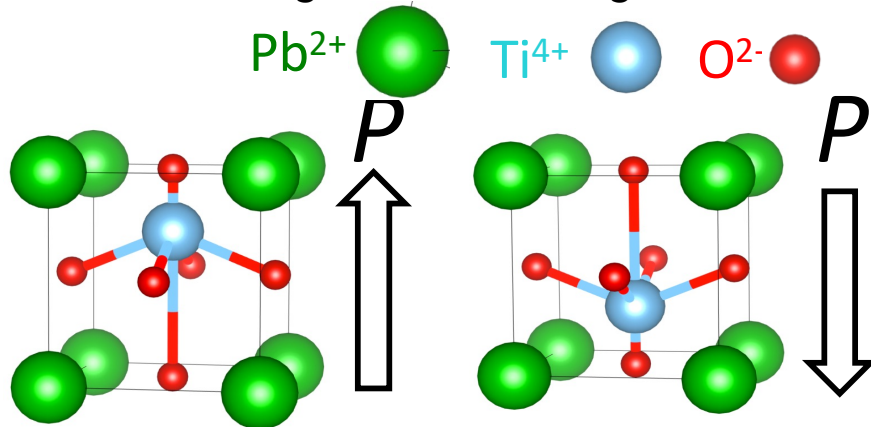
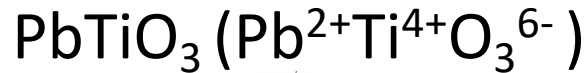
Charge Voltage Characteristics



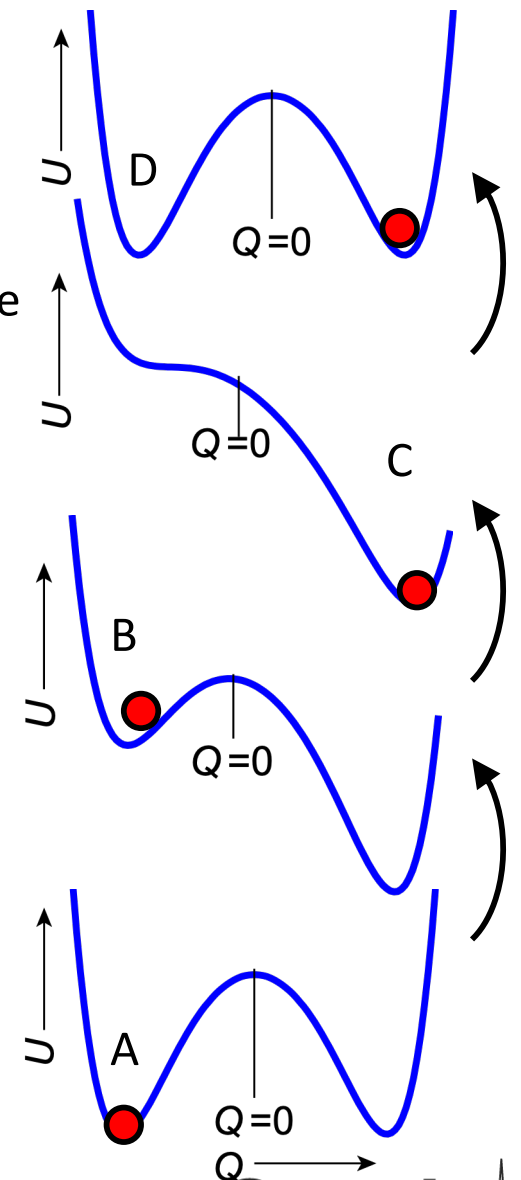
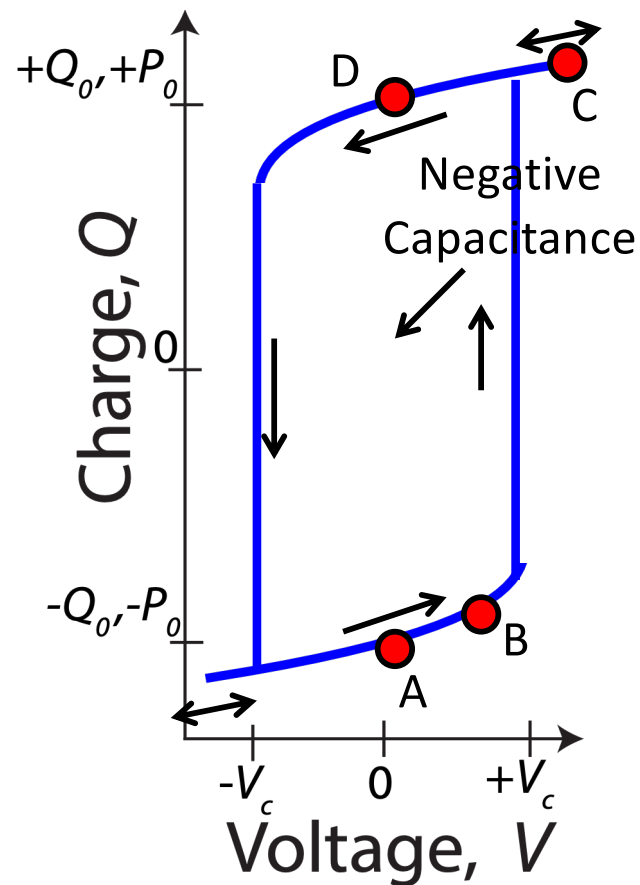
A ferroelectric is an insulator with a spontaneous polarization, which can be switched by an above coercivity voltage.

Ferroelectric Oxides

Classical Ferroelectric

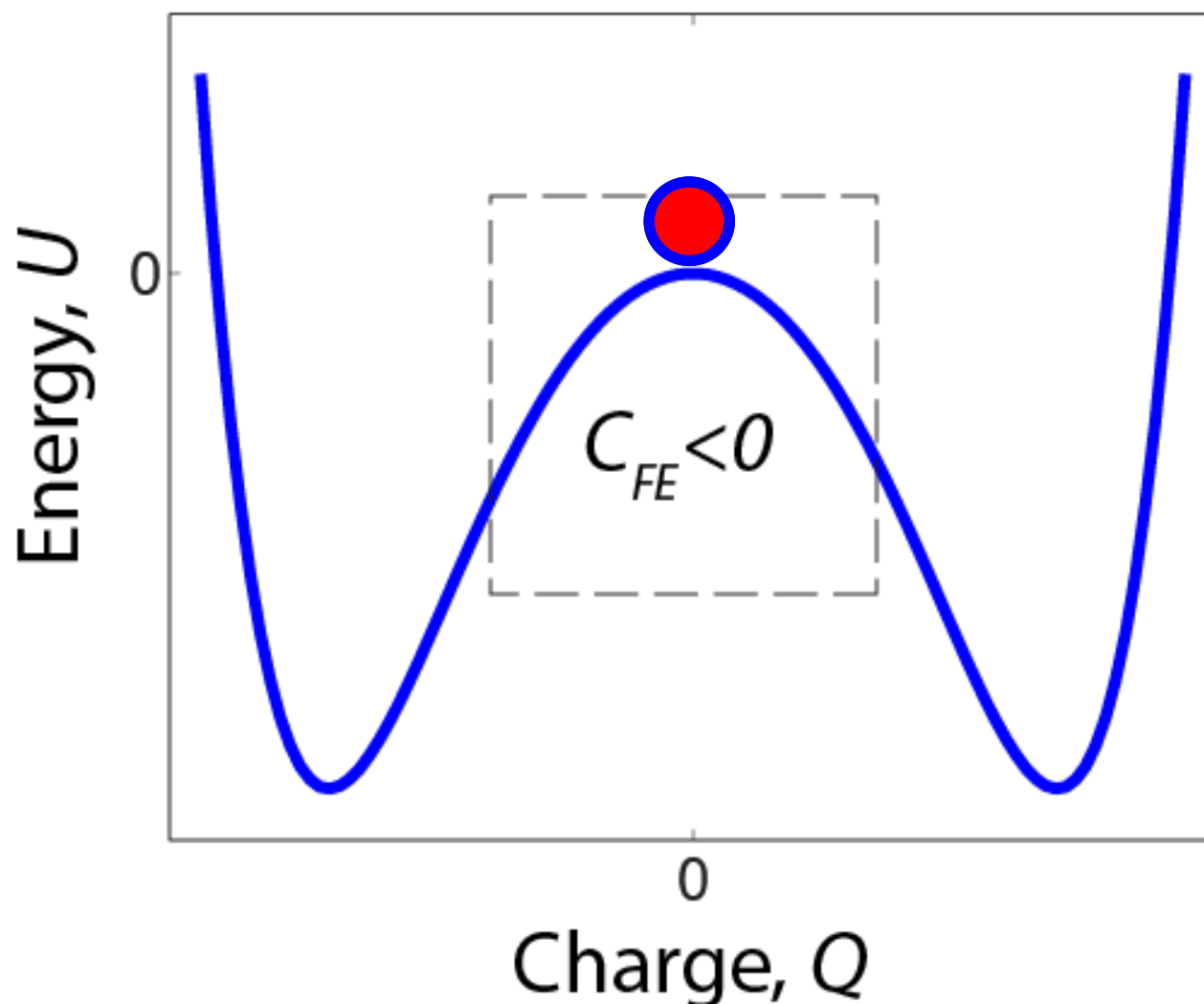


Charge Voltage Characteristics



A ferroelectric is an insulator with a spontaneous polarization, which can be switched by an above coercivity voltage.

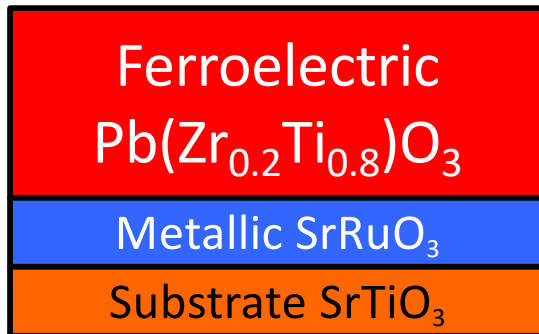
Why is it difficult to measure?



Ferroelectric Negative Capacitance is unstable and cannot be accessed in a time independent equilibrium measurement.

Growth of Ferroelectric Oxides

Experimental Heterostructure



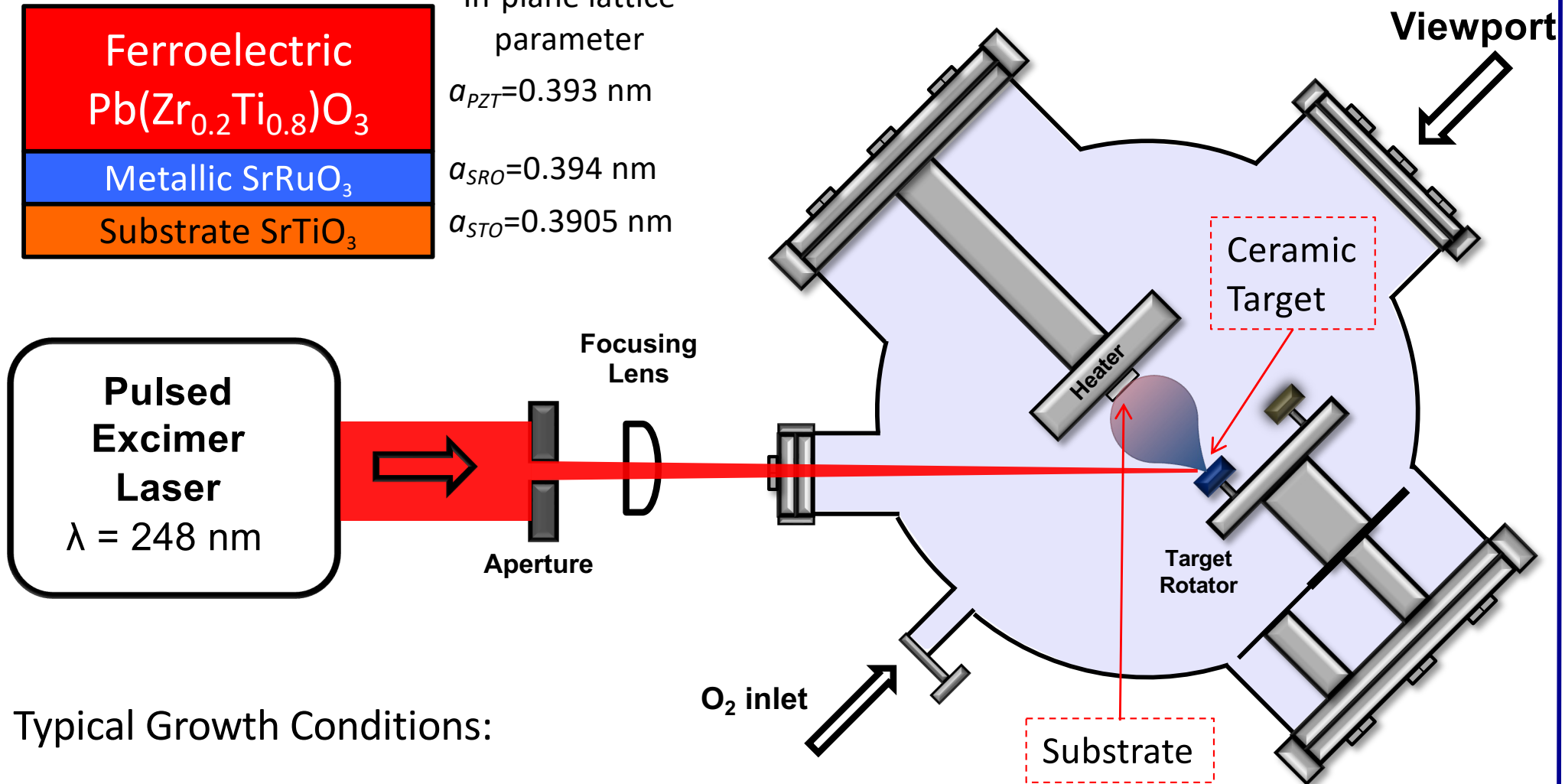
In-plane lattice parameter

$$a_{\text{PZT}} = 0.393 \text{ nm}$$

$$a_{\text{SRO}} = 0.394 \text{ nm}$$

$$a_{\text{STO}} = 0.3905 \text{ nm}$$

Pulsed laser deposition technique



Typical Growth Conditions:

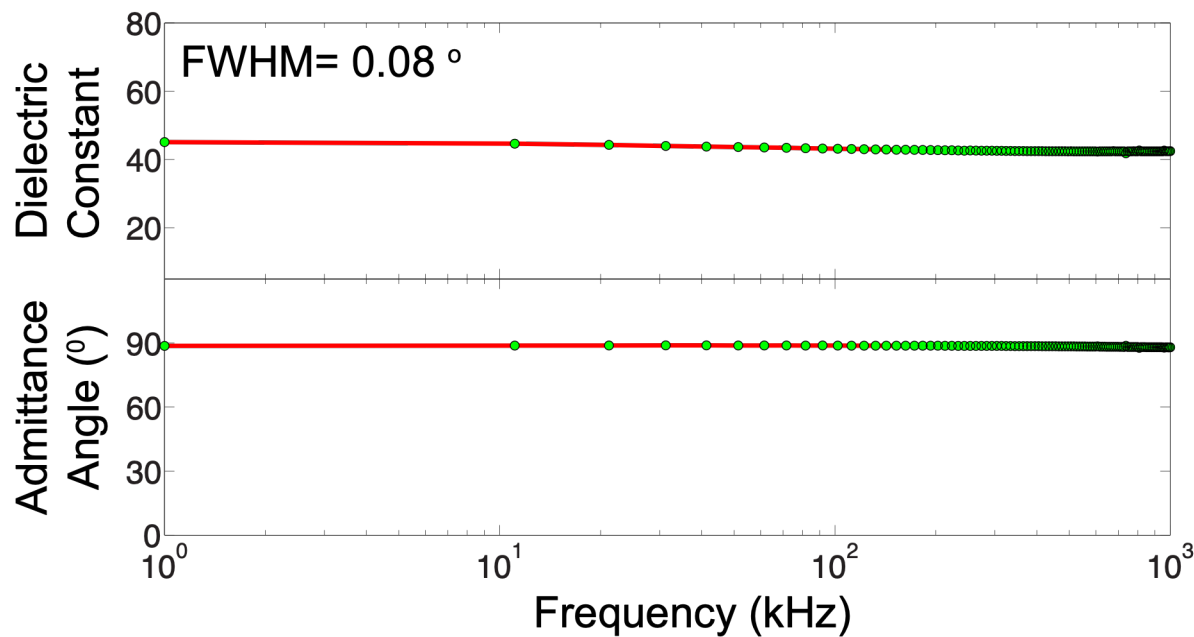
SrRuO_3 : 720°C, 100 mTorr, 1.2 J/cm², 5-15 Hz

$\text{Pb}(\text{Zr}_{0.2}\text{Ti}_{0.8})\text{O}_3$: 630 °C, 100 mTorr, 1-1.5 J/cm², 5-15 Hz

Correlation with Structural Properties

Negative capacitance effect is correlated to the structural quality of the thin film.

Dielectric Frequency Dispersion

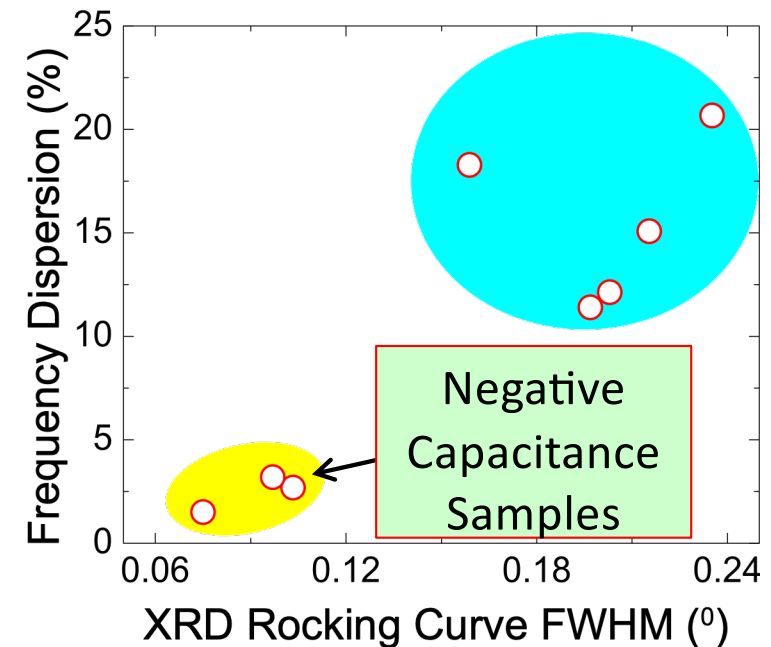


Frequency Dispersion between 1 kHz and 1 MHz < 2 %

Khan et al. Nature Mater. 14, 182 (2015)

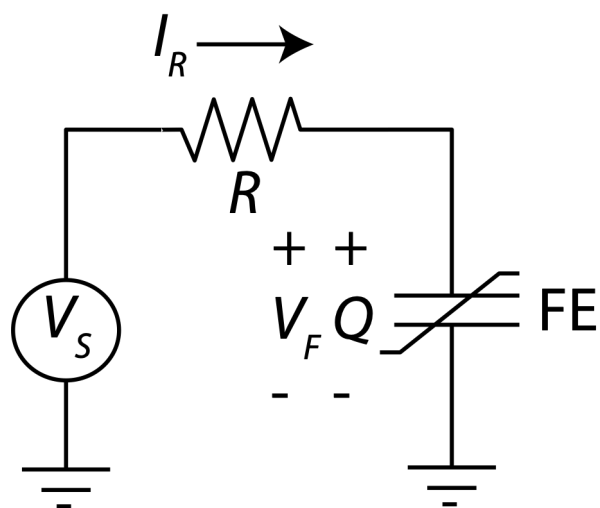
Khan et al. Appl. Phys. Lett. 104, 262407 (2014)

Frequency Dispersion vs. FWHM



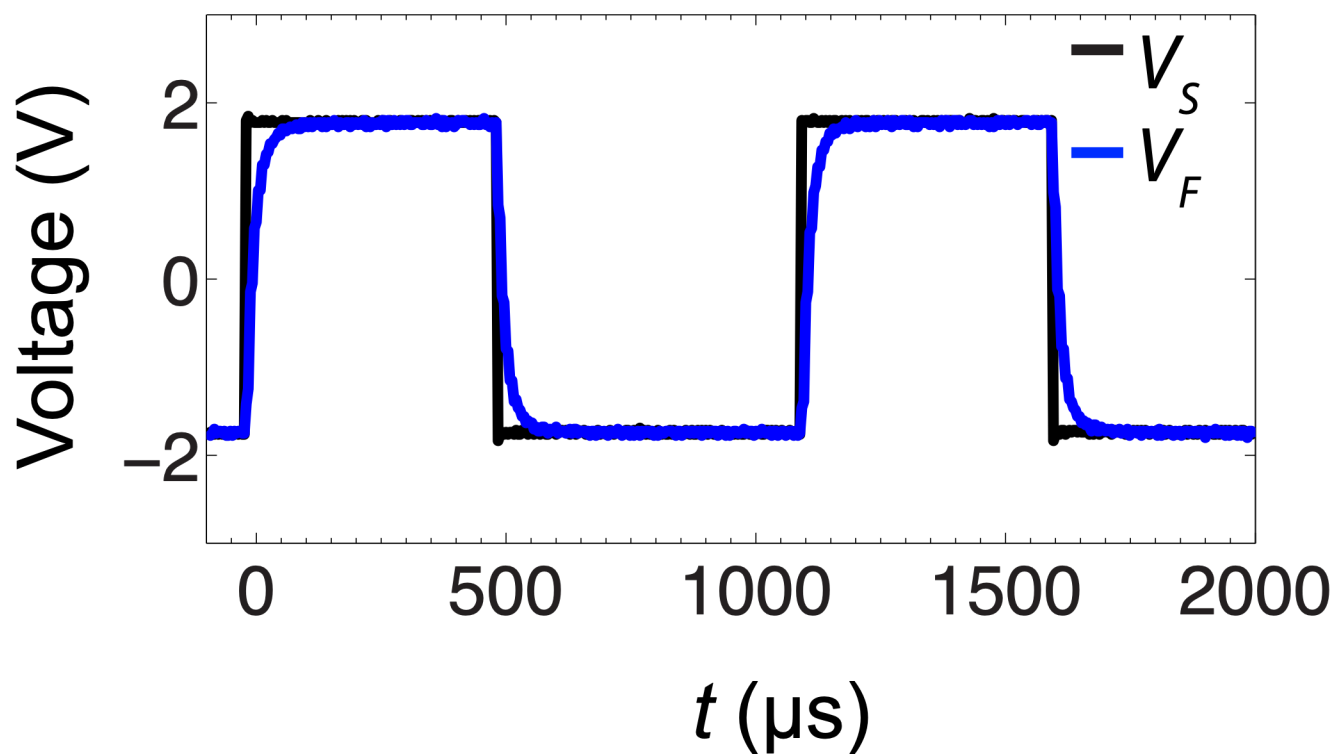
High quality materials growth is the key!

Correlation with ferroelectric switching

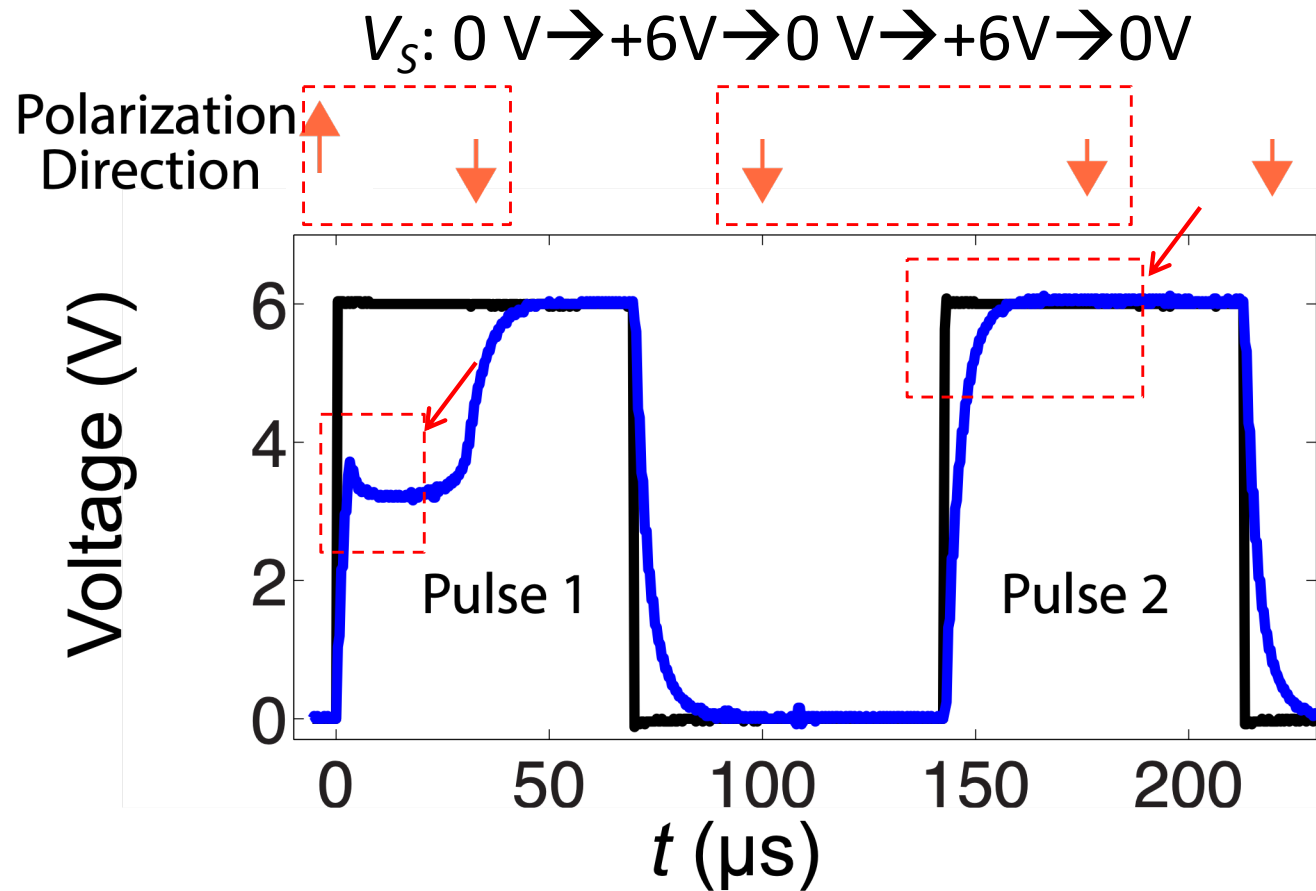
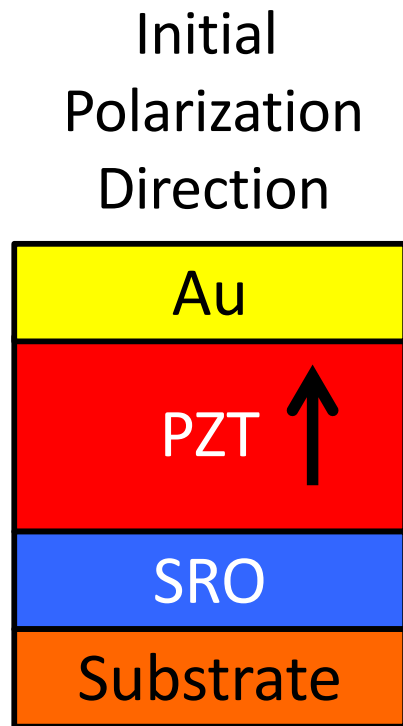


No negative capacitance transients are observed when the voltage is smaller than coercive voltage.

Coercive Voltage (~ 2.5 V)
 $V_S: -1.8 \text{ V} \rightarrow +1.8 \text{ V} \rightarrow -1.8 \text{ V} \rightarrow +1.8 \text{ V}$



Correlation with ferroelectric switching

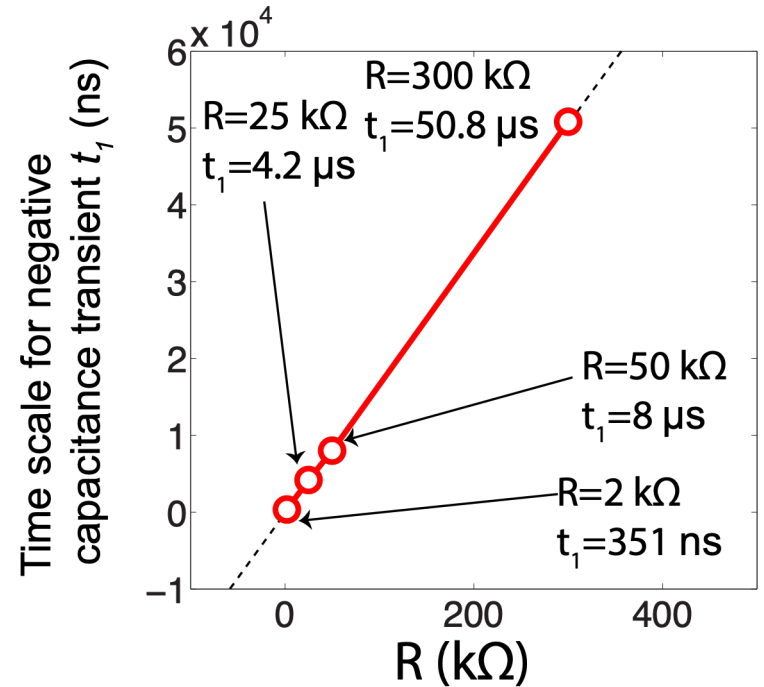
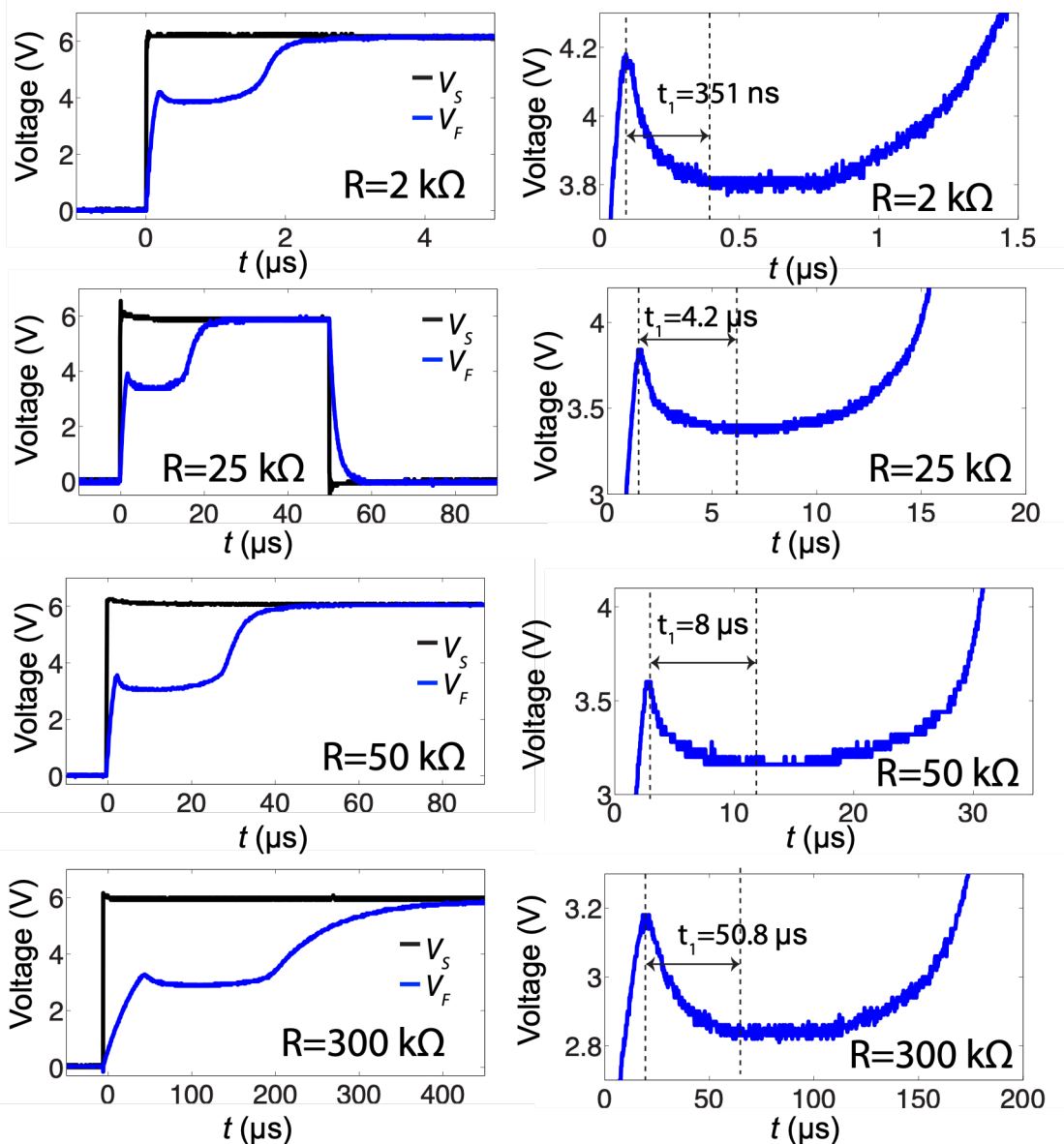


Khan et al. Nature Mater. 14,
182 (2015)

No negative capacitance transients are observed when there is no FE switching event.

Time scale of transients

Close-up of the negative capacitance transients



The intrinsic time scale of negative capacitance transient could be very small.

Landau-Khalatnikov model

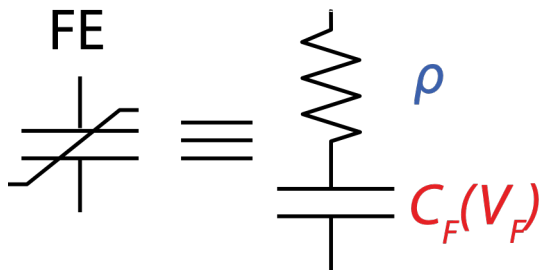
Landau-Khalatnikov Equation: $\rho \frac{dP}{dt} = - \frac{dU}{dP}$

P =Ferroelectric polarization

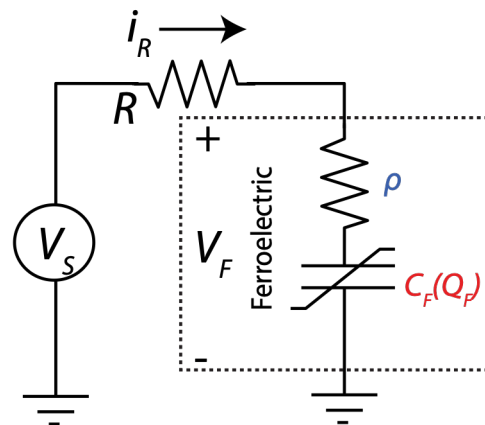
U =Free energy of the system

ρ = Material dependent parameter

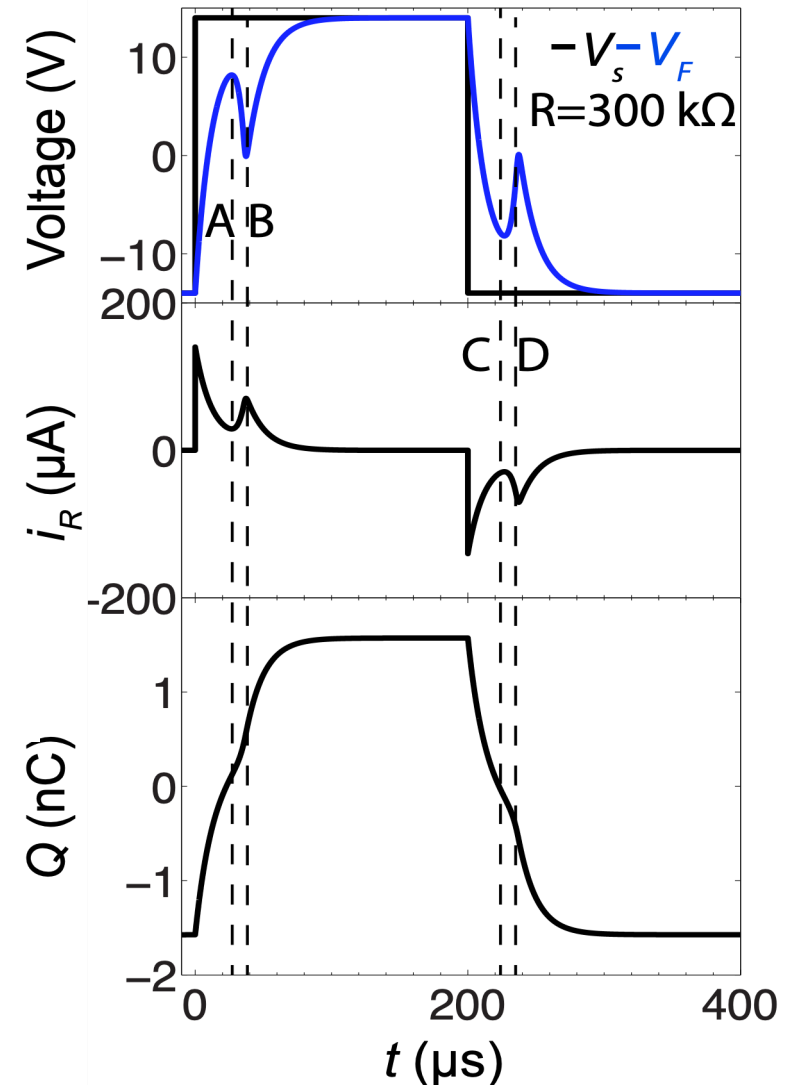
Equivalent Circuit



Simulated Circuit



Qualitative match between simulated and experimental transients.



Landau-Khalatnikov model

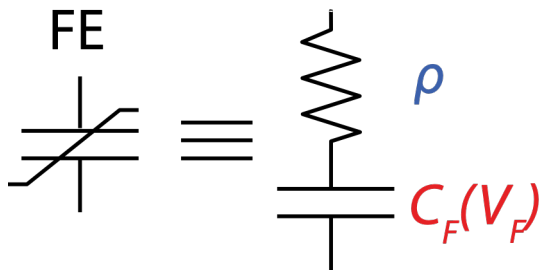
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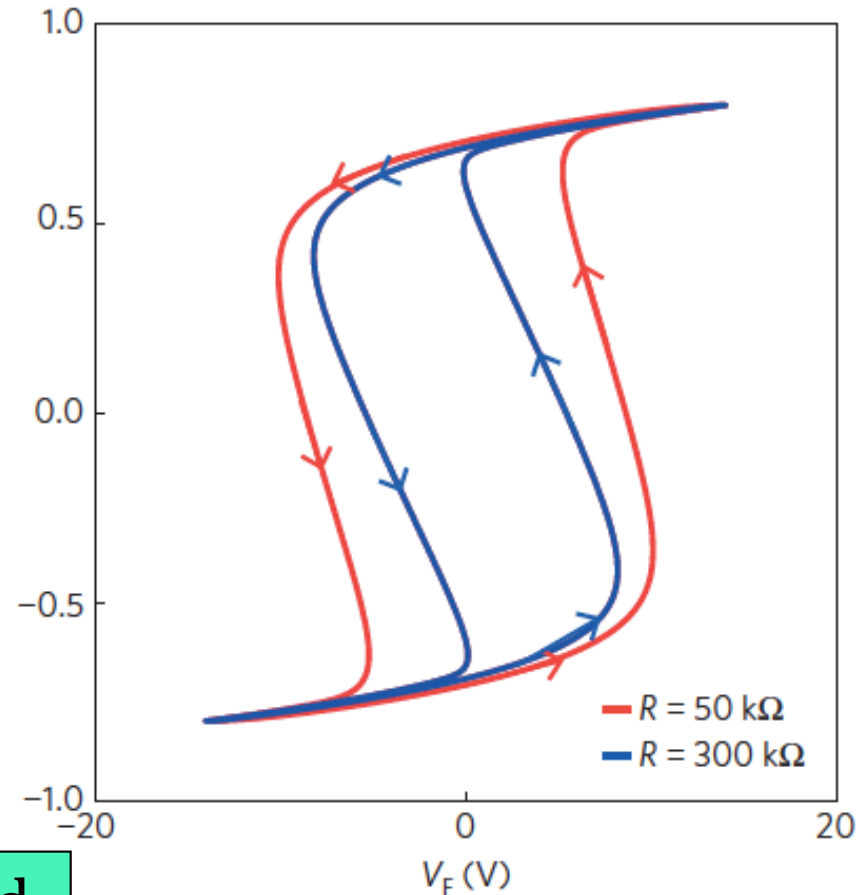
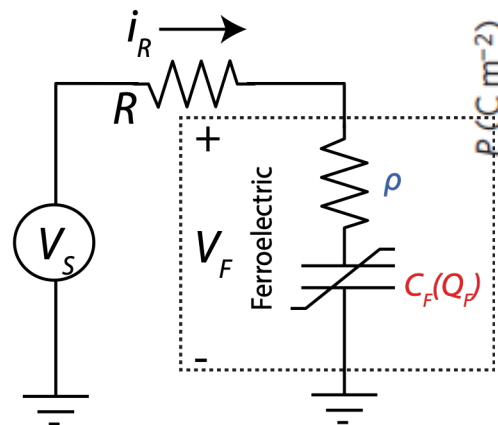
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Equivalent Circuit



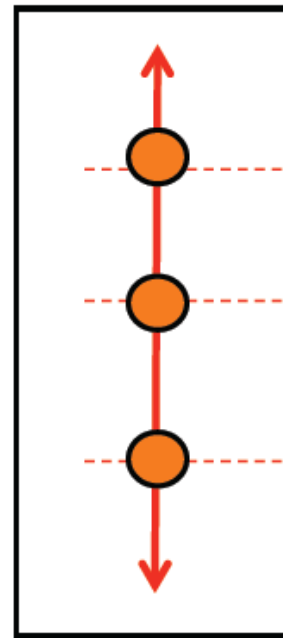
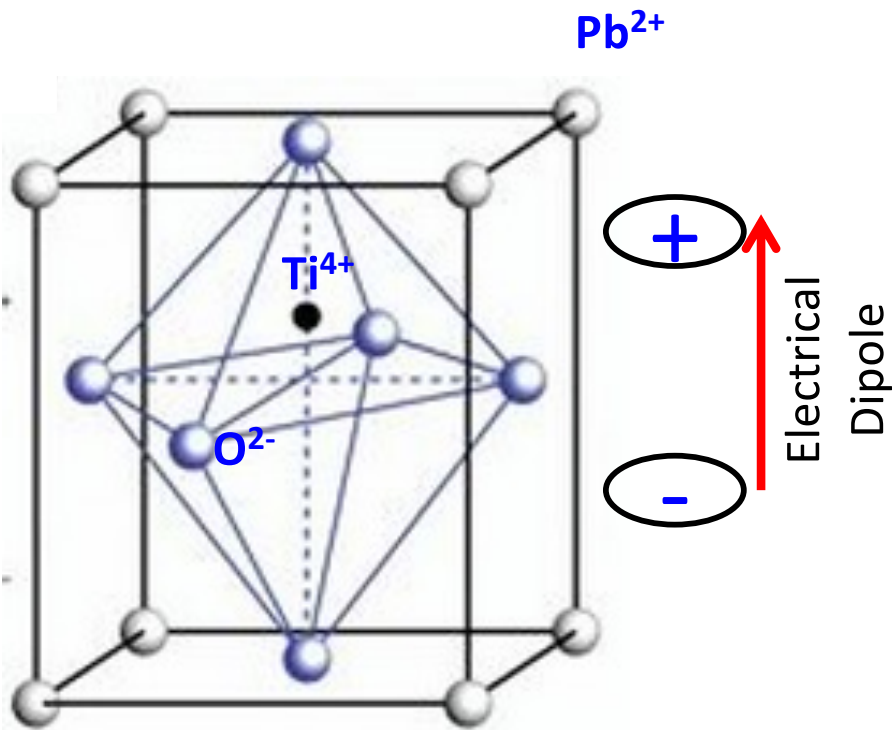
Simulated Circuit



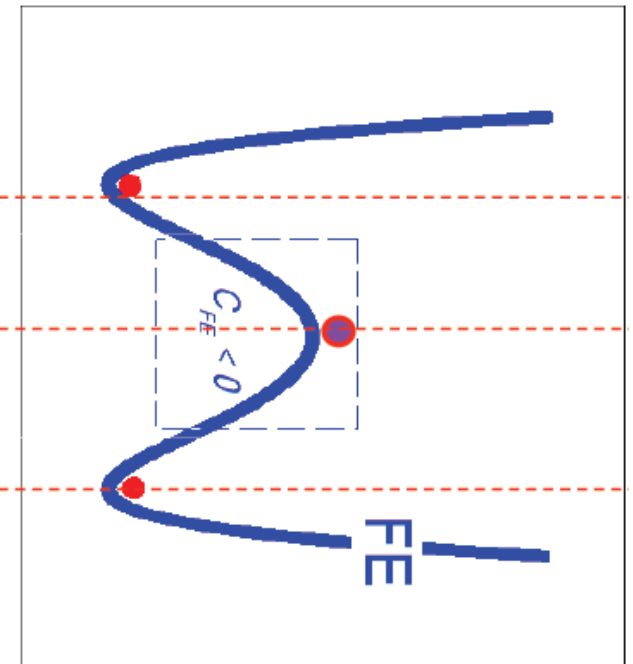
Qualitative match between simulated and experimental transients.

Ferroelectricity and Negative Capacitance

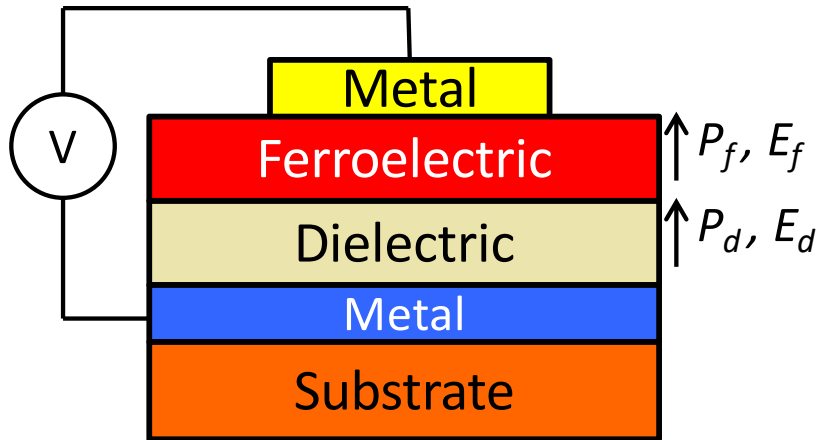
Ferroelectric Unit Cell



Polarization



Temperature Effects on Negative Capacitance



Laudau-Devonshire formalism

$$U_i = \alpha_i P_i^2 + \beta_i P_i^4 + \gamma_i P_i^6 - E_i \cdot P_i$$

$$U_{f+p} = l_f U_f + l_d U_d$$

Self-consistent solution with Poisson's equation.

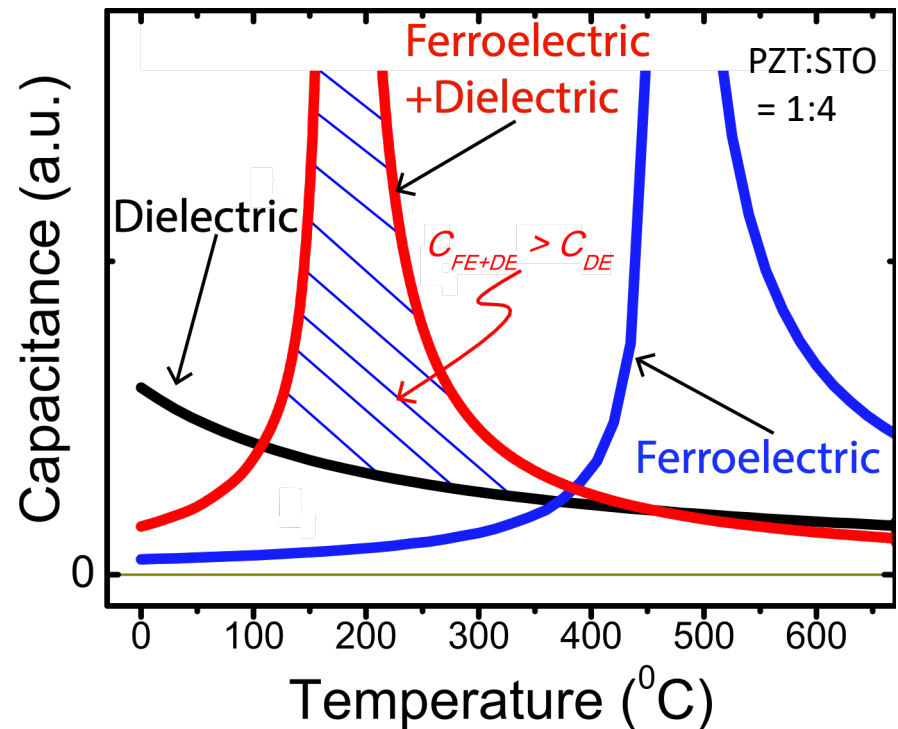
α, β, γ = Landau coefficients

U_i = Free energy on i -th layer

E_i = Electric field of i -th layer

P_i = Polarization in i -th layer

l_i = Thickness of i -th layer

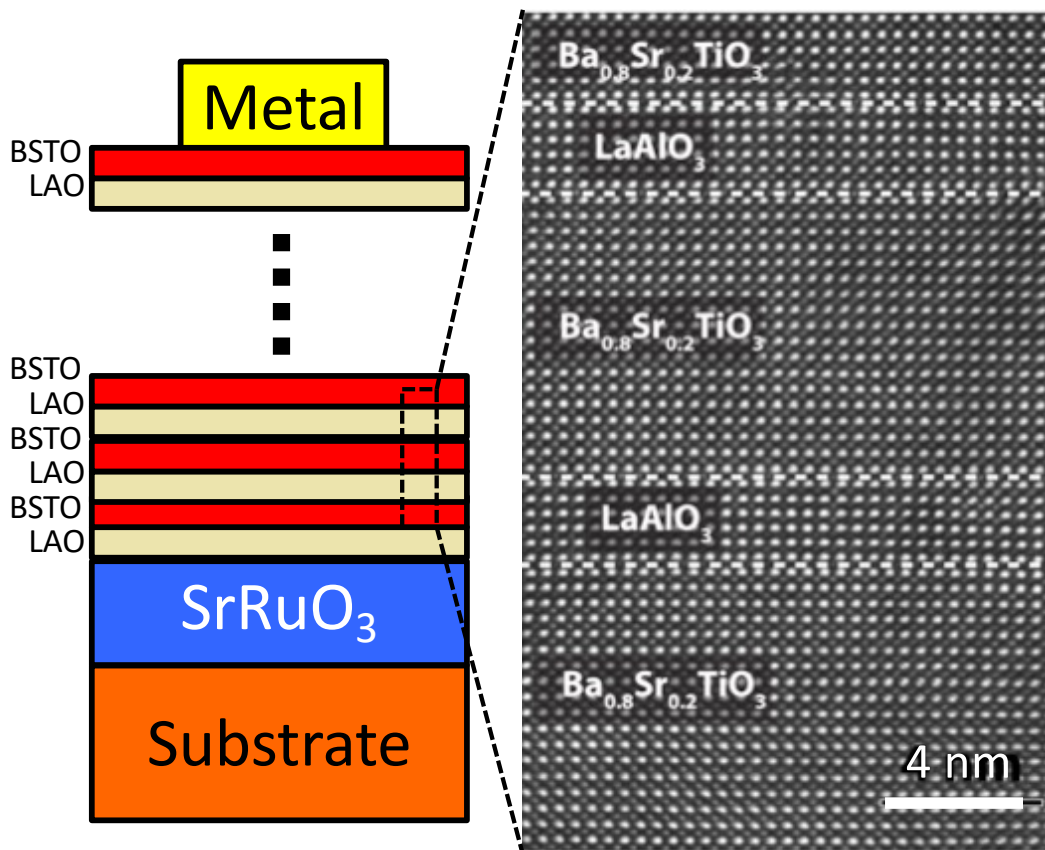


The temperature range of capacitance enhancement depends on the Curie temperature of the ferroelectric!

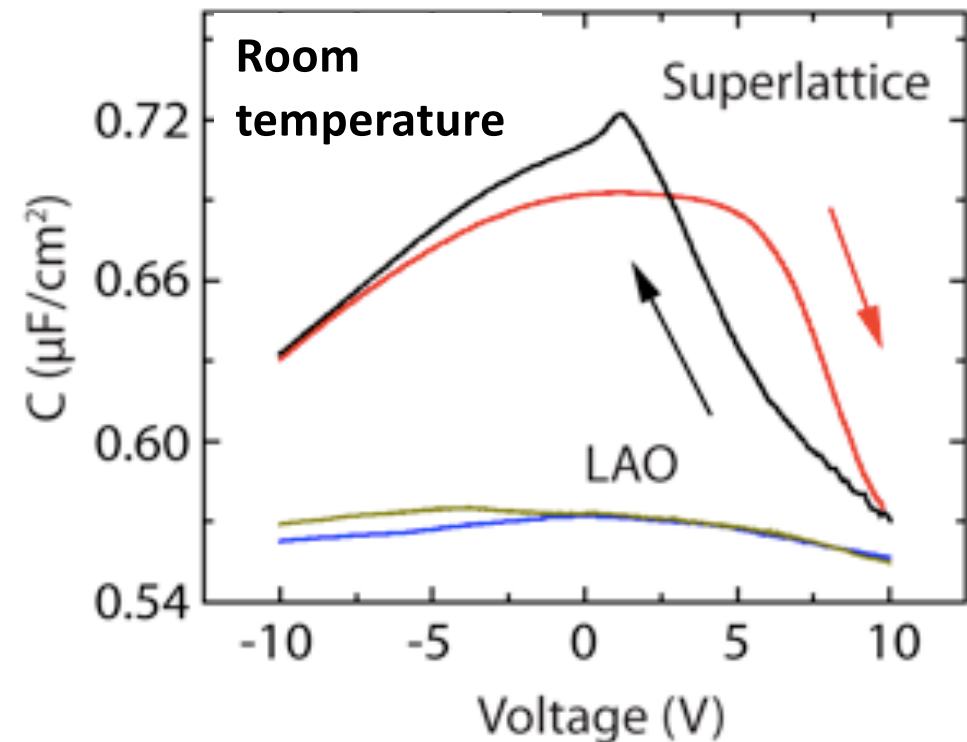
Room temperature capacitance enhancement

Ferroelectric	$(\text{Ba}_{0.8}\text{Sr}_{0.2})\text{TiO}_3$
Dielectric	LaAlO_3

$(\text{Ba}_{0.8}\text{Sr}_{0.2})\text{TiO}_3$ bulk Curie Temperature: $<110^\circ\text{C}$



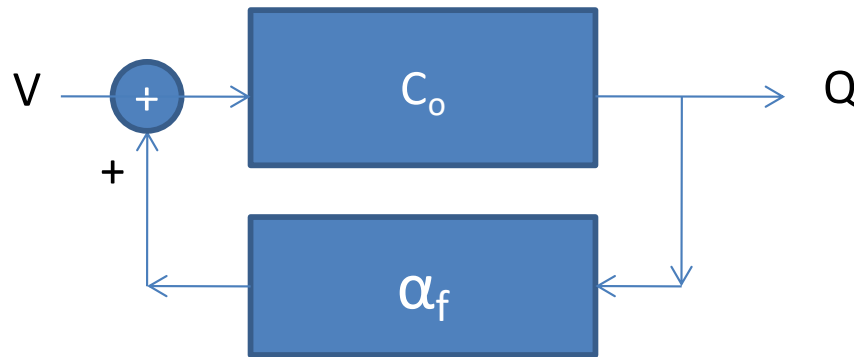
Superlattice: LAO(6 monolayer (ML))/BSTO(18 ML) repeated 11 times on GSO.



Gao, Khan et al. Nano Lett. 14, 5814 (2014).

Negative Capacitance effect in sub-10 nm ferroelectrics at room temperature

A General Guideline for finding Negative Capacitance in different systems



$$Q \in_0 (V_+ \alpha Q)$$

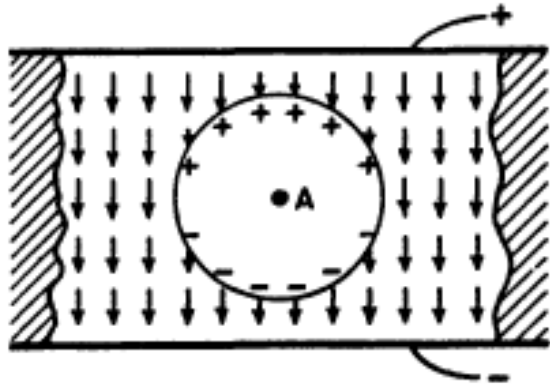
$$C = \frac{C_0}{1 - \alpha_f C_0}$$

$$\alpha_f C_0 > 1 \Rightarrow C < 0$$

Positive feedback can give rise to Negative Capacitance

Origin of Negative Capacitance (contd)

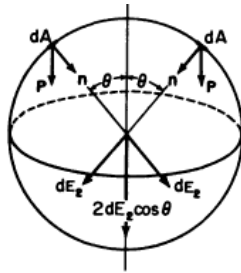
The Clausius Mossoti Picture of Polar Insulators



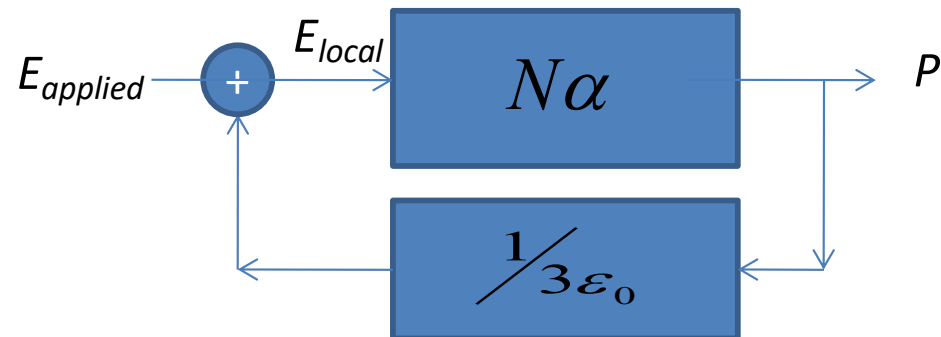
$$E_{local} = E_{applied} + E_{outside} + E_{inside}$$

$$E_{inside} = 0$$

$$E_{outside} = \frac{P}{3\epsilon_0}$$



$$E_{local} = E_{applied} + \frac{P}{3\epsilon_0}, P = N\alpha E_{local}$$



$$\epsilon_r = \frac{N\alpha / \epsilon_0}{1 - N\alpha / 3\epsilon_0}$$

Origin of Negative Capacitance (contd)

$$P = \frac{N\alpha}{1 - \frac{N\alpha}{3\epsilon_0}} E_{\text{applied}}$$

$$\alpha = \alpha_o + \frac{\mu^2}{3kT}$$

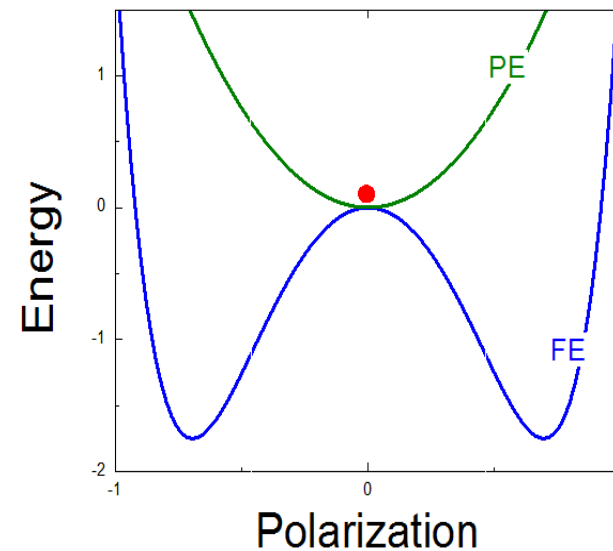
$$\epsilon_r = \frac{3T_c}{T - T_c} \quad \begin{array}{l} \text{Curie-Weiss} \\ \text{Law} \end{array}$$

$$T < T_c : \quad \epsilon_r < 0$$

Polar Catastrophe and manifestation
Remnant Polarization

Feedback mechanism is well described in the framework of the mean field theory in condensed matter physics.

Landau Model incorporates the feedback mechanisms in the phenomenological energy well description of ferroelectric and paraelectric.

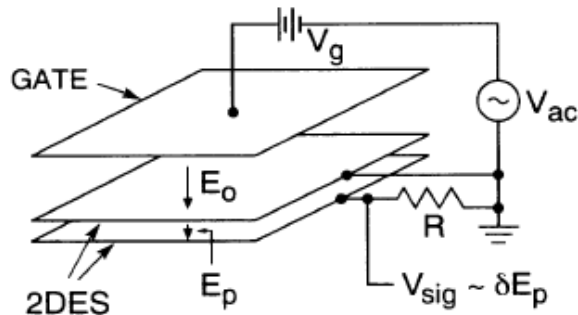


Is it possible to achieve negative capacitance in non-ferroelectric materials?

A system with bistability/stored energy can provide negative capacitance

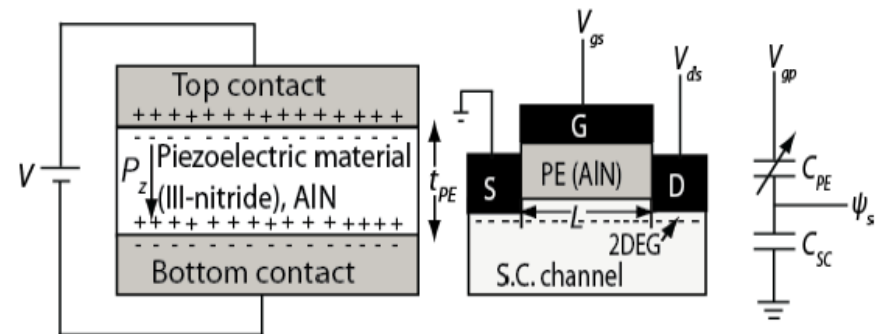
Interaction between two 2D electron gases

Electron-electron interaction; exchange and correlation energies among electrons lower the chemical potential of an electron system as the electron density increases.



Eisenstein, et al. Phys. Rev. Lett. 68, 674 ('92)
Li et al. Science 332, 825 ('11).

Piezoelectricity

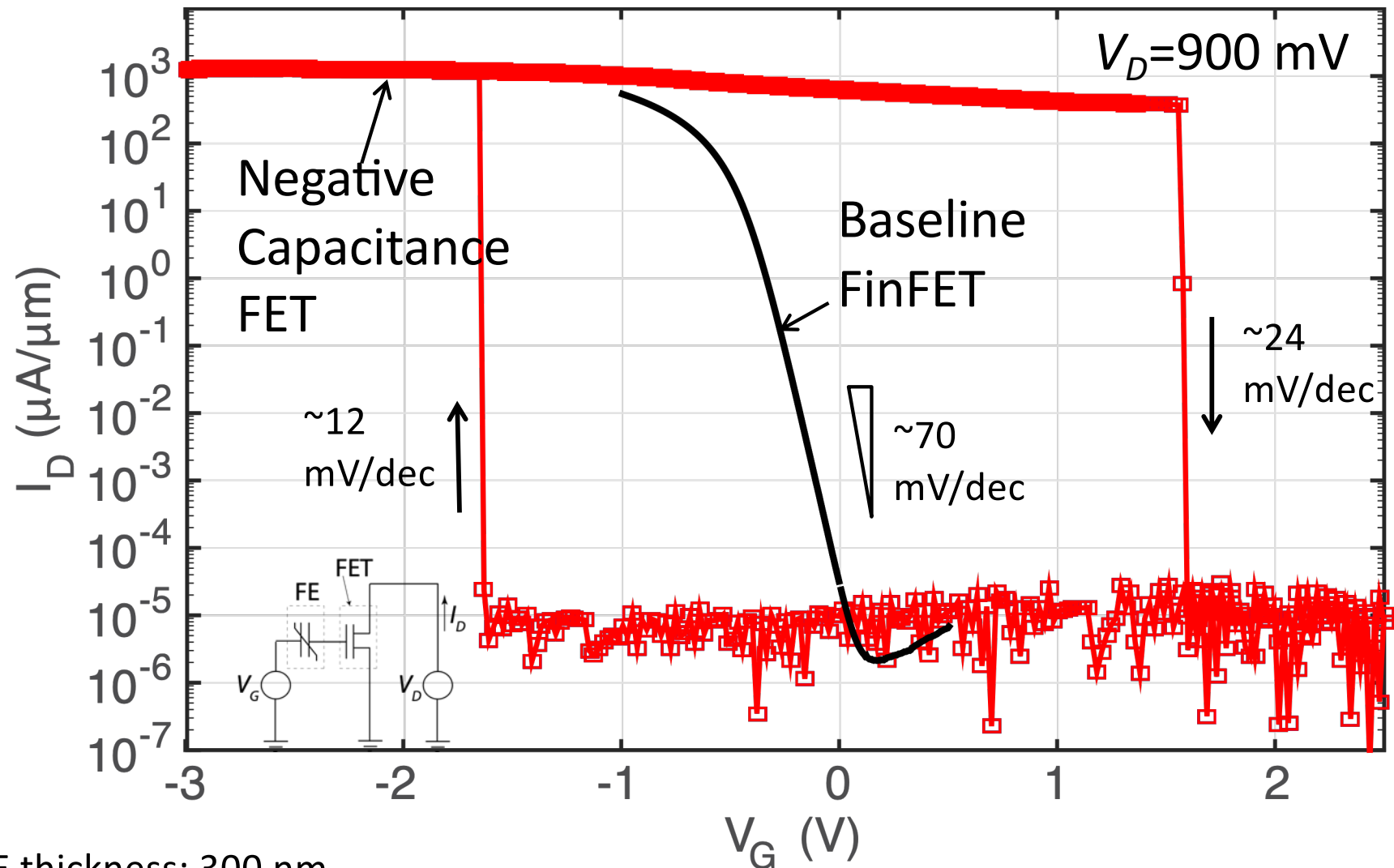


Jana et al. Physica Status Solidi (c) 10, 1469 ('13).

Jana et al. IEDM 2014.

Wong et al. IEDM 2014.

Negative Capacitance Transistors: Experiment



FE thickness: 300 nm

FinFET $L_G = 100$ nm

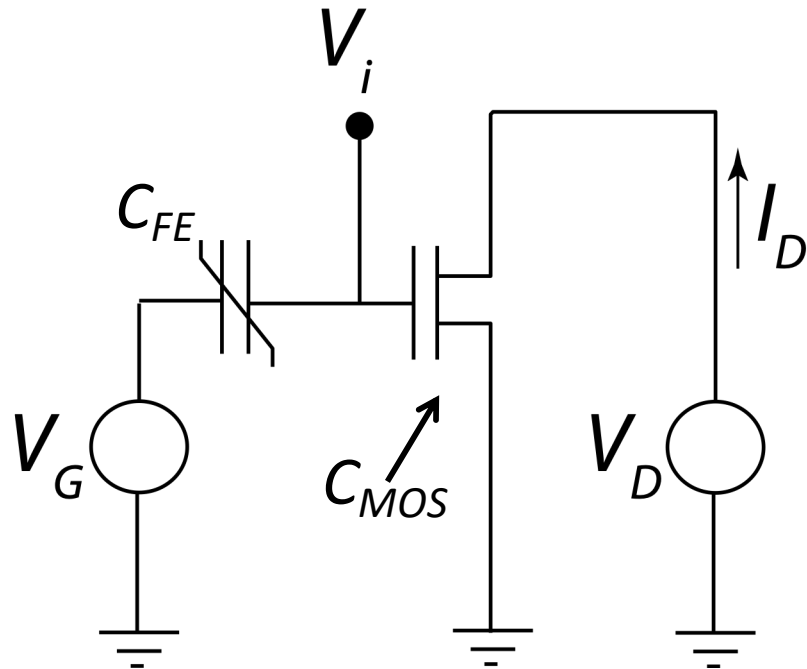
Fin width = 35 nm

Fin height = 25 nm

EOT = 1.4 nm

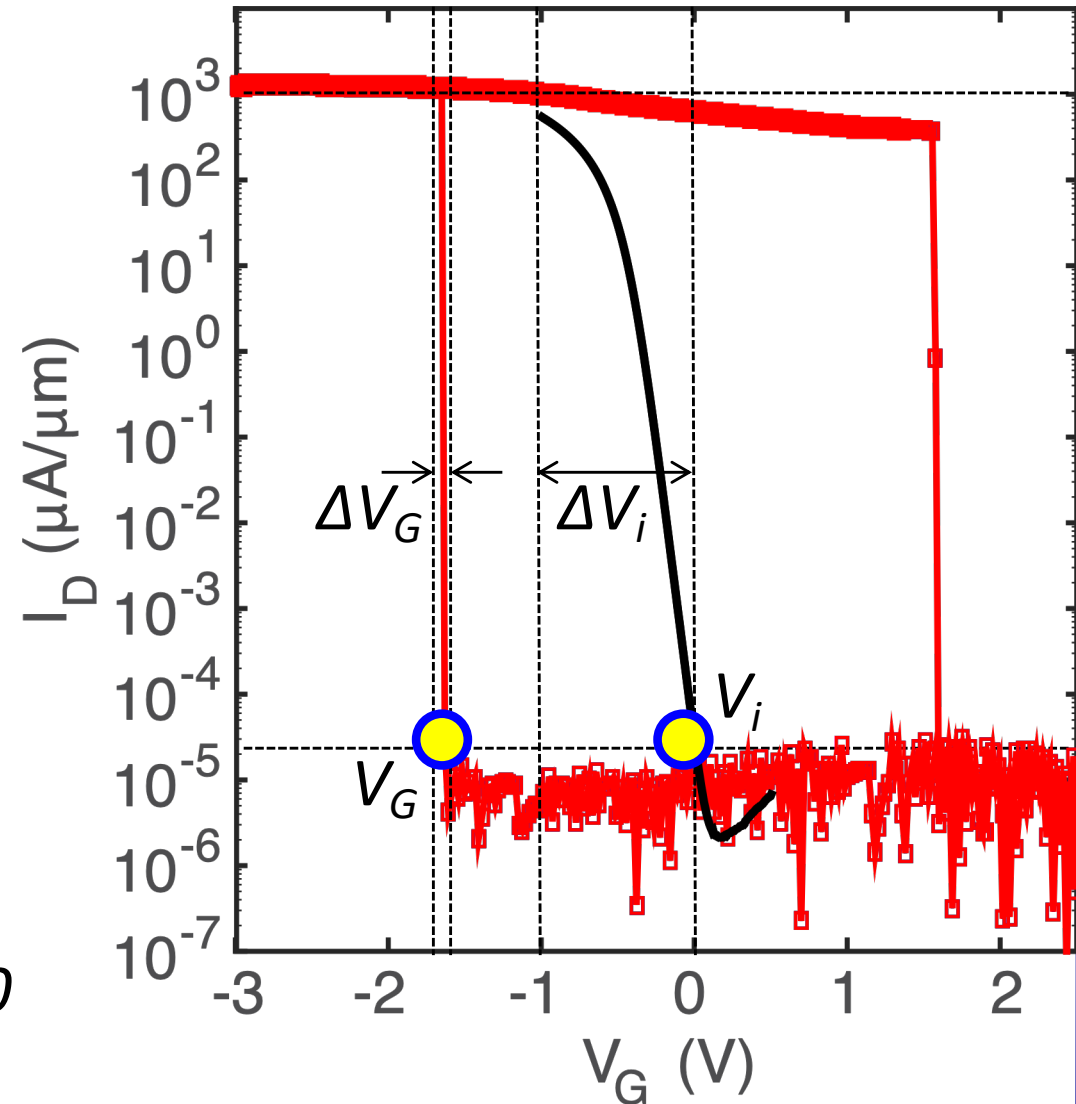
Extremely sharp turn-on & -off (10-20 mV/dec) over 7-8 orders of on-current!

Negative Capacitance Transistors: Experiment



$$\frac{\Delta V_i}{\Delta V_G} > 1$$

$$\frac{\Delta V_i}{\Delta V_G} = \frac{C_{FE}}{C_{MOS} + C_{FE}} > 1 \text{ only if } C_{FE} < 0$$

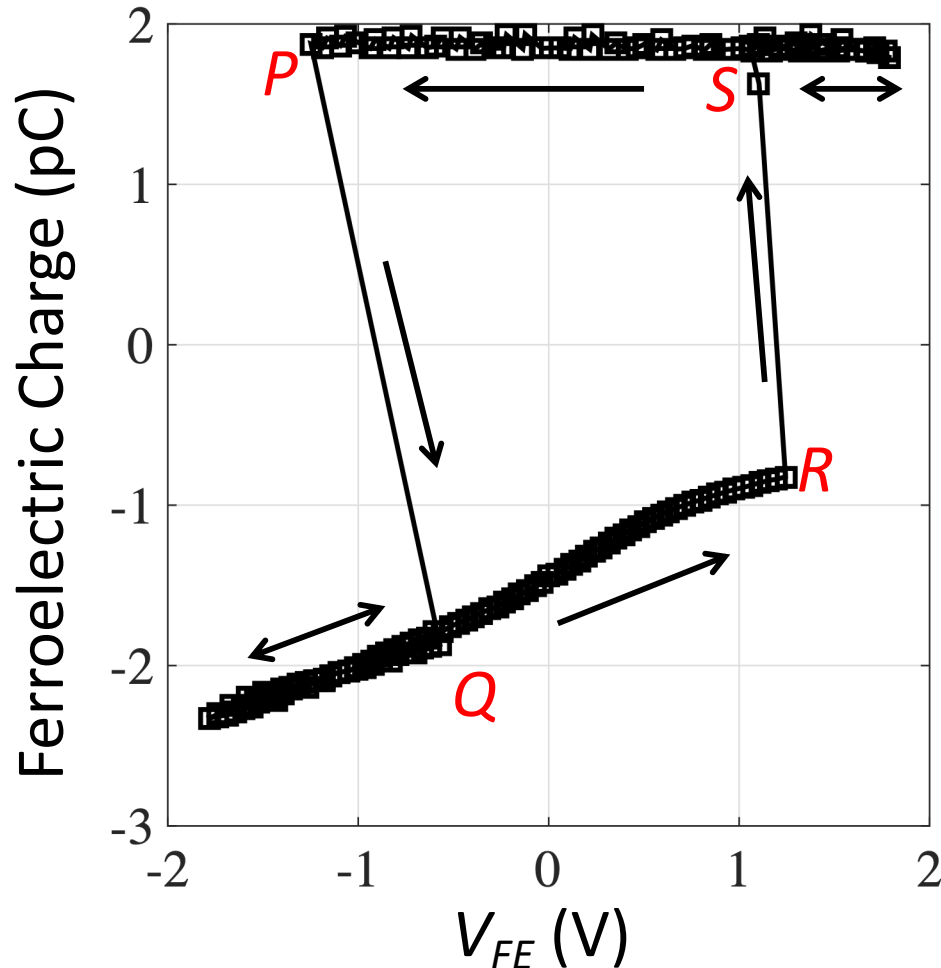


Sharp switching occurs due to ferroelectric negative capacitance!

Negative Capacitance Transistors: Experiment

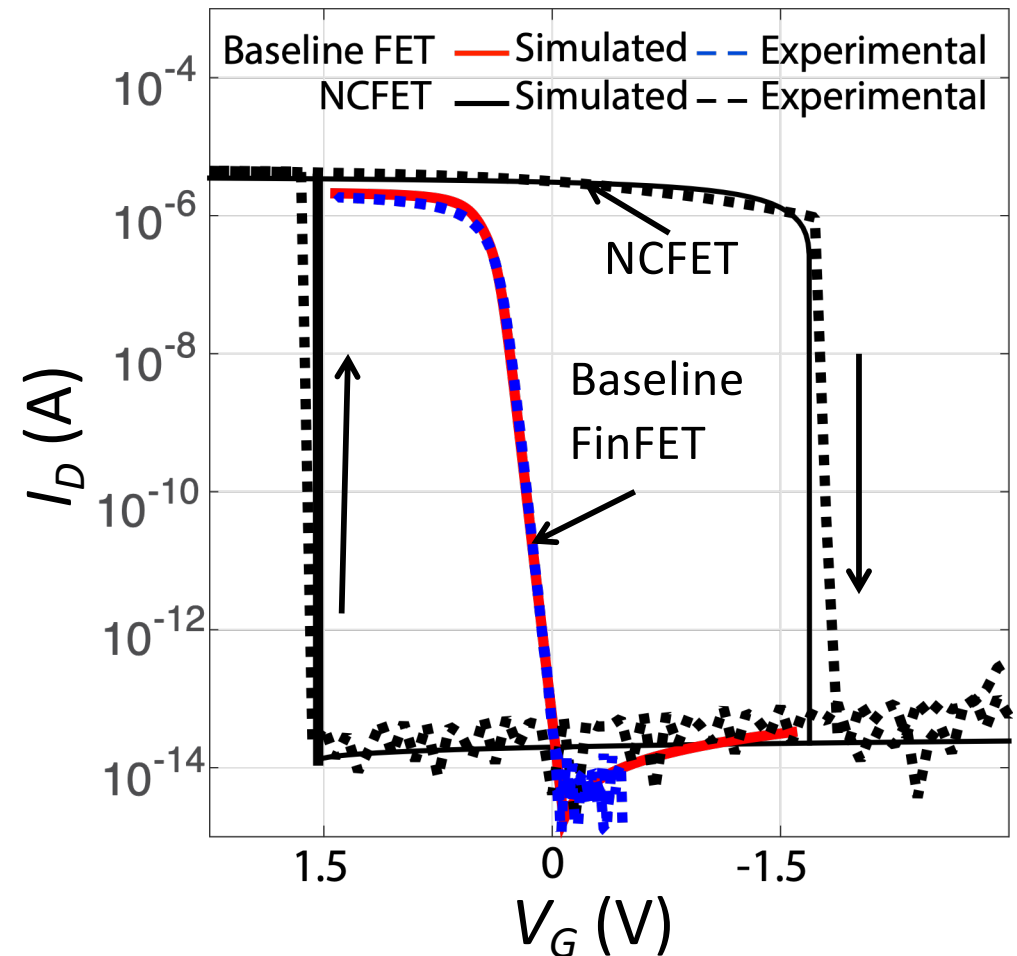
Hysteresis Loop

extracted from the circuit



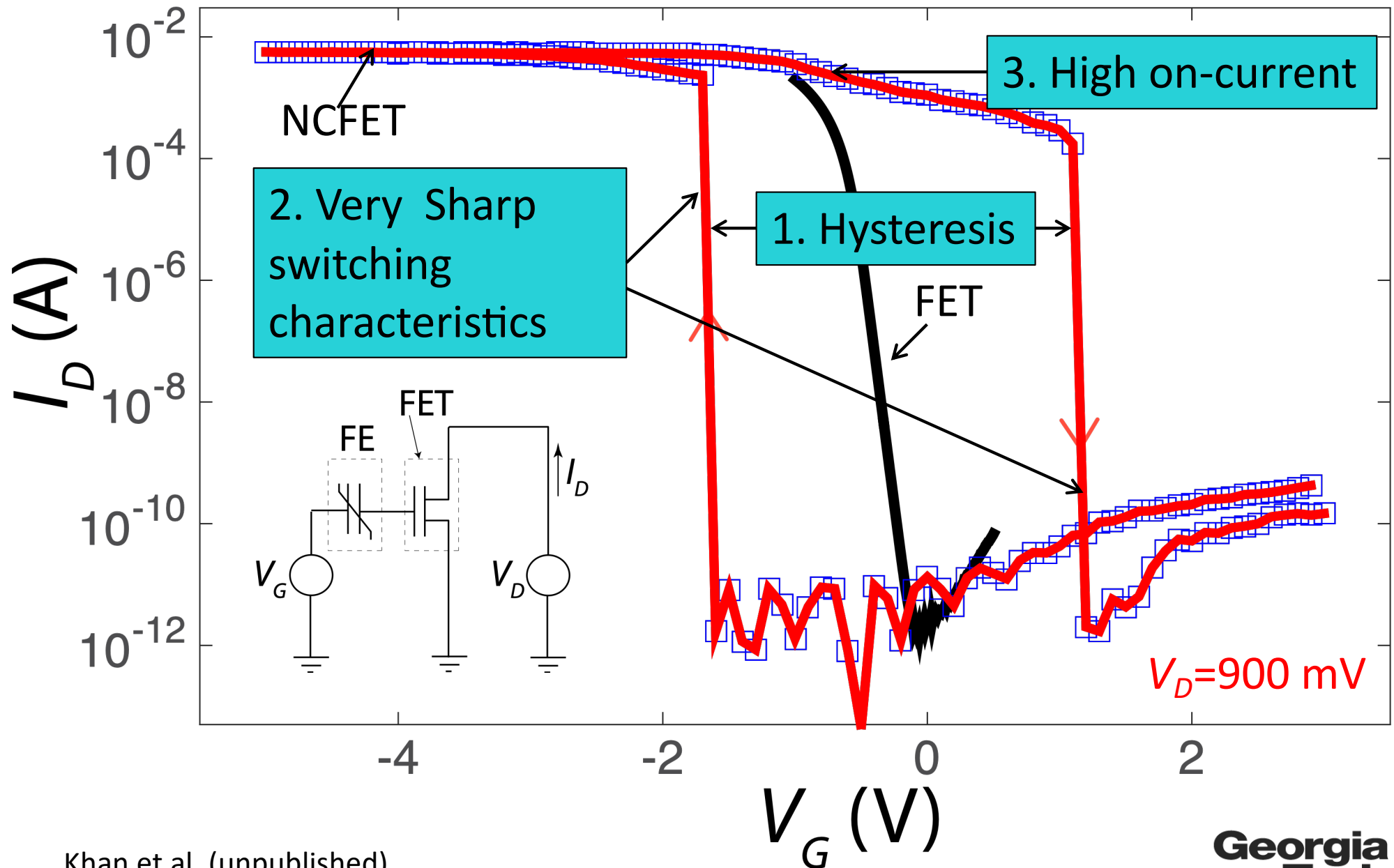
Negative Capacitance
Regions: PQ and RS

Experiment vs. Simulation

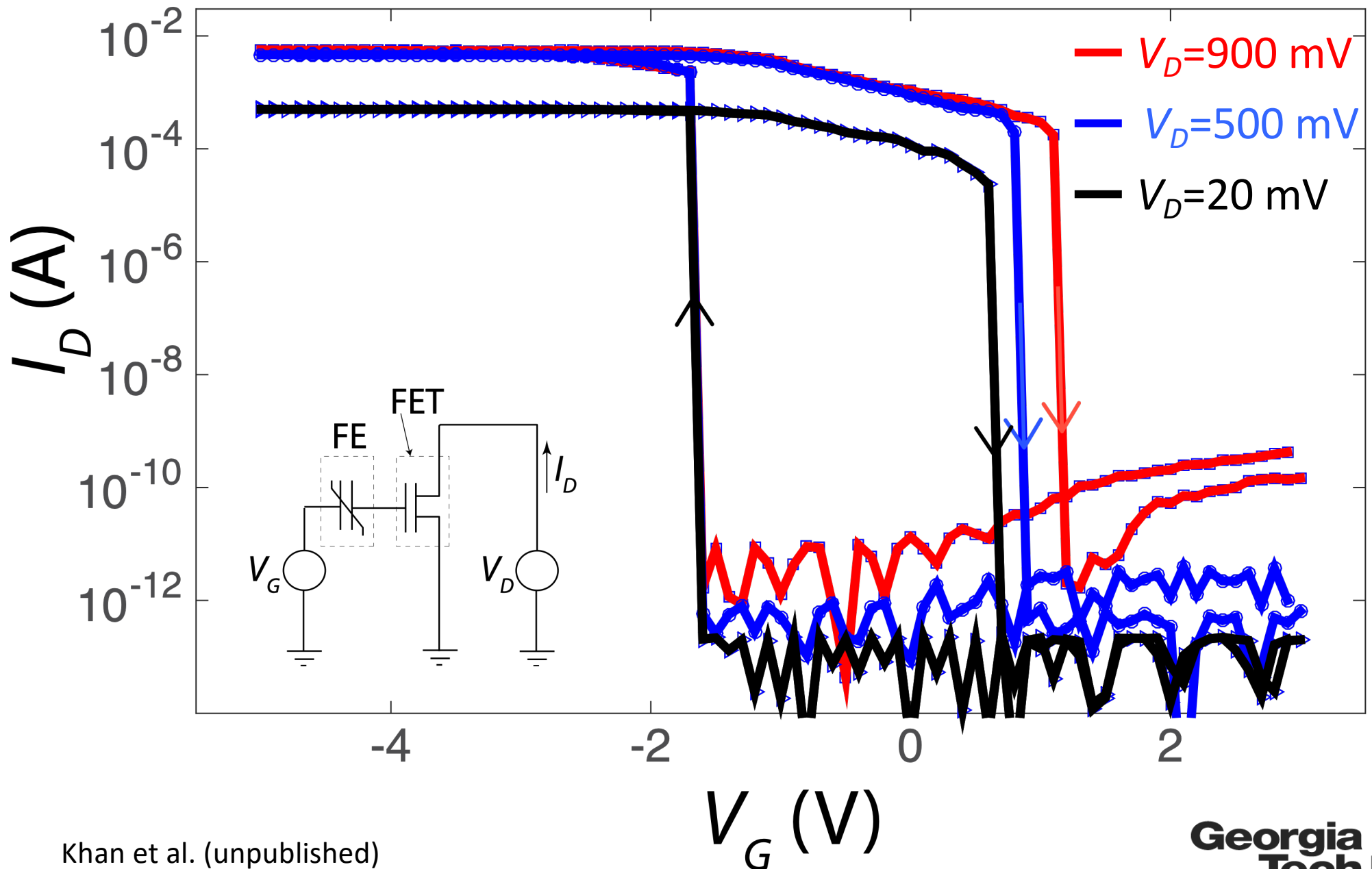


Sharp switching occurs due to
ferroelectric negative capacitance!

External connection to a ferroelectric

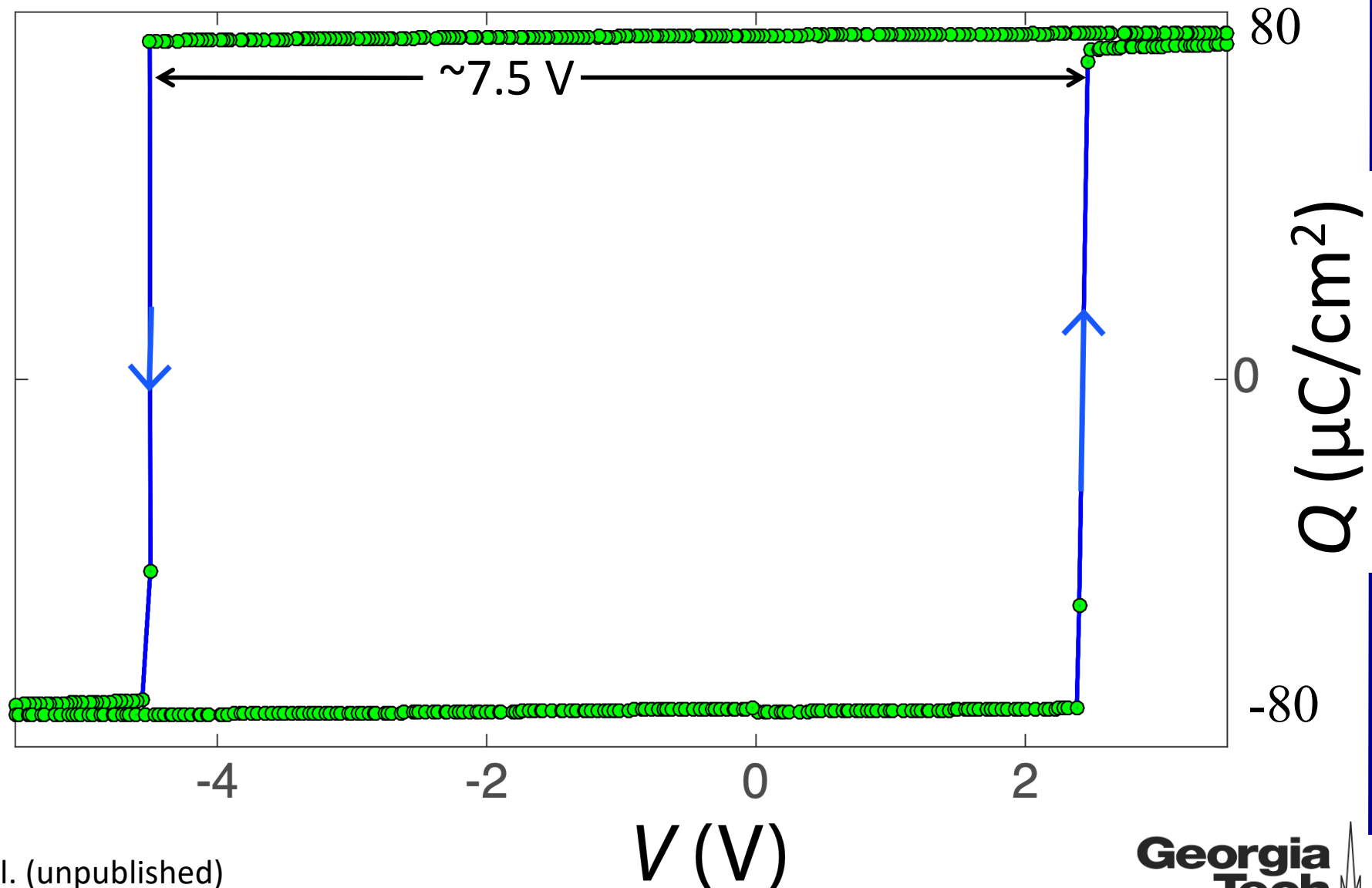


External connection to a ferroelectric



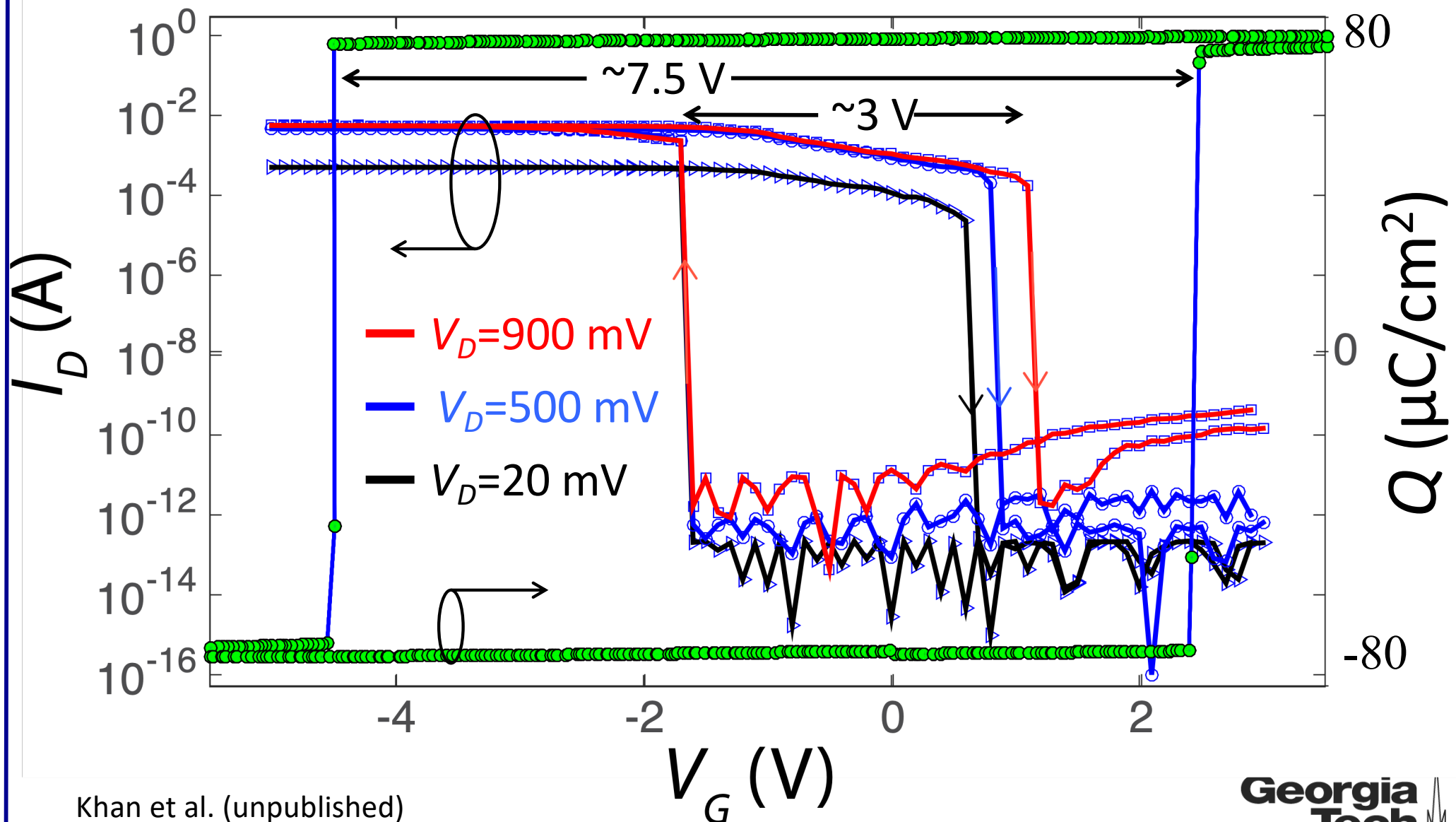
External connection to a ferroelectric

Q-V Characteristics of the ferroelectric capacitor



External connection to a ferroelectric

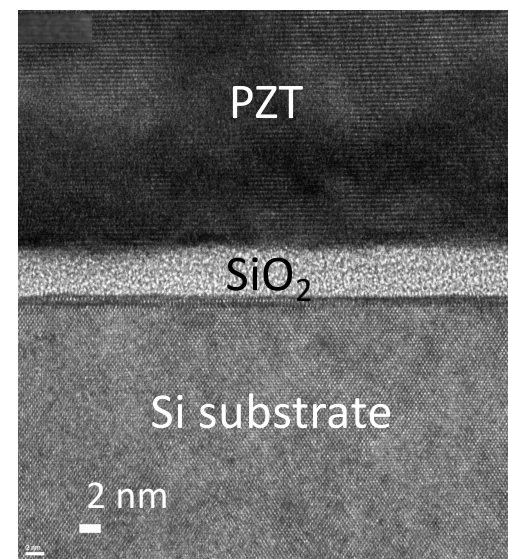
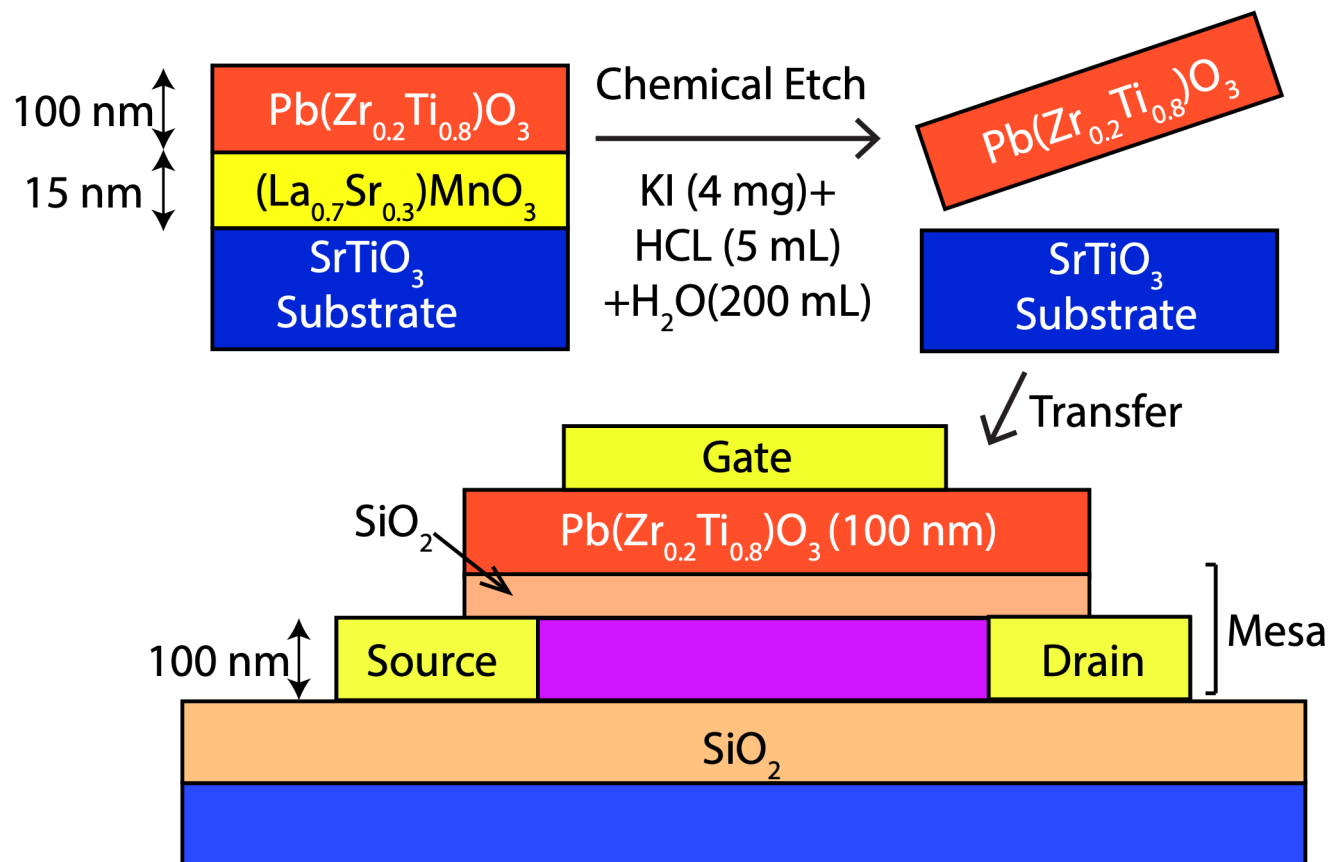
Comparison of hysteresis characteristics



Fully Integrated Ferroelectric FET

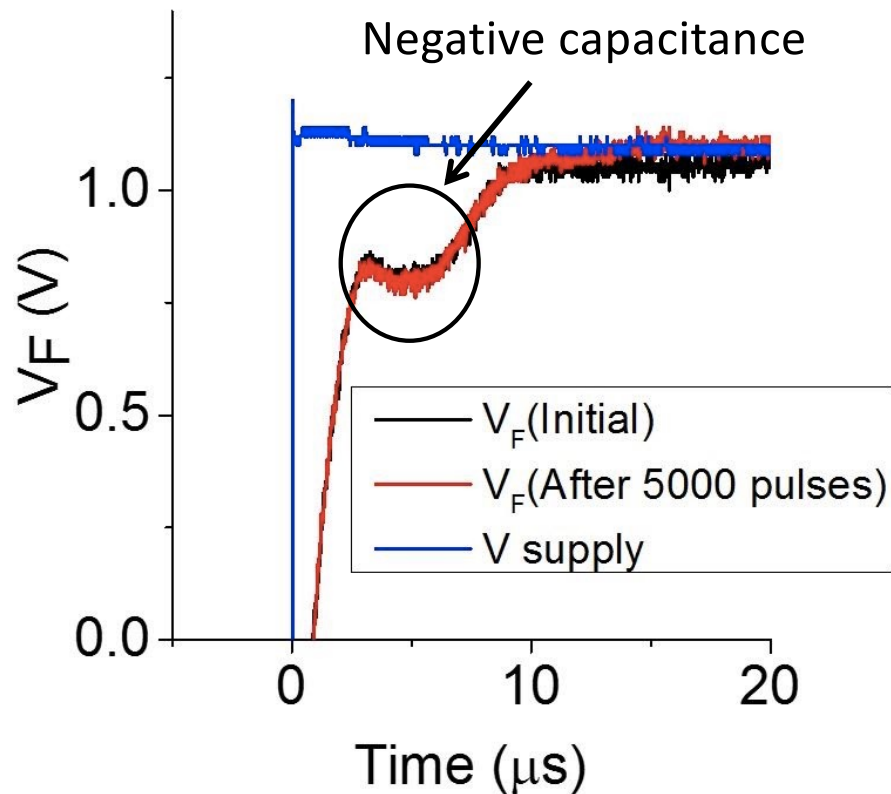
Transfer Technique of Epitaxial Ferroelectric

TEM Image



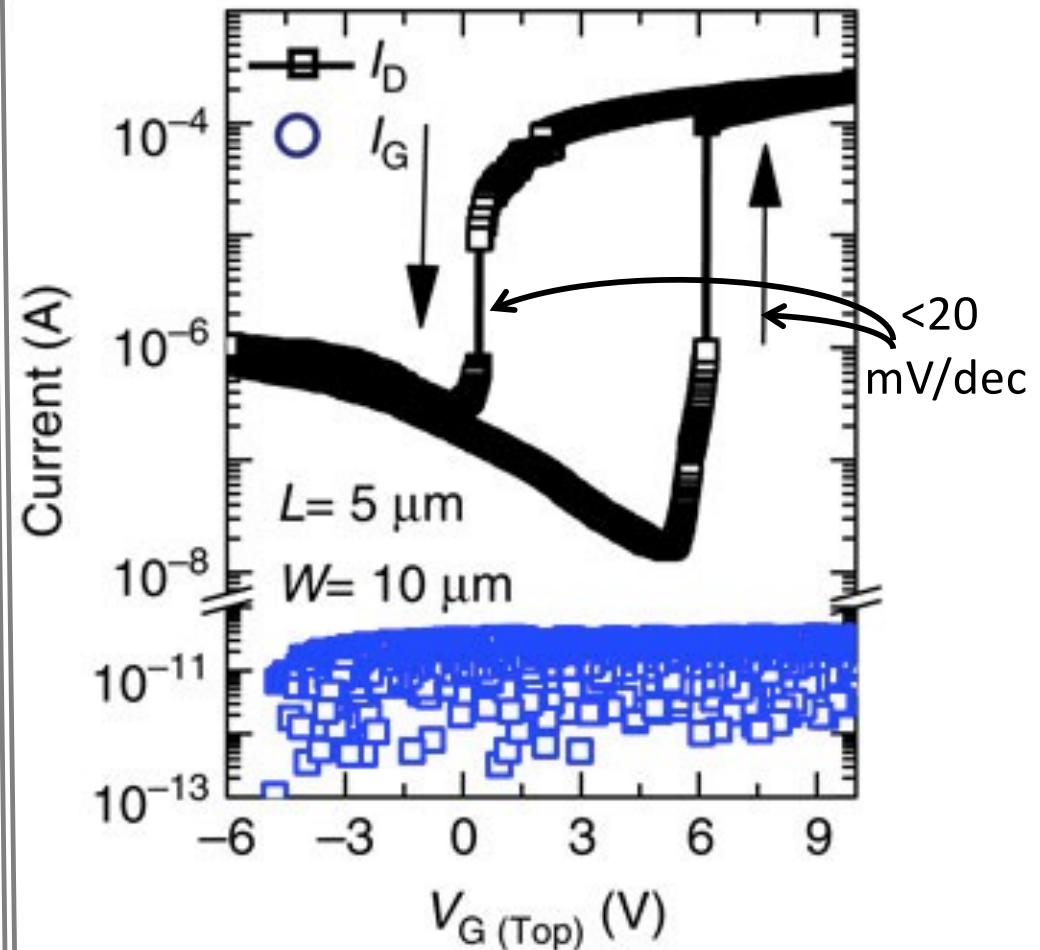
Fully Integrated Ferroelectric FET

Negative Capacitance Transients



Transferred ferroelectric films show negative capacitance transients.

Characteristics of FE-FET

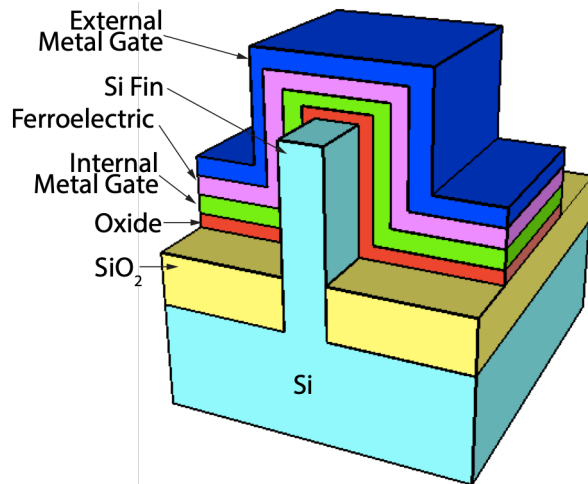


World's First Epitaxial Single Crystal Ferroelectric FET!

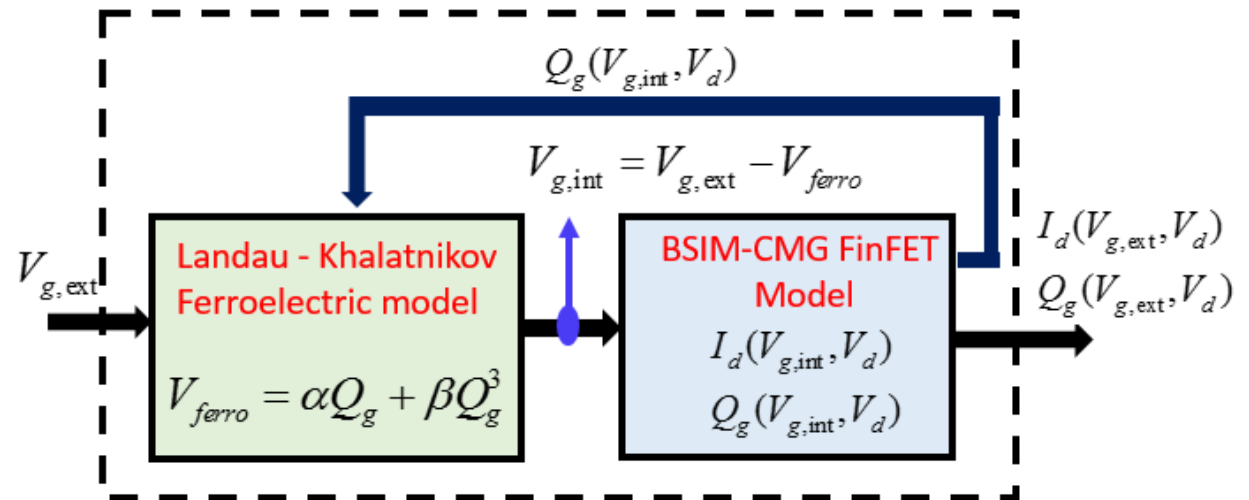
Bakaul, ..., Khan et al. Nature Comm. 7, 10540 (2016)

Performance Projection of NCFETs

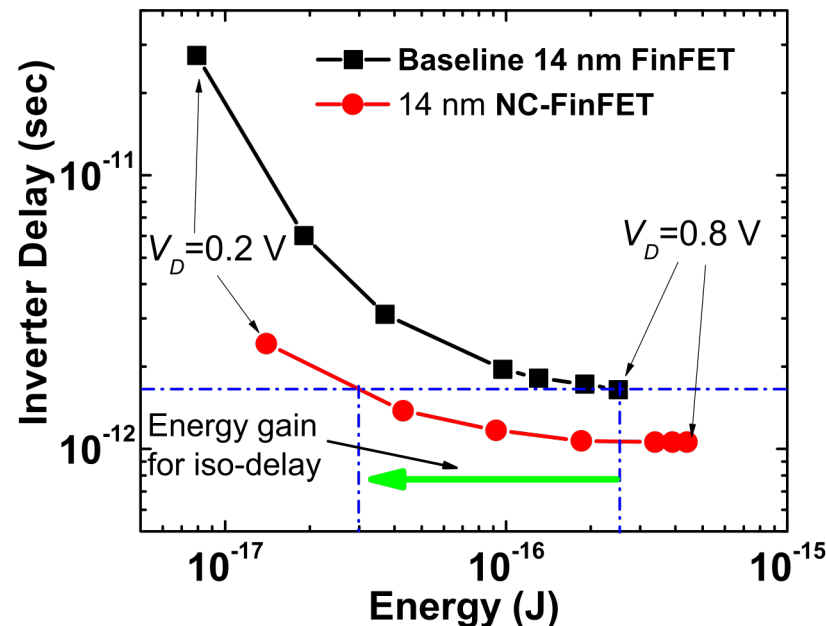
14 nm FinFET Node



Compact Model

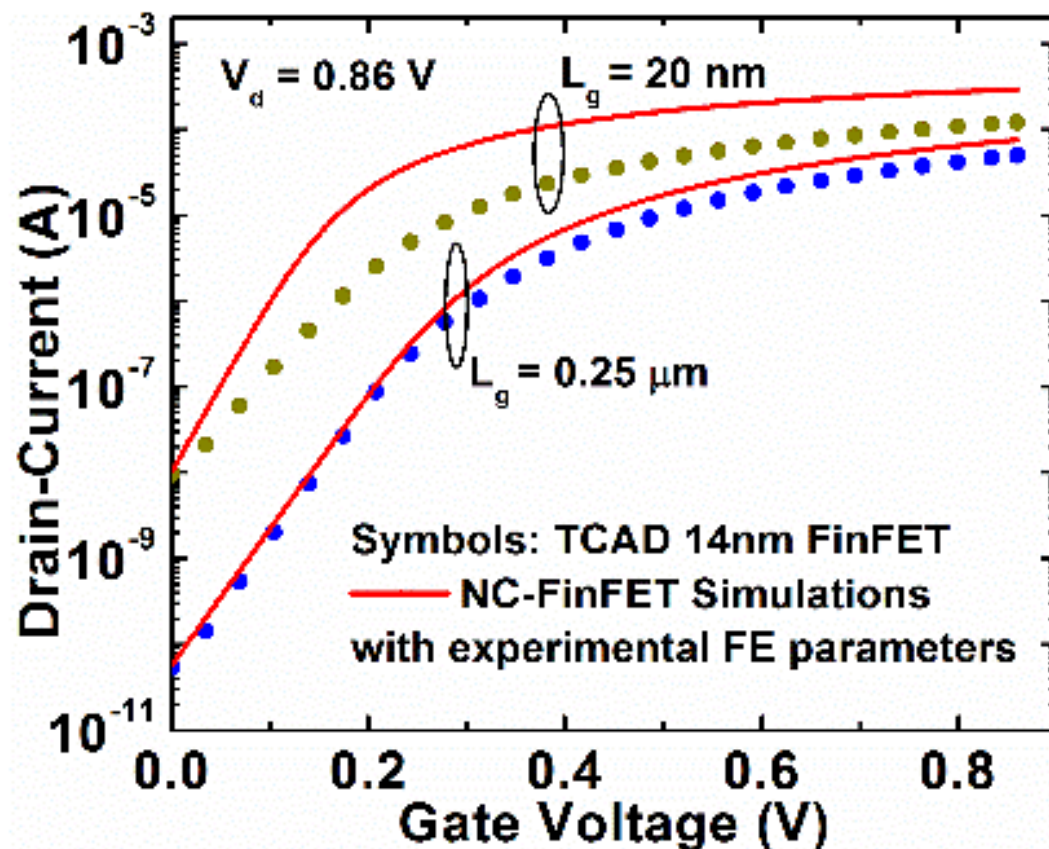
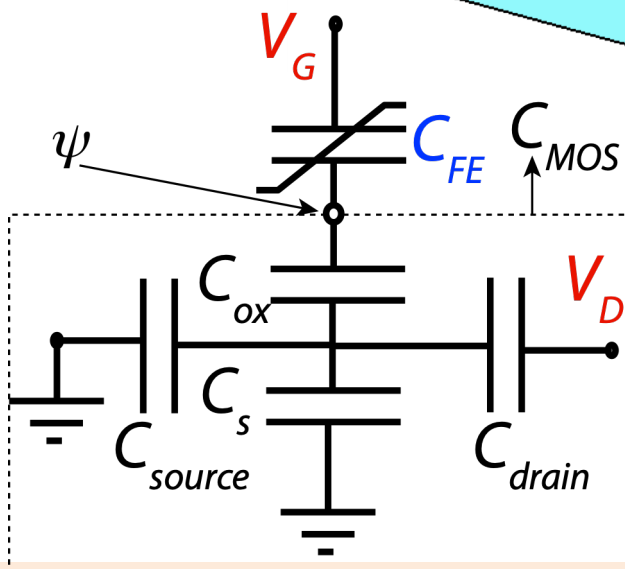
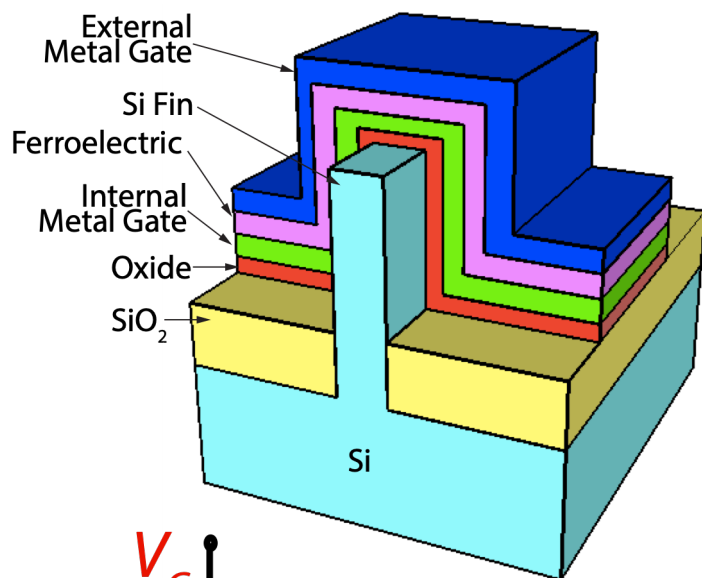


Energy-Delay Curve



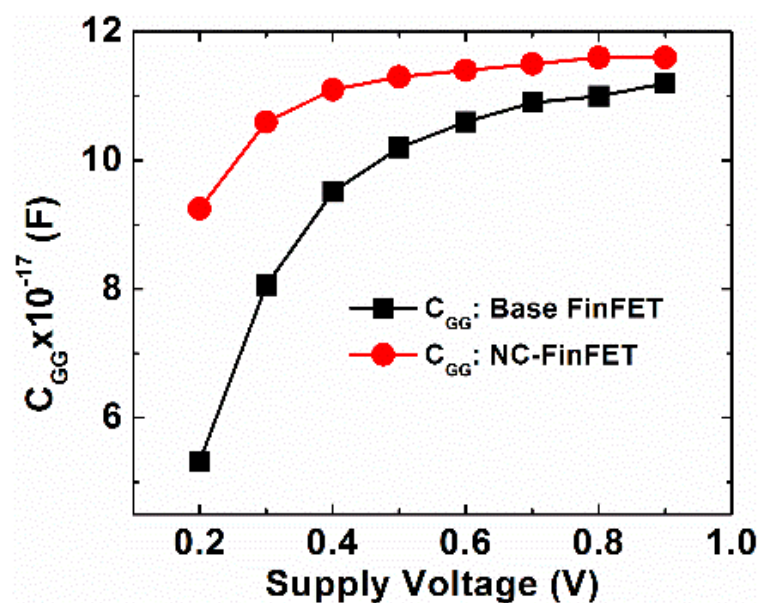
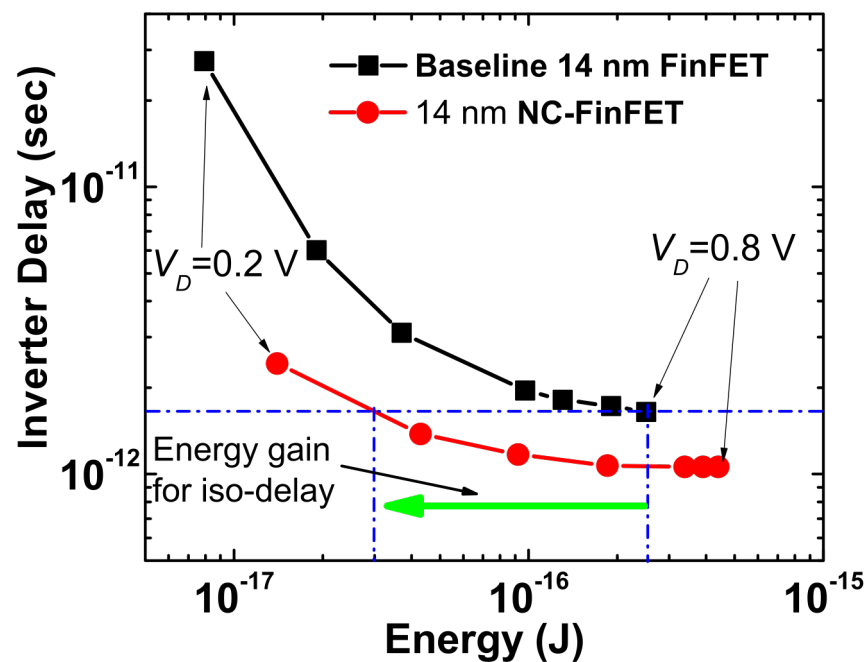
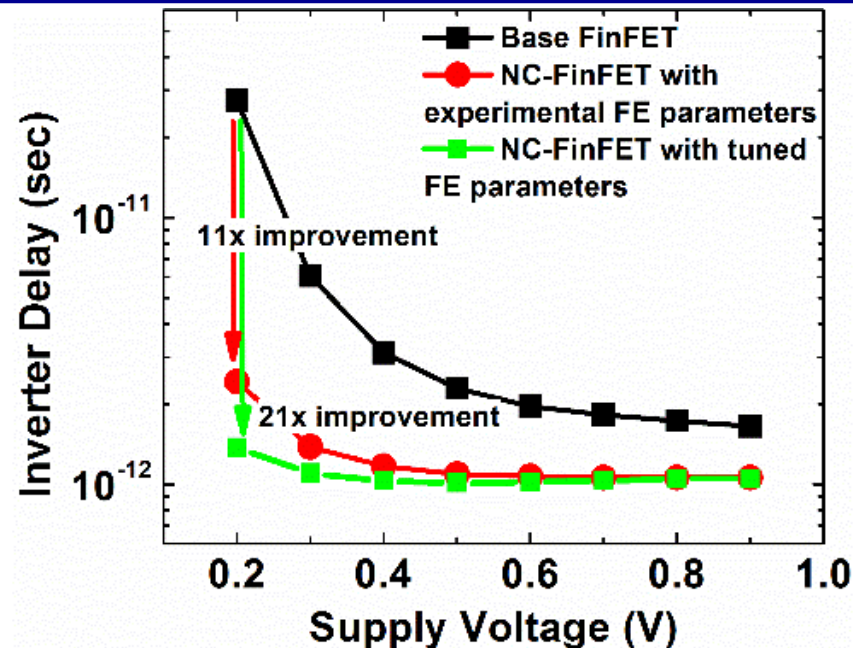
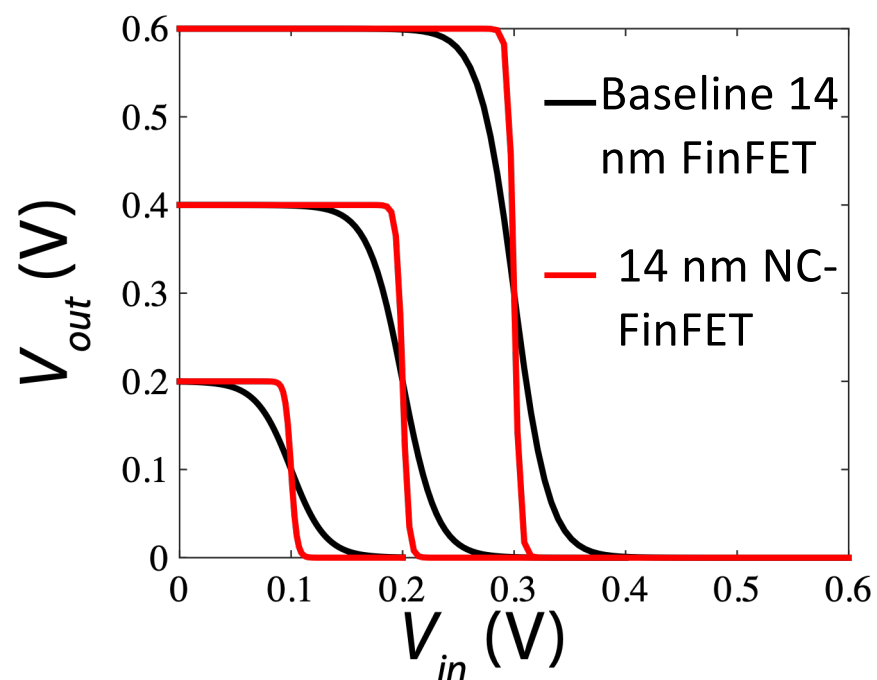
Significant reduction of energy-delay product is possible with negative capacitance

Short L_g Negative Capacitance Transistors

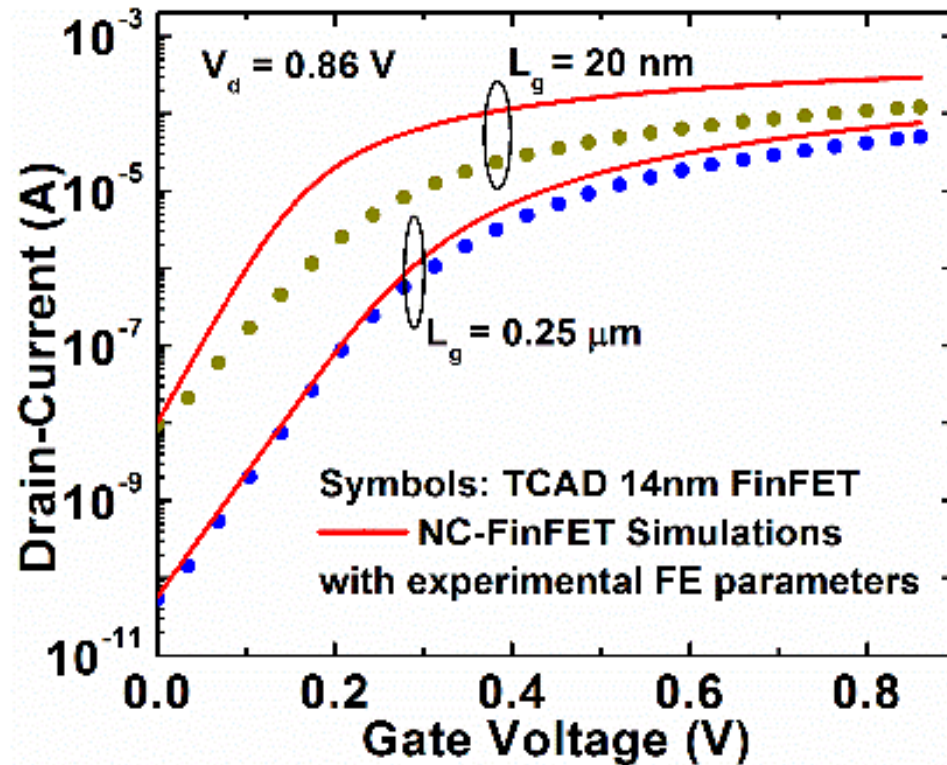


Improvement of Subthreshold Swing is higher in short channel transistors!

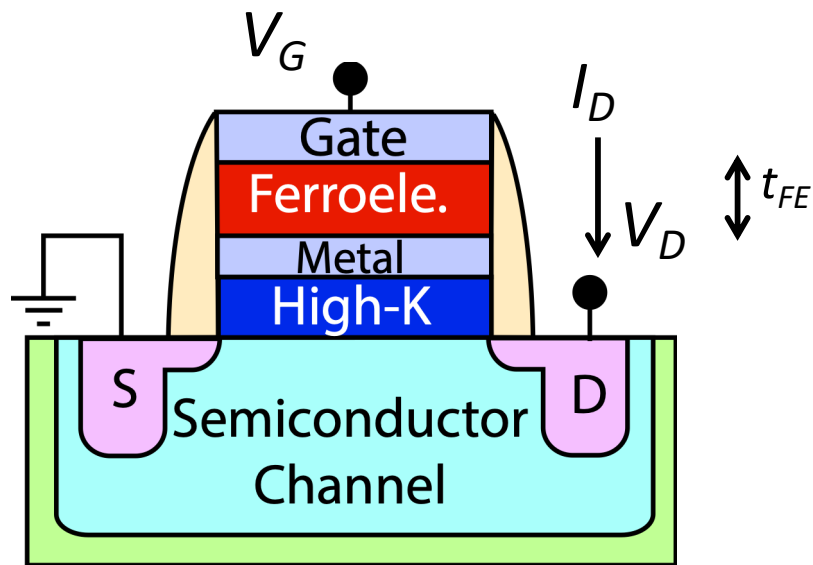
Performance Projection of NCFETs



Effects of Channel Length Scaling



Physics of Negative Capacitance FET

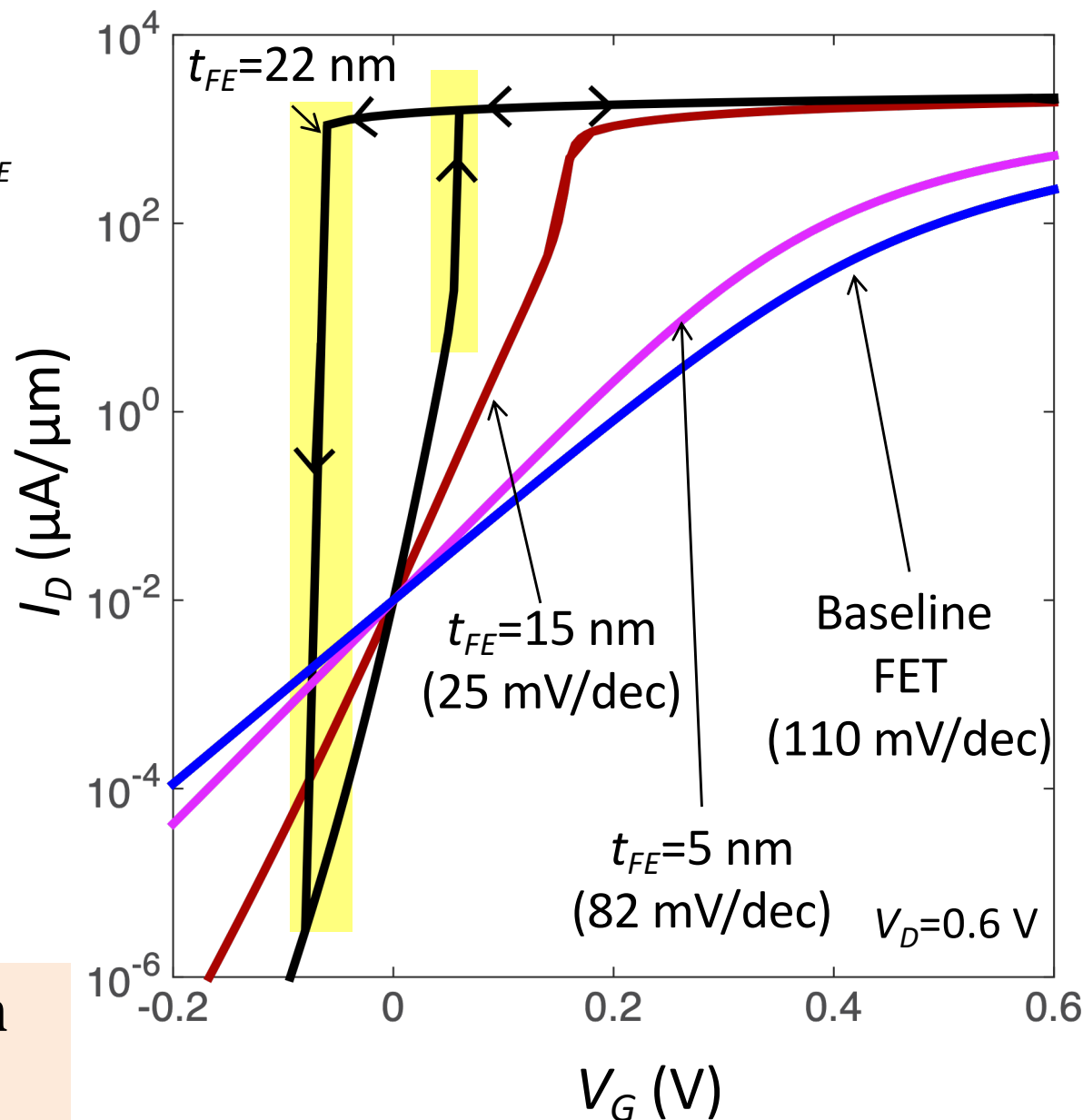


Ferroelectric Capacitance

$$|C_{FE}| \approx \frac{P_o}{E_{FE} t_{FE}}$$

Intel 45 nm node, MIT MVS Model
Collaboration with
Dimitri Antoniadis, MIT

Capacitance Matching can
lead to wide range of
behavior in NCFETs!

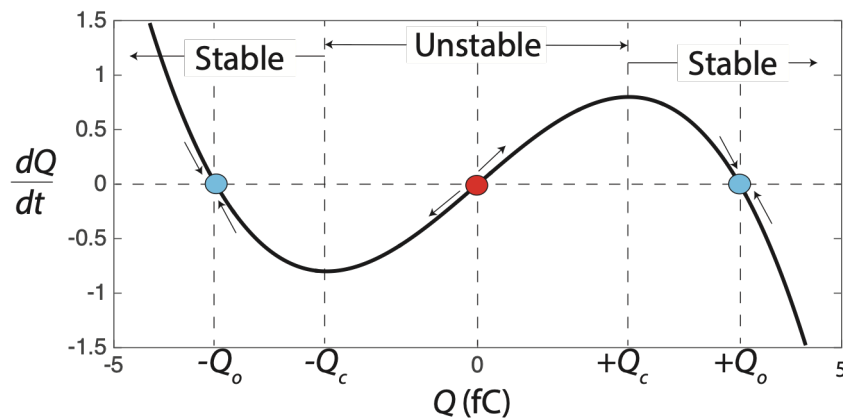
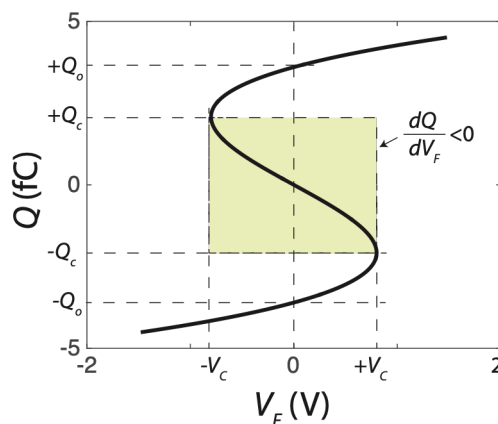
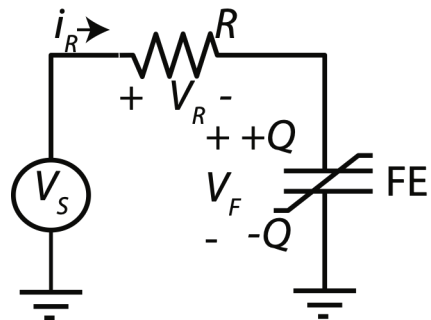


Stability Criteria

$$V_S = R \frac{dQ}{dt} + V_F(Q)$$

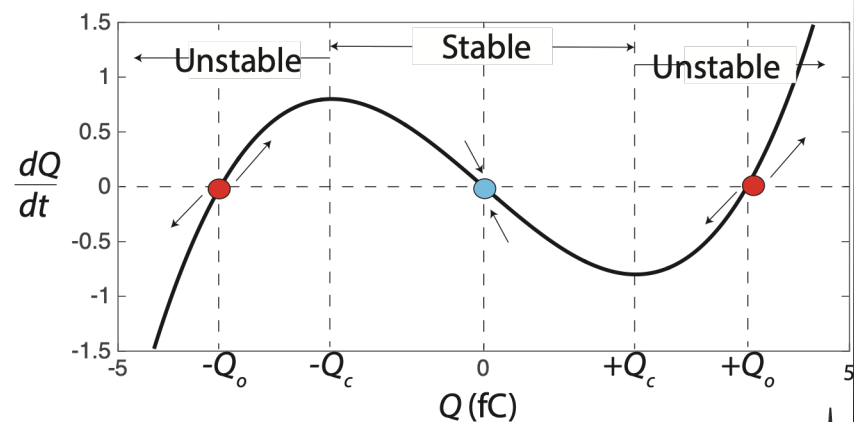
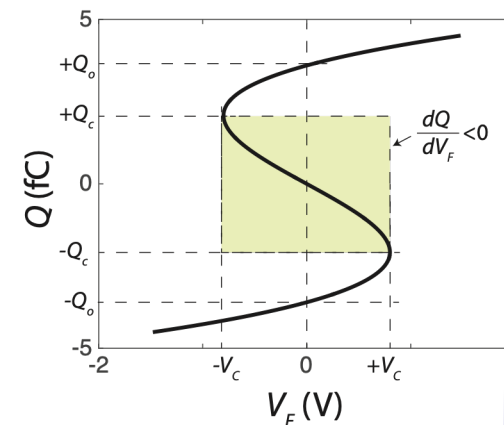
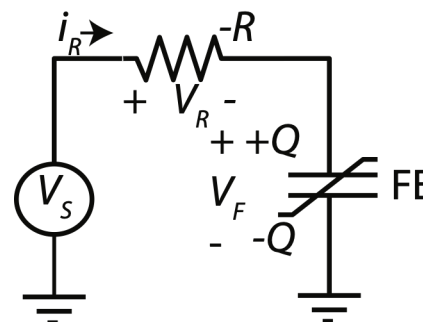
$$\frac{dQ}{dt} = \frac{1}{R}(V_S - (\alpha Q + \beta Q^3))$$

$$V_F = \alpha Q + \beta Q^3$$



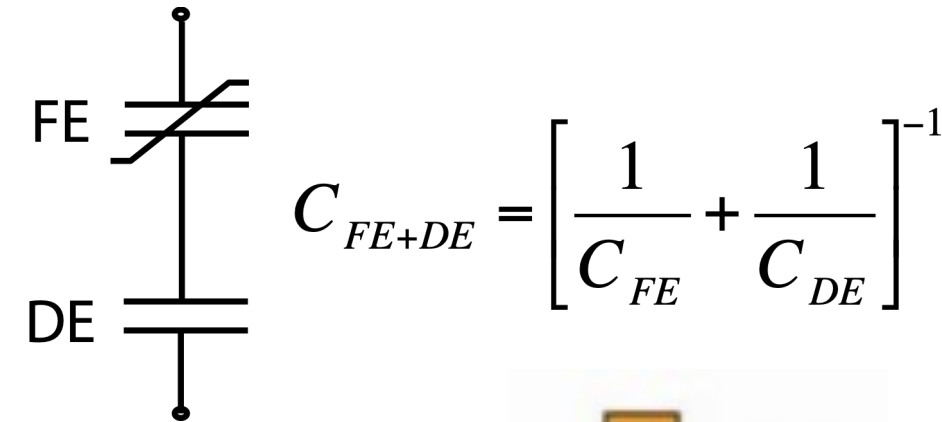
$$V_S = -R \frac{dQ}{dt} + V_F(Q)$$

$$\frac{dQ}{dt} = -\frac{1}{R}(V_S - (\alpha Q + \beta Q^3))$$



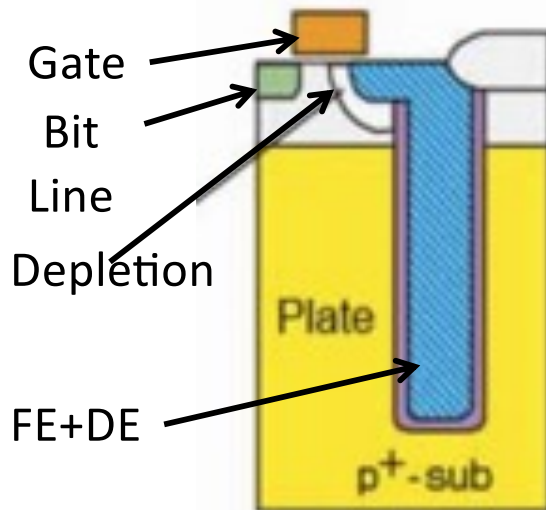
New Applications of Negative Capacitance

DRAM Technology with Negative Capacitance



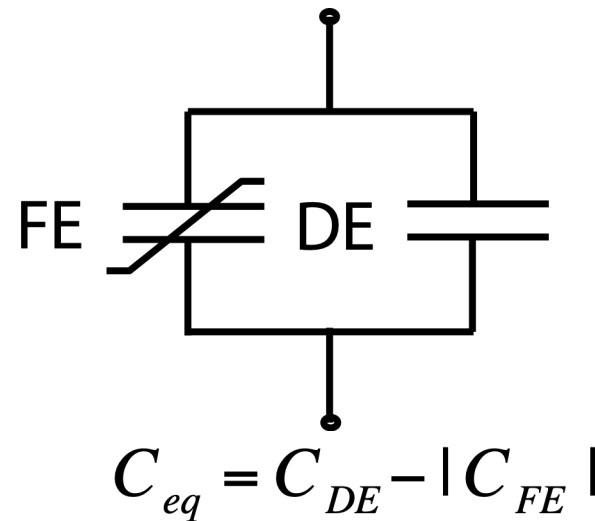
General structure of DRAM capacitor

Image source:
Kim et al. IEDM
2012



Capacitance Enhancement
can help DRAM Scaling

Interconnect Technology with Negative Capacitance

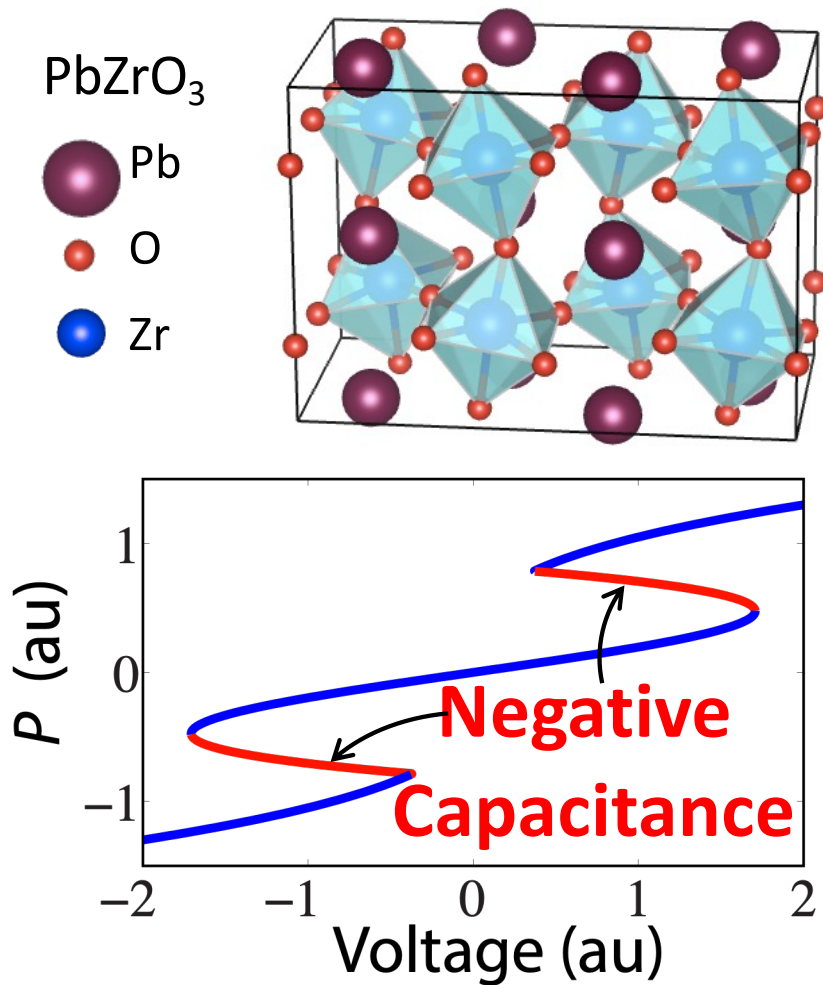


Negative capacitance reduce the capacitance in a parallel combination

Capacitance reduction
techniques can lead to better
interconnects

Antiferroelectric Negative Capacitance

Antiferroelectrics



Negative capacitance regions marked by **red curves**.

Material Systems

- Complex Oxide Antiferroelectrics
 - $\text{Pb}(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$ and doped variants
 - BeFeO_3 and doped variantsGrowth: Pulsed laser deposition
- CMOS Compatible $\text{Hf}_x\text{Zr}_{1-x}\text{O}_2$
Growth: Atomic layer deposition, sputtering, pulsed laser deposition

Why Antiferroelectricity?

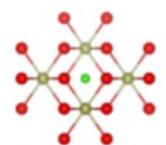
- Better charge matching of the negative capacitance region with that of MOSFET
- Possibility of Intrinsic Switching

Antiferroelectrics may show stronger negative capacitance effects!

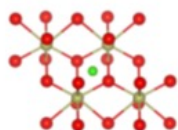
Origin of Ferroelectricity in HfO₂

**Tetragonal to Cubic
@ 2800 K**

NON-POLAR

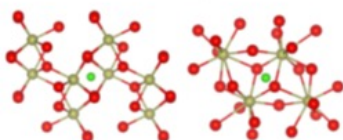


Cubic (Fm $\bar{3}$ m)

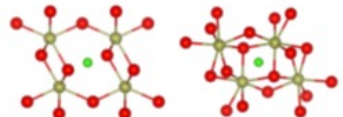


Tetragonal (P4₂/nmc)

**Monoclinic to Tetragonal
@ 2000 K**

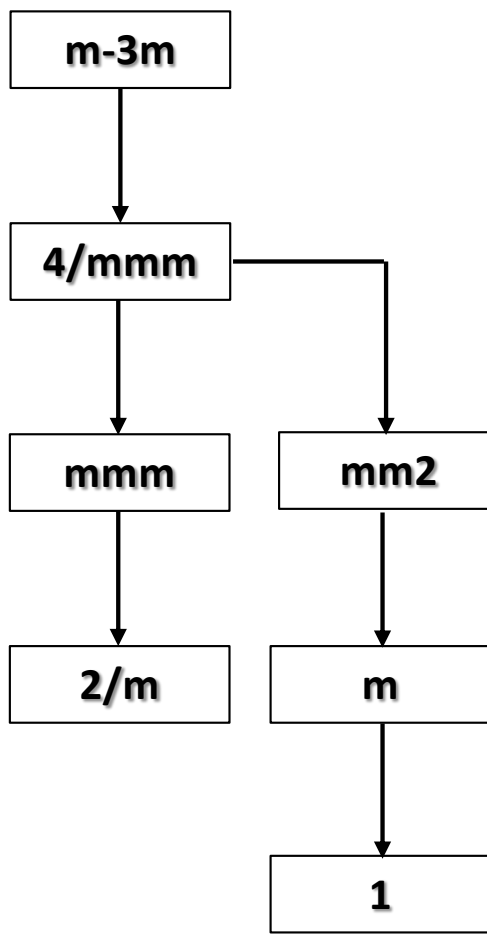


Orthorhombic (Pcba) Orthorhombic (Pnma)



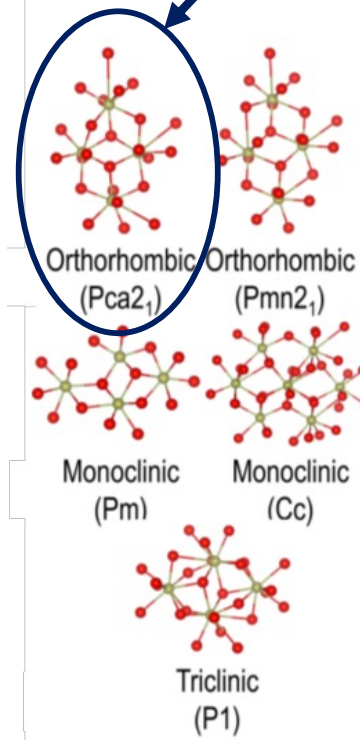
Monoclinic (P2₁/m) Monoclinic (P2₁/c)

Point Group



POLAR

Polycrystalline ferroelectric films belong to orthorhombic space group Pca2₁



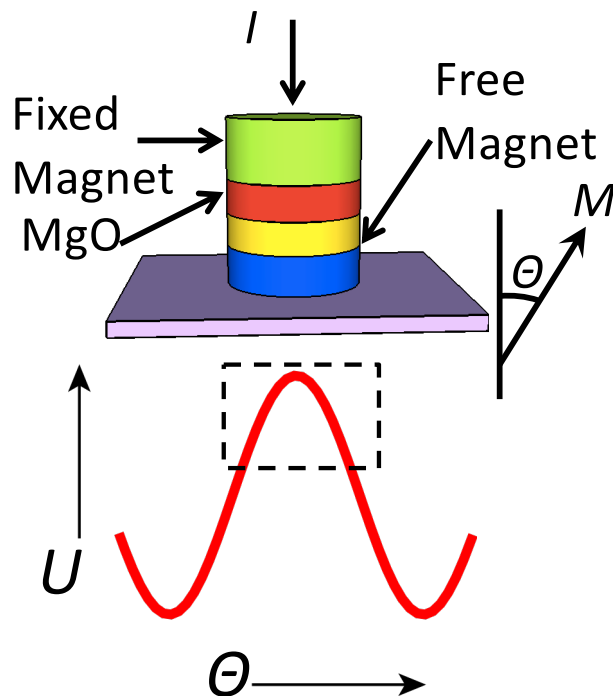
What is there for Physics and Beyond

Static Negative Dielectric Constant is important in the context of Electromagnetism!

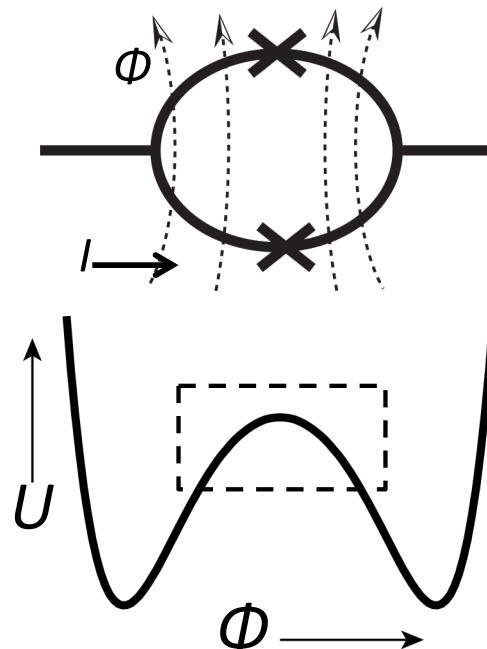
Landau, Lifshitz. Electrodynamics of Continuous Media (Pergamon, NY) 1960.
Pines. The Theory of Quantum Liquids (Benjamin, New York), 1960.

Martin. Phys. Rev. 161, 143 (1967).
Kirkhnits. Sov. Phys. -Usp. 19, 530 (1976).
Dolgov et al. Rev. Mod. Phys. 53, 81 (1981).

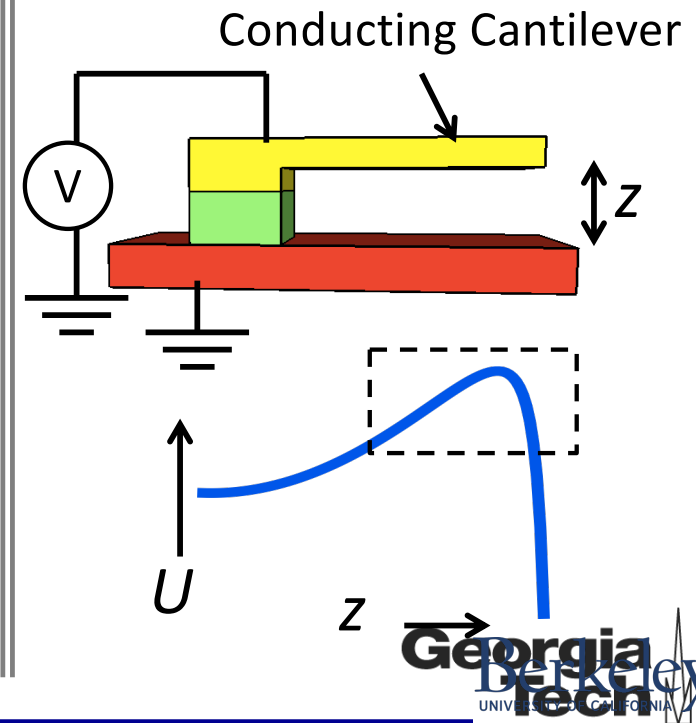
Spintronic Devices



Superconducting Devices

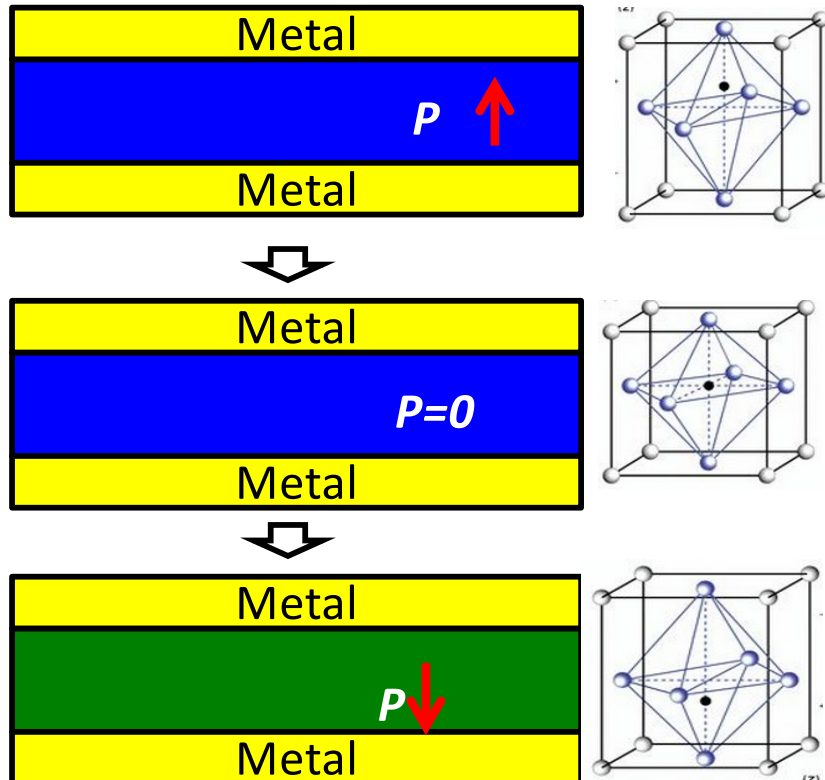


MEMS Devices

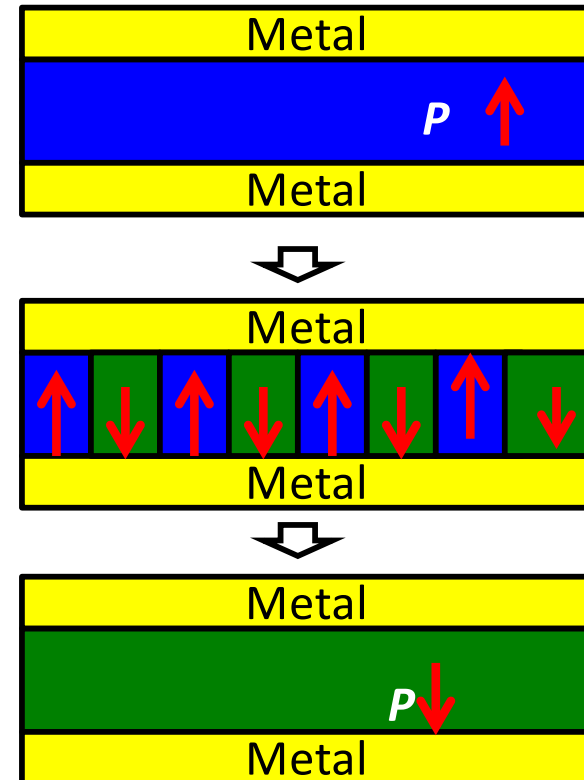


Effects of Domain Mechanisms

Landau-Khalatnikov Dynamics

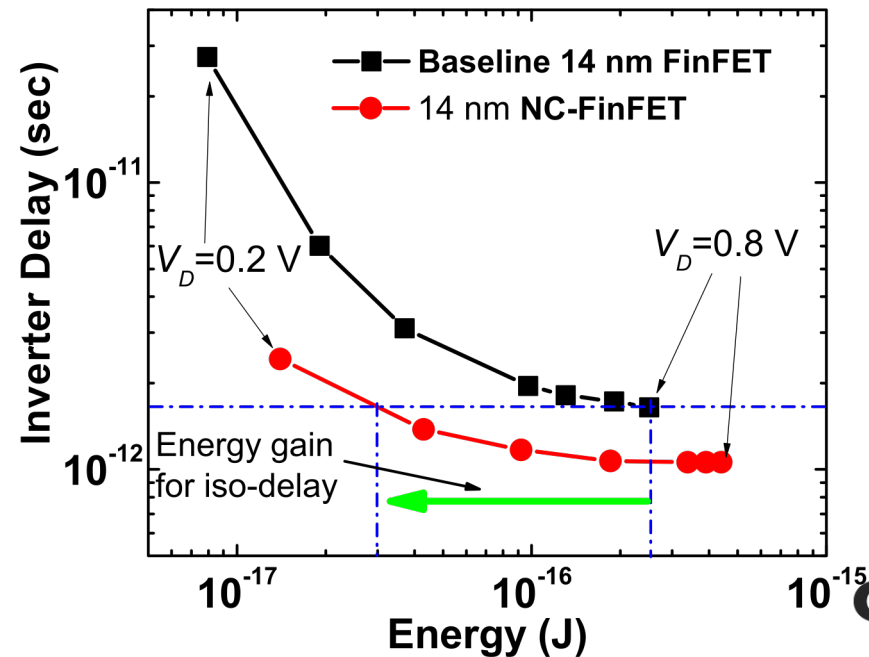
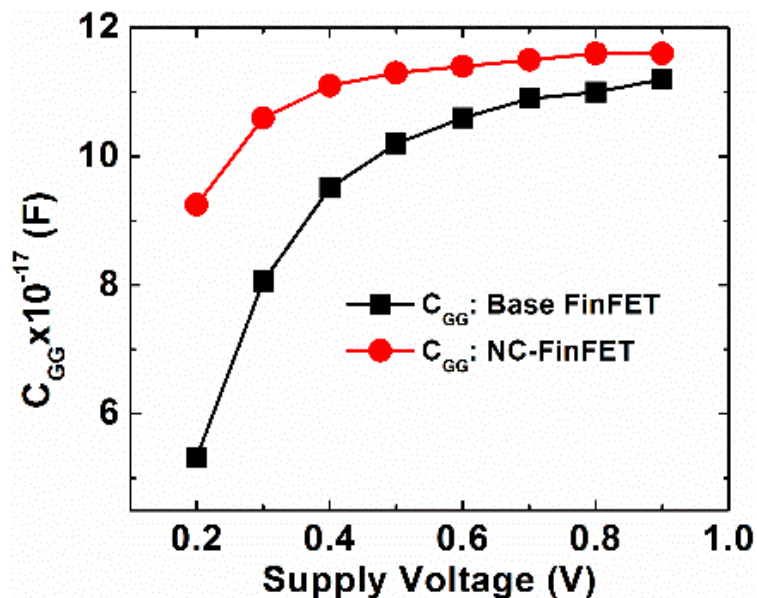
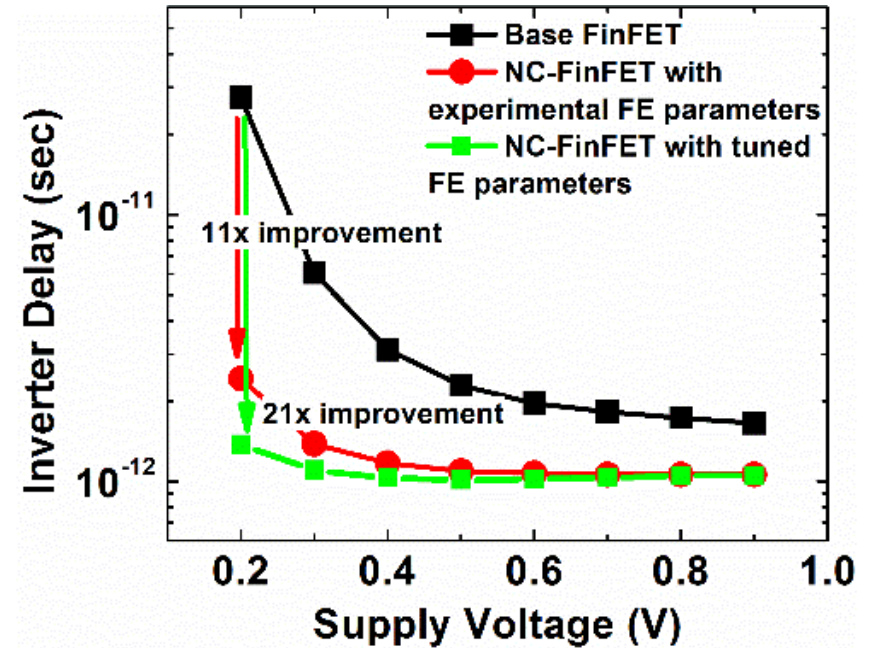
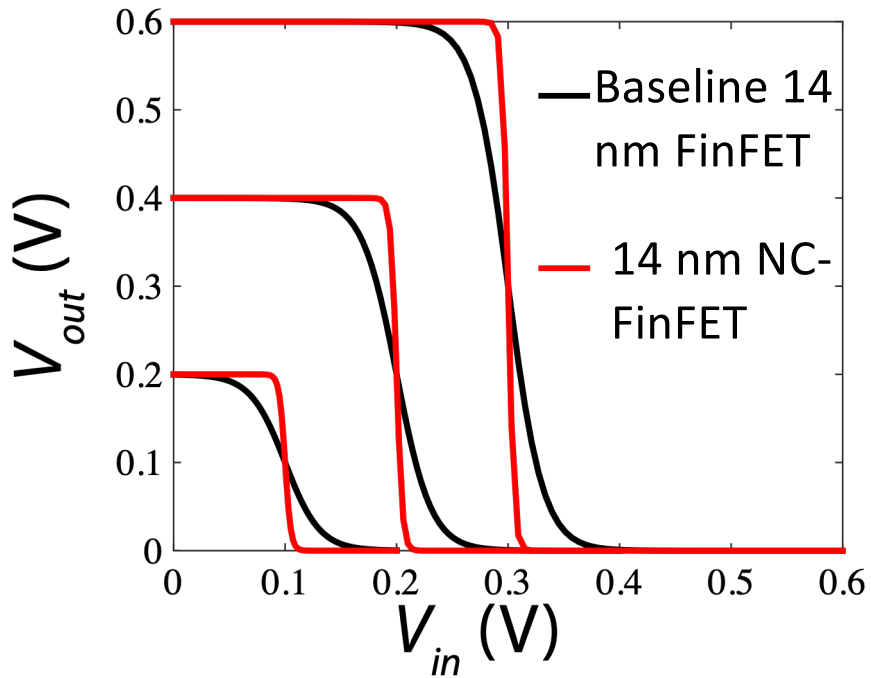


Domain Mediated Dynamics



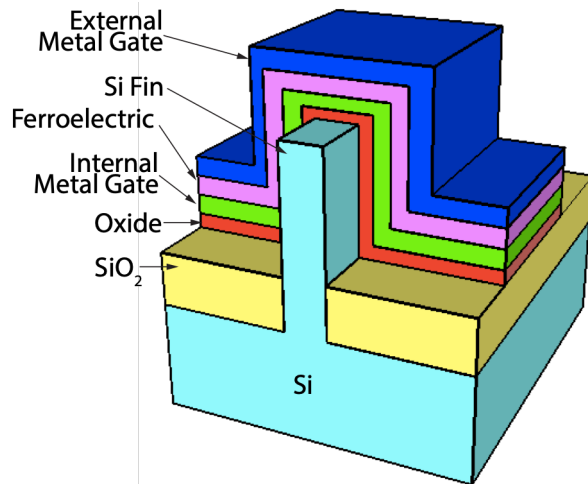
The Negative Capacitance effect is present even though the ferroelectric does not switch in a single domain fashion.

Performance Projection of NCFETs

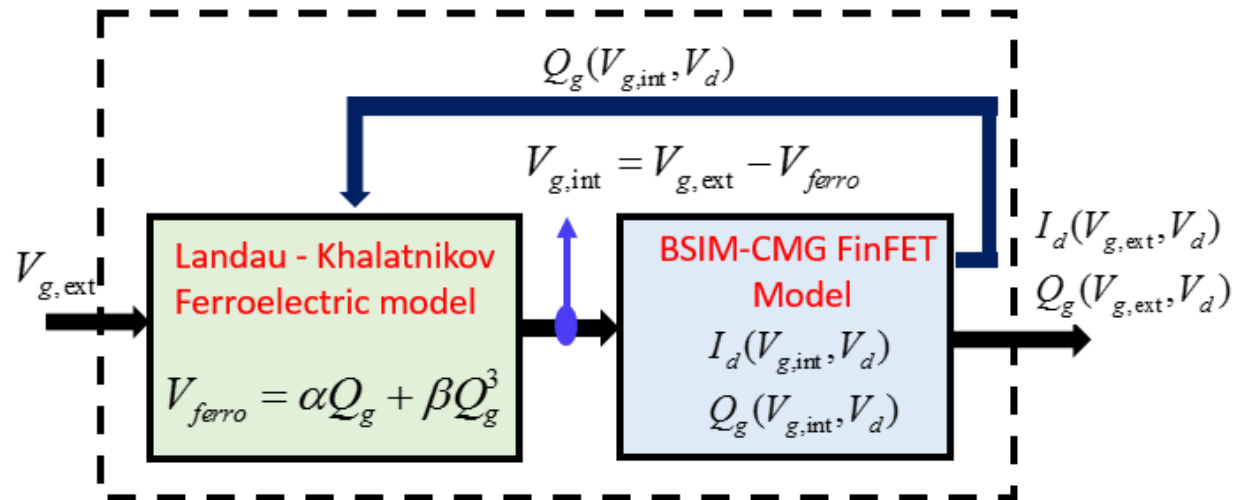


Performance Projection of NCFETs

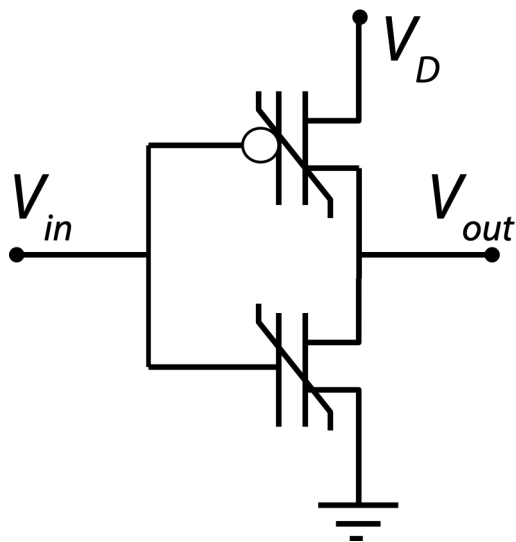
14 nm FinFET Node



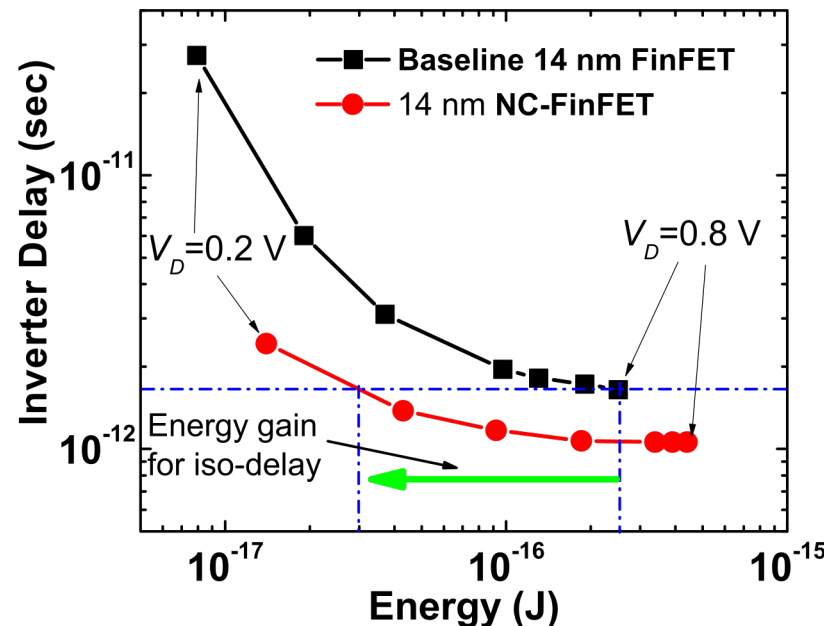
Compact Model



Inverter

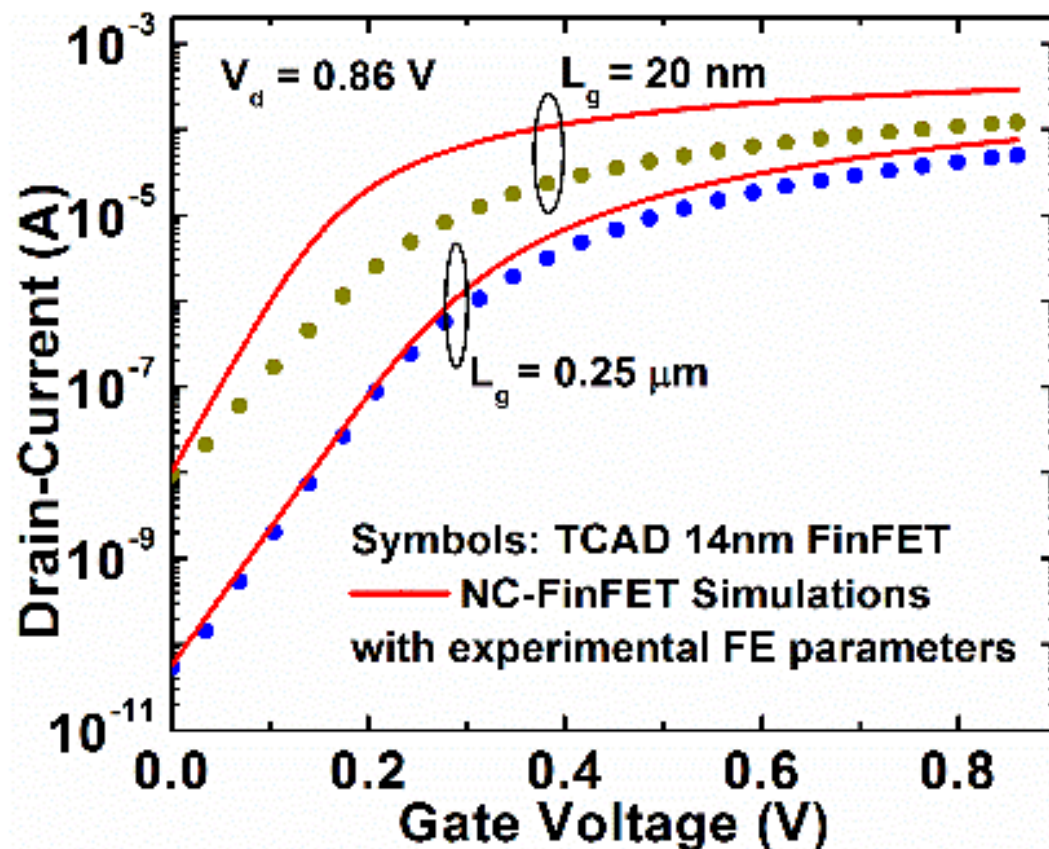
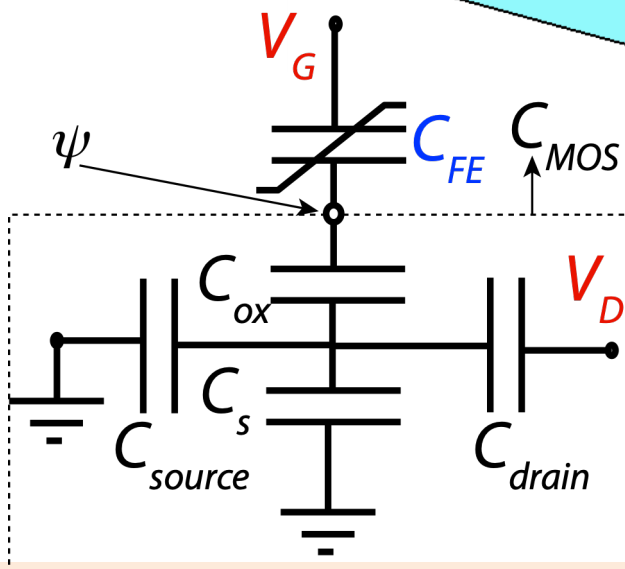
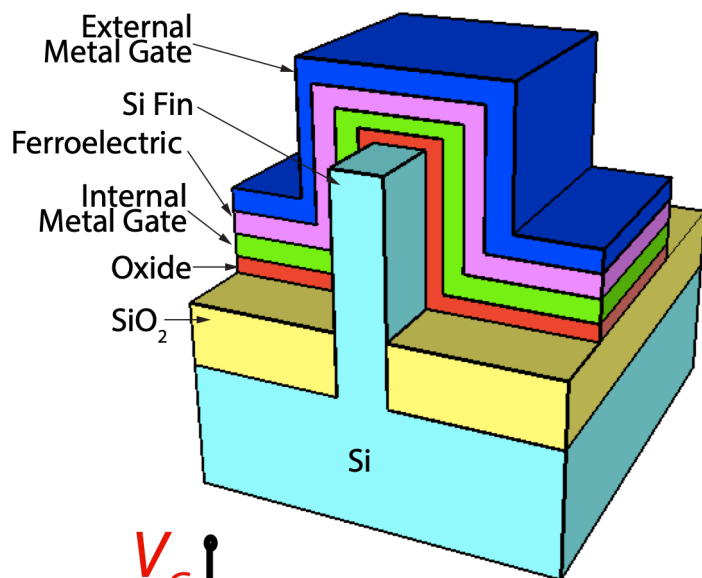


Energy-Delay Curve



Significant reduction of energy-delay product is possible with negative capacitance

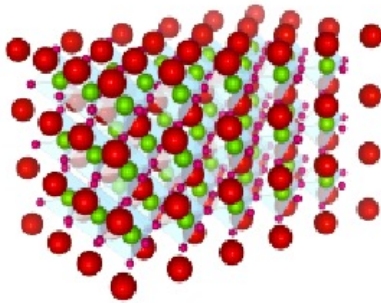
Short L_g Negative Capacitance Transistors



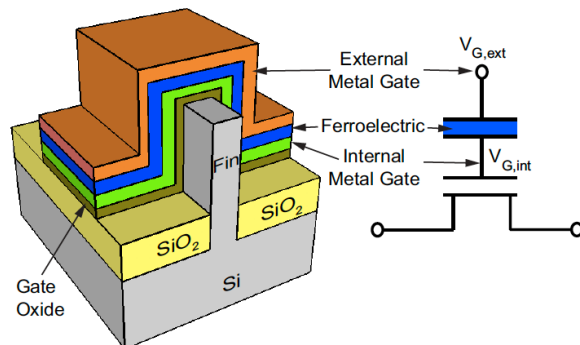
Improvement of Subthreshold Swing is higher in short channel transistors!

What am I going to do today?

Physics of Negative Capacitance in Materials



Physics of Negative Capacitance Transistors

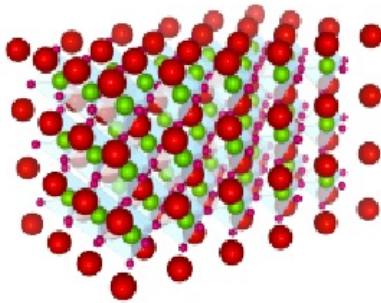


What do we need to know?

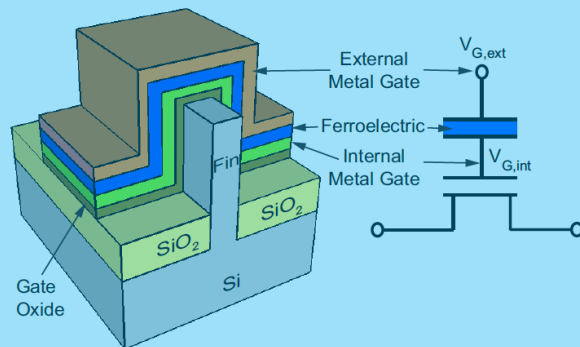


What am I going to do today?

Physics of Negative Capacitance in Materials



Physics of Negative Capacitance Transistors

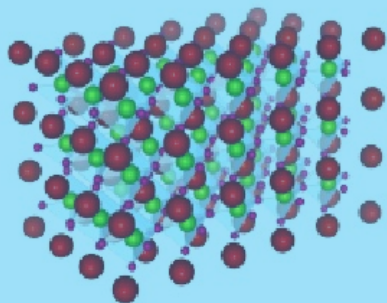


What do we need to know?

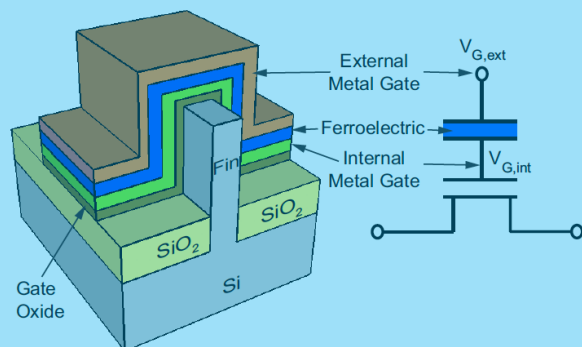


What am I going to do today?

Physics of Negative Capacitance in Materials



Physics of Negative Capacitance Transistors

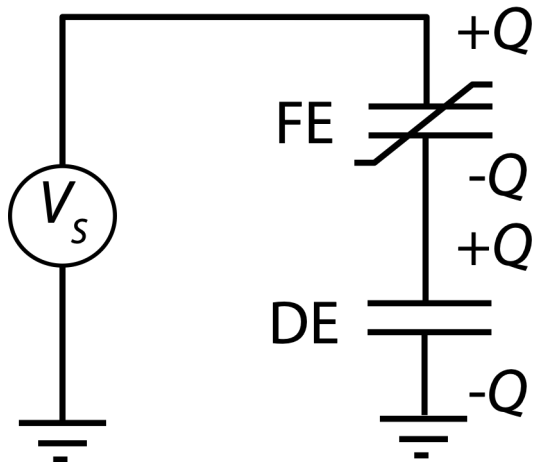


What do we need to know?



Breaking the Circuit Laws using Neg. Cap.

Ferroelectric-Dielectric
Series Combination



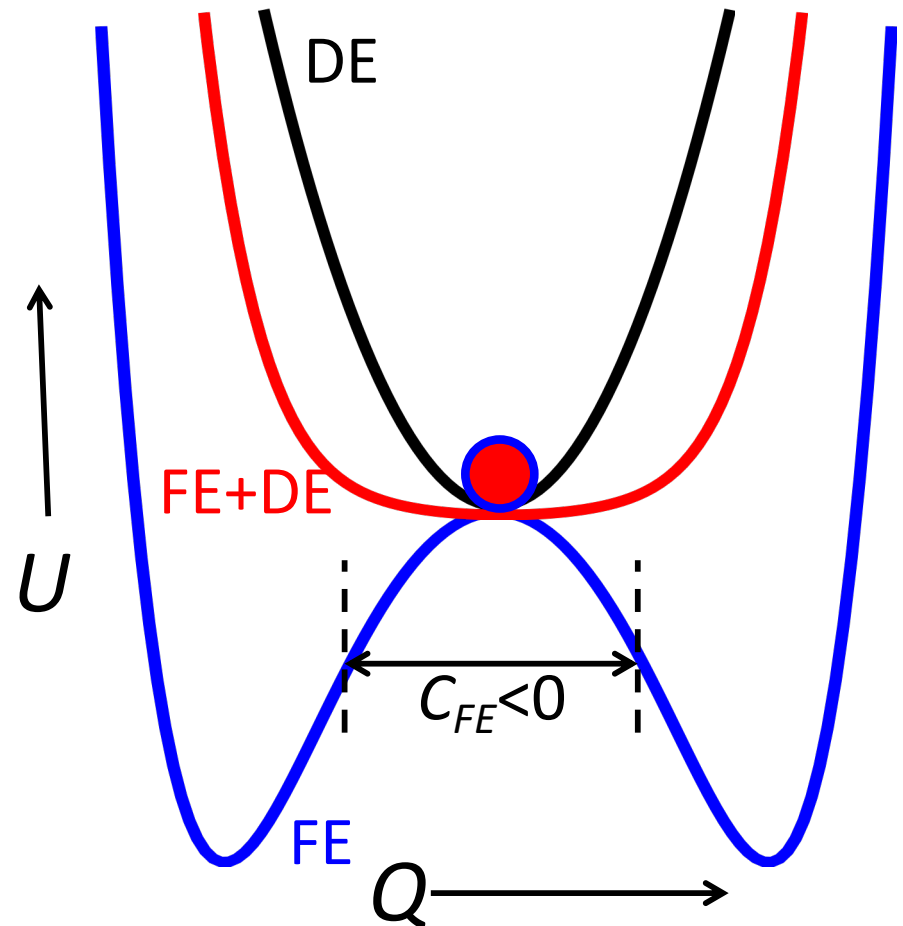
$$C = \left[\frac{d^2 U}{dQ^2} \right]^{-1}$$

Equivalent Capacitance

$$C_{FE+DE} = \left[\frac{1}{C_{FE}} + \frac{1}{C_{DE}} \right]^{-1}$$

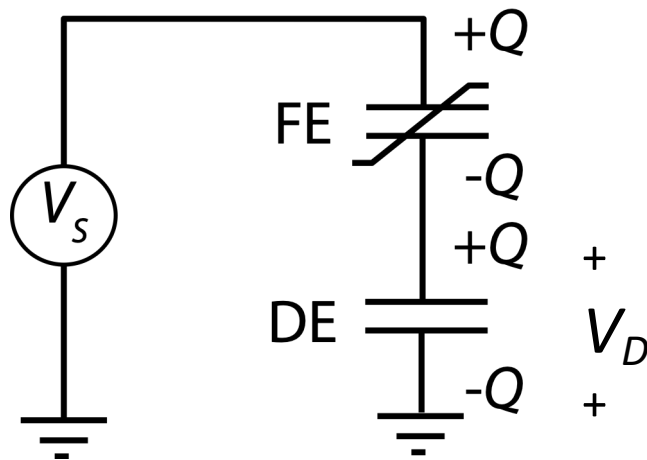
$$C_{FE+DE} > C_{DE} \quad \text{if} \quad C_{FE} < 0$$

Energy Landscapes



Enhancement of capacitance in a FE-DE Series combination!

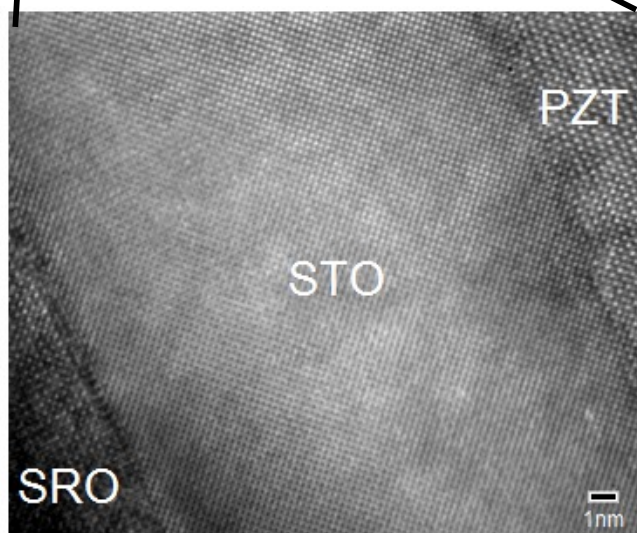
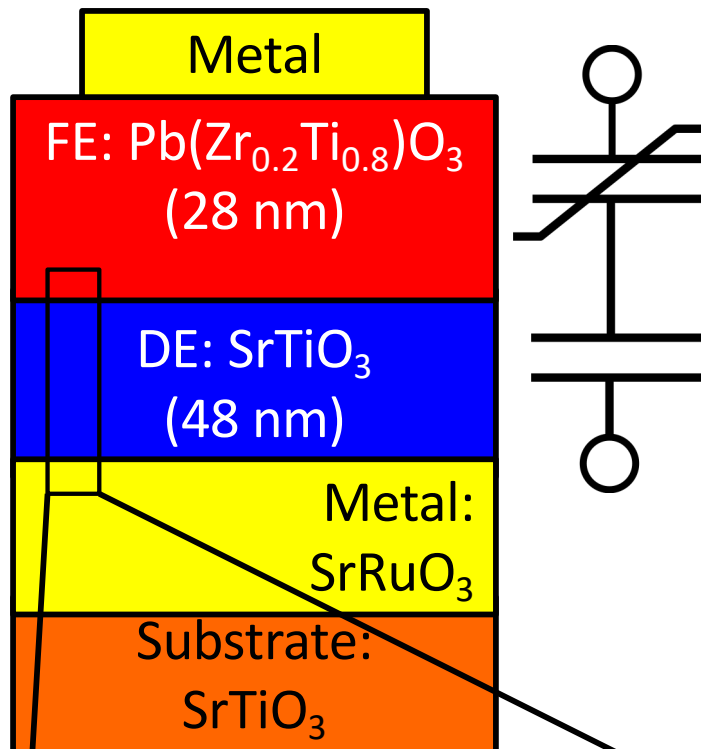
Ferroelectric+Dielectric Capacitor



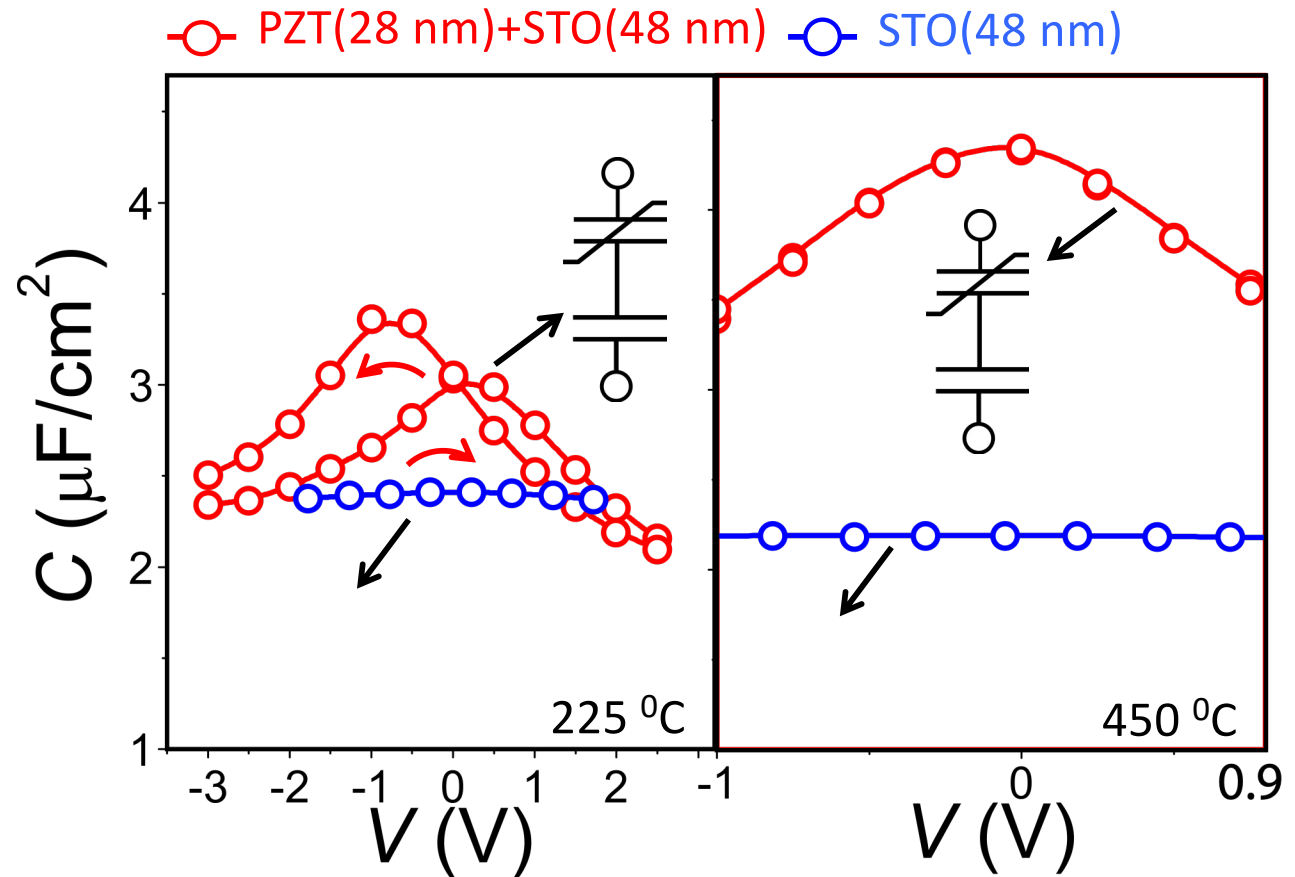
Voltage amplification: $\frac{dV_D}{dV_S} = \frac{C_F}{C_F + C_D} = \frac{|C_F|}{|C_F| - C_D}$ If $|C_F| > C_D$, $\frac{dV_D}{dV_S} > 1$

Equivalent Capacitance $C_{F+D} = \left[\frac{1}{C_D} + \frac{1}{C_F} \right]^{-1}$ If $|C_F| > C_D$, $C_{D+F} > C_D$

Breaking the Circuit Laws using Neg. Cap.



C-V Characteristics



Capacitance Enhancement in a FE-DE heterostructure experimentally observed!

Khan et al. Appl. Phys. Lett. 100, 113501 (2011)

Gao, Khan et al. Nano Lett. 14, 5814 (2014)

Appleby et al. Nano Lett. (2014)